

OFDM Demodulator LSI for Terrestrial Digital Broadcasting

MB86A21

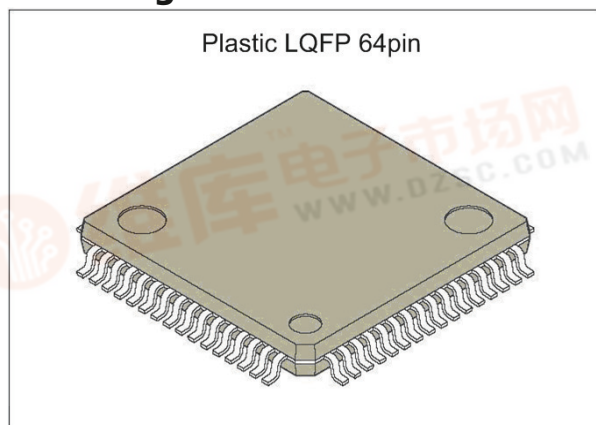
1. Overview

- MB86A21 is OFDM demodulation LSI compliant with Japan's terrestrial digital broadcasting (ISDB-T)
- MB86A21 is OFDM demodulation LSI compliant with receiving 1-segment and 3-segments of Japan's digital terrestrial sound broadcasting. (ISDB-Tsb).
- A/D, 8K-FFT, deinterleaver (frequency, time, byte, bit), error correction (Viterbi, Reed Solomon), Multi frame process TMCC decoder with the necessary memory implemented in one chip.

2. Feature

- Supports Japan's terrestrial digital broadcasting. (ISDB-T)
- Supports 1-segment / 3-segment reception of digital terrestrial sound broadcasting. (ISDB-Tsb)
- Supports Zero-IF/4MHz/44MHz/57MHz IF signal input
(± 3 MHz band limitation from the center frequency)
- Carrier capture ± 250 KHz
- Auto-detect of transmission mode/ guard interval
- Incorporates TMCC decode circuit
- Incorporates de-interleave memory
- Incorporates C/N Monitor, MER Monitor, BER Monitor
- Incorporates FFT window adaptation control
- Incorporates high-speed channel search function
- Incorporates spurious removal function
- Incorporates I²C bus interface
- Incorporates I²C bus for tuner control
- Incorporates standby mode
- Power supply voltage : 1.2V (internal circuit, analog circuit), 3.3V (I/O), 3.3V (analog circuit)
- Package : LQFP 64pin (Lead pitch=0.5mm)

3. Package



4. Package Dimension

