

Precise Low Dropout Voltage Regulator

● Features

- Output Voltage selectable in 100mV steps (1.2V to 5.0V)
- Highly Accurate
- Low Dropout Voltage
- Low Power Consumption
- Internal current limiting and short protecting
- Small packages

● General Description

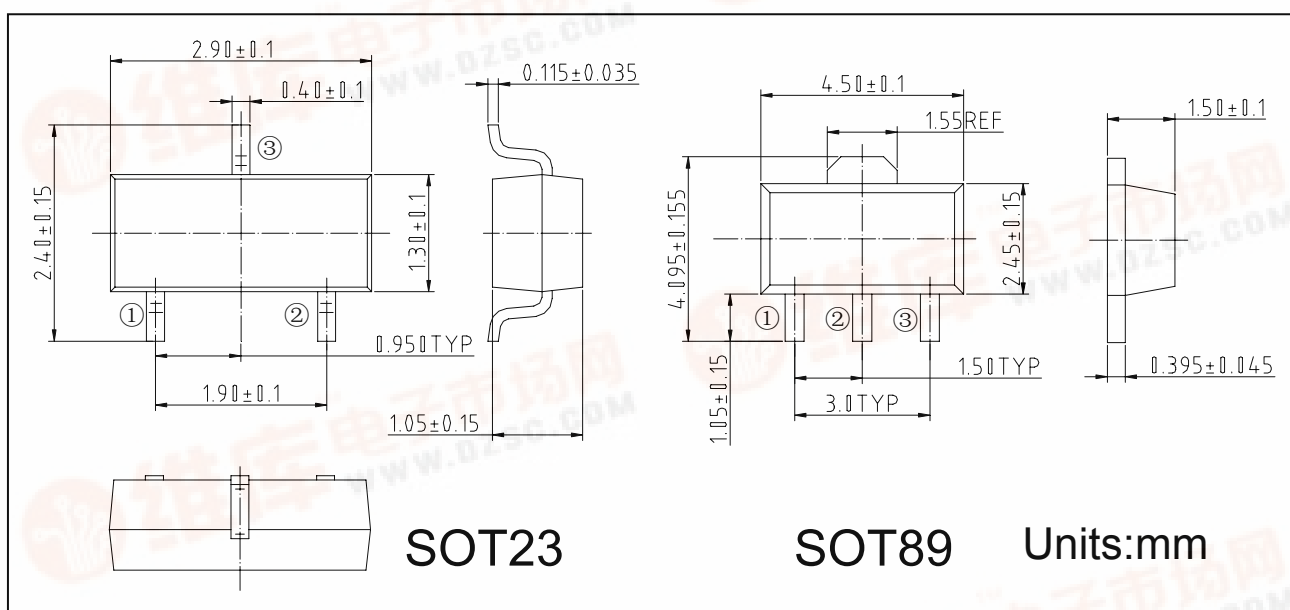
The ZYG6206 series are precise, low power consumption, positive voltage regulators manufactured using CMOS and laser trimming technologies. The series provides large currents with a significantly small dropout voltage. The ZYG6206 consists of a current limit circuit, a driver transistor,

a precision reference voltage and an error correction circuit. The series is compatible with low ESR ceramic capacitors. The current limiter's foldback circuit also operates as a short protect for the output current limiter and the output pin. Output voltage can be set internally by laser trimming technologies. It is selectable in 100mV increments within a range of 1.2V to 5.0V. SOT89 and SOT23 packages are available.

● Applications

- Battery powered equipment
- Reference voltage sources
- Cameras, Video cameras
- Mobile phones
- Communication tools

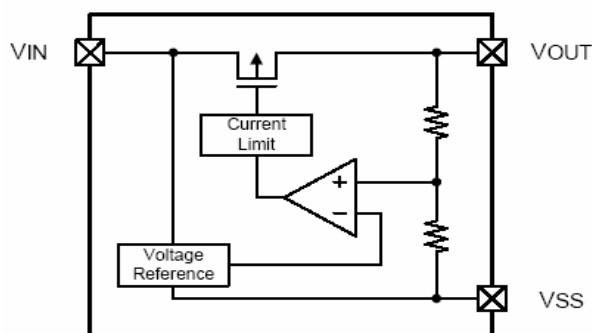
● Package Information



● Pin Configurations

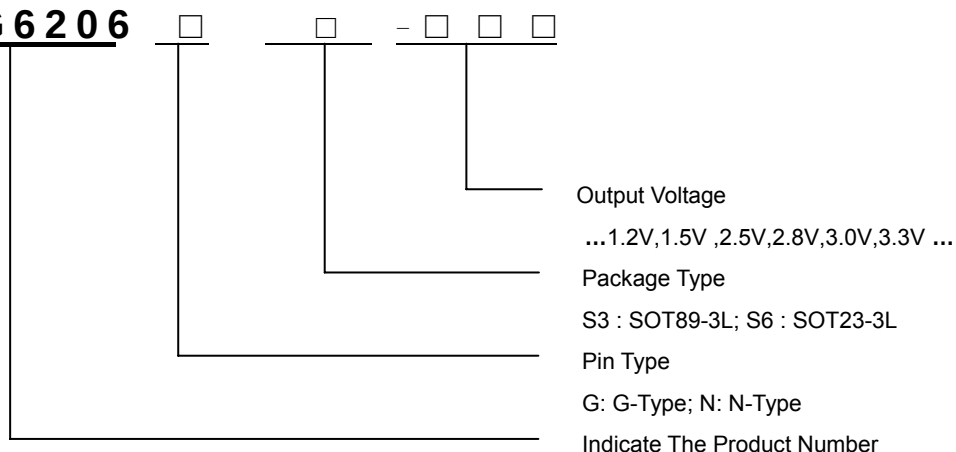
Pin Port	SOT23	SOT89(G-Type)	SOT89(N-Type)
①	VSS	VOUT	VSS
②	VOUT	VSS	VIN
③	VIN	VIN	VOUT

● Functional Block Diagram



● Ordering Information

ZYG6206



● Absolute Maximum Ratings

Operating temperature range applies unless otherwise specified.

Parameter	Symbol	Ratings	Unit
Input Voltage	V_{IN}	-0.3 to 6	V
Output Current	V_{OUT}	$V_{SS}-0.3$ to $V_{IN}+0.3$	
Power Dissipation ($T_A = 25^\circ\text{C}$)	P_D	SOT23	150
		SOT89	500
Operating Temperature Range	T_{OPR}	-40 to +125	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-65 to +150	$^\circ\text{C}$

● Electrical Characteristics

$T_A = +25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Test Conditions	Test Circuit	Min	Typ	Max	Unit
Input Voltage	V_{IN}	--	--	1.8	--	6	V

Supply Current	I_{SS}	$V_{IN} = V_{OUT} + 1.0\text{ V}$	2	--	2	--	μA
Output Voltage	V_{OUT}	$V_{IN} = V_{OUT} + 1.0\text{ V}$, $I_{OUT} = 40\text{ mA}$	1	$V_{OUT(T)} \times 0.98$	$V_{OUT(T)}$	$V_{OUT(T)} \times 1.02$	V
Output Current	I_{OUT}	$V_{IN} \geq V_{OUT} + 1.0\text{ V}$	1	300	--	--	mA
Dropout Voltage	V_{DROP}	$I_{OUT} = 100\text{ mA}$	1	--	0.20	0.28	V
		$1.5\text{ V} \leq V_{OUT} \leq 2.5\text{ V}$		--	0.16	0.24	
		$2.6\text{ V} \leq V_{OUT} \leq 3.3\text{ V}$		--	0.12	0.20	
Line Regulations	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \cdot V_{OUT}}$	$V_{OUT} + 0.5\text{ V} \leq V_{IN} \leq 5.5\text{ V}$	1	--	0.05	0.3	%/V
		$I_{OUT} = 80\text{ mA}$		--	20	40	mV
Load Regulations	ΔV_{OUT}	$V_{IN} = V_{OUT} + 1.0\text{ V}$ $1.0\text{ mA} \leq I_{OUT} \leq 80\text{ mA}$		--	± 100	--	ppm/ $^{\circ}\text{C}$
Output Voltage Temperature Characteristics	$\frac{\Delta V_{OUT}}{\Delta T_A \cdot V_{OUT}}$	$V_{IN} = V_{OUT} + 1.0\text{ V}$, $I_{OUT} = 10\text{ mA}$ $-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$	1	--	40	--	dB
Ripple Rejection	RR	$V_{IN} = V_{OUT} + 1.0\text{ V}$, $F = 1.0\text{ kHz}$ $V_{RIP} = 0.5\text{ V}_{RMS}$, $I_{OUT} = 80\text{ mA}$	1	--	30	--	mA
Short current	I_{SHORT}	$V_{IN} = V_{OUT} + 1.5\text{ V}$	1	--	380	--	mA
Maximum Output Current	I_{OUT}	$V_{IN} = V_{OUT} + 1\text{ V}$	1	--	--	--	mA

Note 1. $V_{OUT(T)}$ = Specified output Voltage

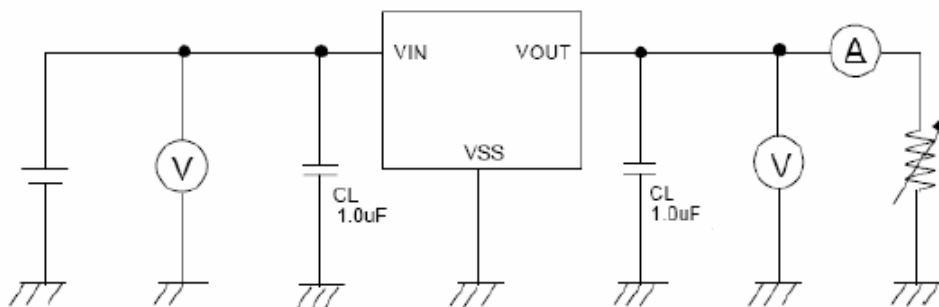
Note 2. $V_{OUT(E)}$ = Effective output Voltage (i.e. the output voltage when " $V_{OUT(T)}+1.0\text{V}$ " is provided at the VIN pin while maintaining a certain I_{OUT} value)

Note 3. $V_{DROP} = \{ V_{IN1} (\text{note5}) - V_{OUT1} (\text{note4}) \}$

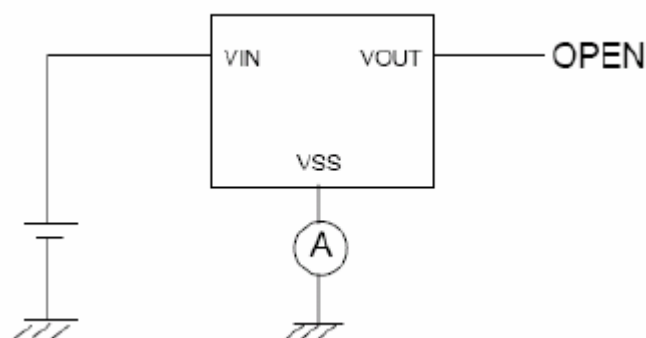
Note 4. V_{OUT1} = A voltage equal to 98% of the output voltage whenever an amply stabilized $I_{OUT} (V_{OUT(T)} + 1.0\text{V})$ is input.

Note 5. V_{IN1} = The input voltage when $V_{OUT} = V_{OUT1}$

Test Circuits

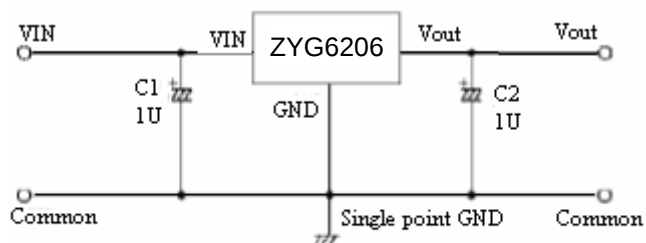


Test Circuit 1



Test Circuit 2

- Typical Application Circuit



- Typical Performance Characteristics

3.0V Out unless otherwise specified

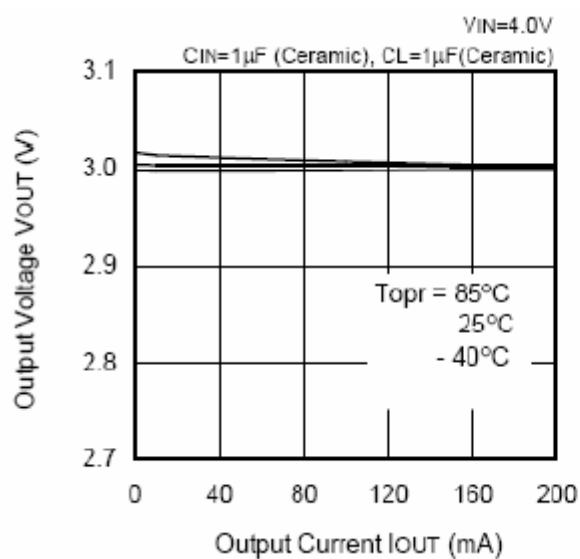


Figure1. Output Voltage vs. Output Current

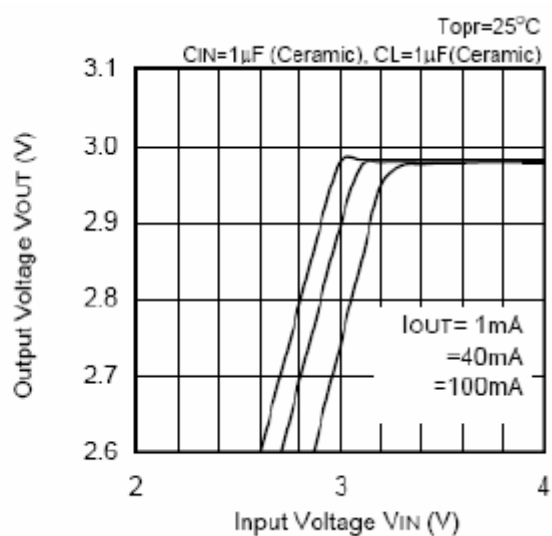


Figure 2. Output Voltage vs. Input Voltage

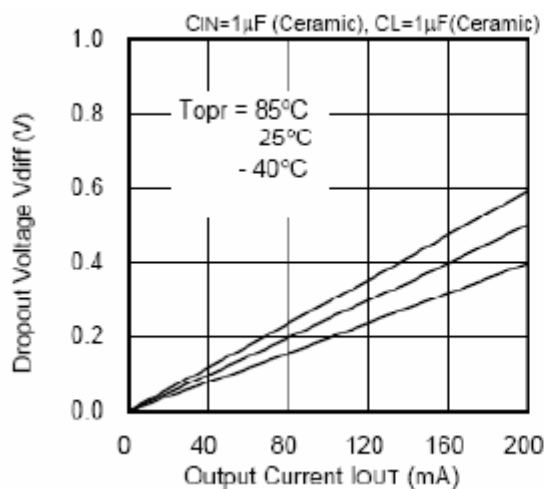


Figure 3. Dropout Voltage vs. Output Current

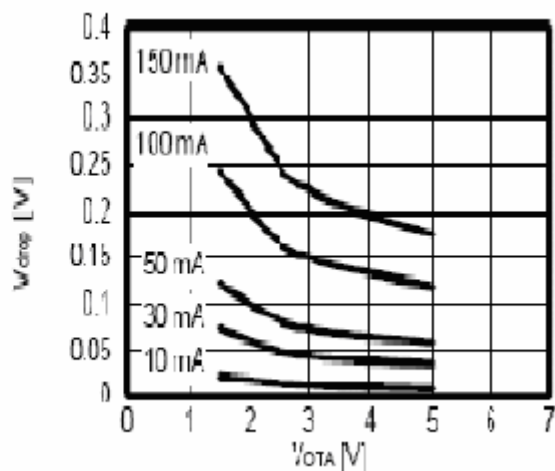


Figure 4. Dropout Voltage vs. Output Voltage

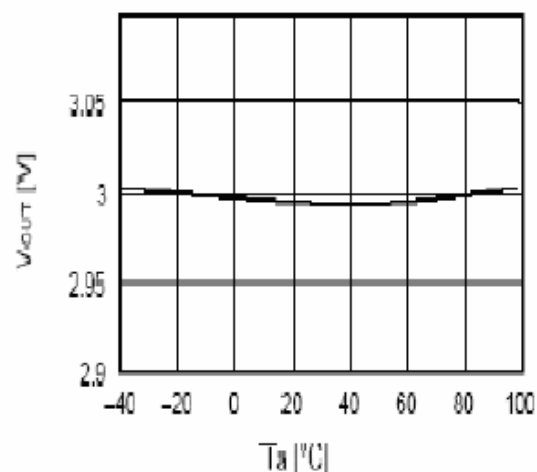


Figure 5. Output Voltage vs. Temperature

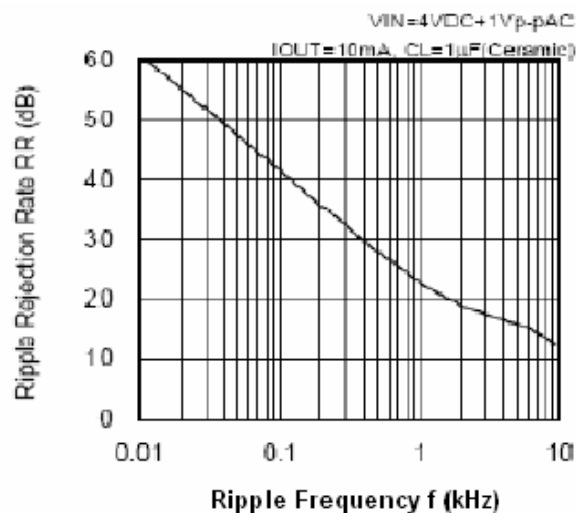


Figure 6. Ripple Rejection Rate

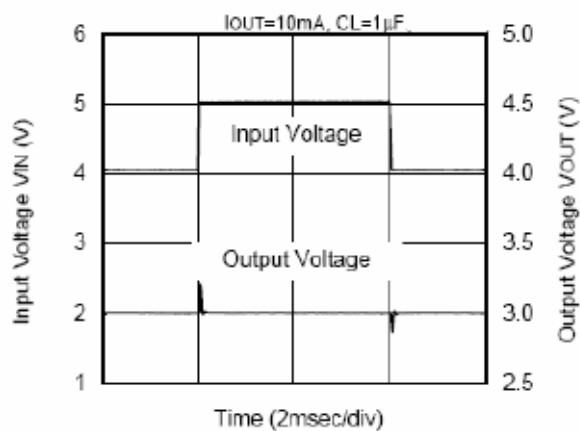


Figure 7. Input Transient Response

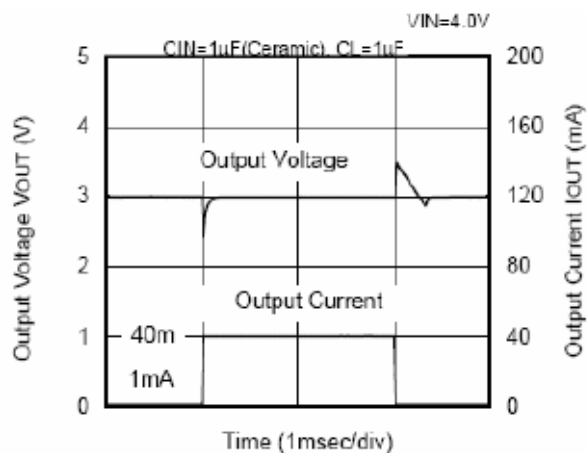


Figure 8. Load Transient Response



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