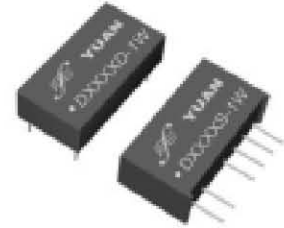


**DXXXXXXS-1W**
**1KV ISOLATED, 1W UNREGULATED DUAL SEPARATE OUTPUT, SIP 7 PACKAGE, MTBF>1M HOURS**
**Available Inputs:**  
**5, 12 and 24 VDC**
**Available Outputs:**  
**(++) 3.3, 5, 7.2, 12, 15 and 18 VDC**  
 Other specifications please enquire Sunyuan Technology.

**Electrical Specifications**

(Typical at + 25° C, nominal input voltage, rated output current unless otherwise specified)

**Input Specifications**

 Voltage range +/- 10 %  
 Filter Capacitors

**Isolation Specifications**

 Rated voltage 1000 VDC  
 Leakage current 1 mA  
 Resistance  $10^9$  Ohm  
 Capacitance 60 pF typ.

**Output Specifications**

 Voltage accuracy +/- 5 %, max.  
 Ripple and noise (at 20 MHz BW) 75 mV p-p, max.  
 Short circuit protection Momentary  
 Line voltage regulation +/- 1,2 % / 1,0 % of  $V_{in}$   
 Load voltage regulation +/- 8 %, load = 20 ~ 100 %  
 Temperature coefficient +/- 0,02 % / °C

**General Specifications**

 Efficiency 70 % to 85 %  
 Switching frequency 125 KHz, typ.

**Environmental Specifications**

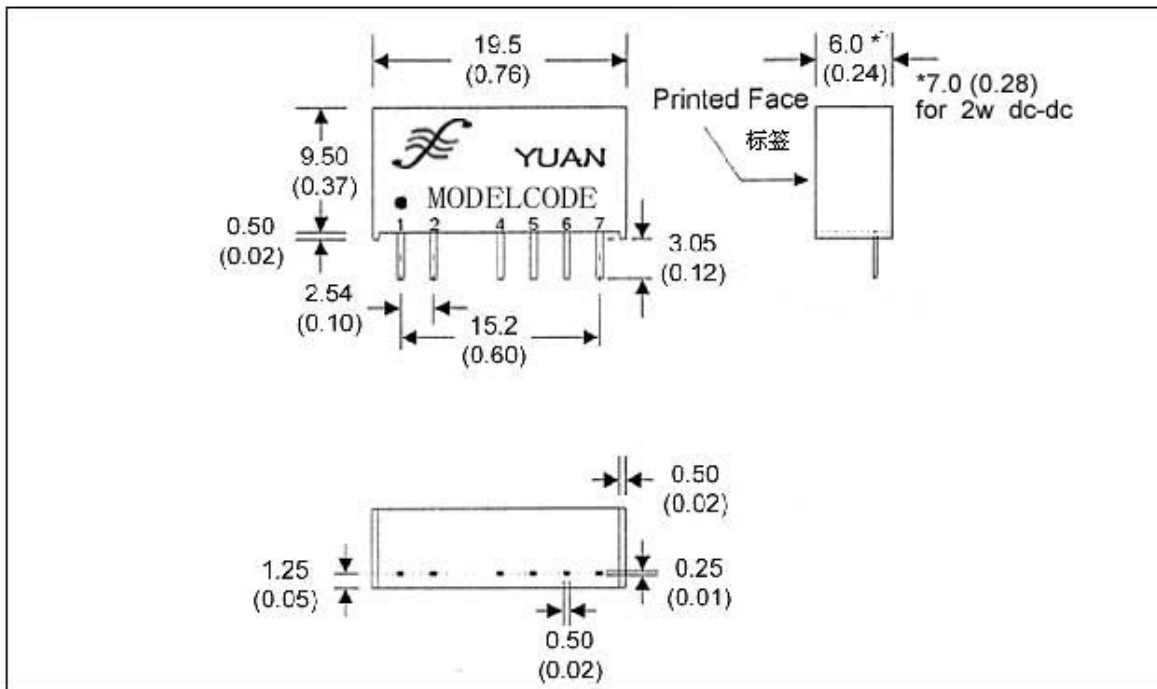
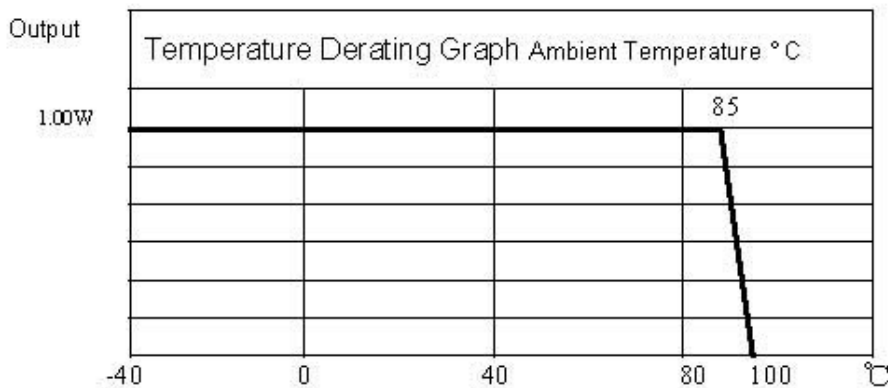
 Operating temperature (ambient) - 40° C to + 85° C  
 Storage temperature - 55 °C to + 125 °C  
 Derating See graph  
 Humidity Up to 90 %, non condensing  
 Cooling Free air convection

**Physical Characteristics**

 Dimensions SIP 19,50 x 6,00 x 9,50 mm  
 0,76 x 0,24 x 0,37 inches  
 Weight 2 g, 3g  
 Case material Non conductive black plastic

**Examples of Part Numbers**

SUNYUAN PARTNO	INPUT VOLTAGE (VDC)	INPUT CURRENT NO LOAD	INPUT CURRENT FULL LOAD	OUTPUT VOLTAGE (VDC)	OUTPUT CURRENT (max. mA)	EFFICIENCY FULL LOAD (% TYPE.)
D050505S-1W	5	16	281	5/5	100/100	71
D051212S-1W	5	16	253	12/12	42/42	80
D051515S-1W	12	11	250	15/15	34/34	80
<b>D120505S-1W</b>	<b>12</b>	<b>10</b>	<b>119</b>	<b>5/5</b>	<b>100/100</b>	<b>80</b>
D121212S-1W	12	15	104	12/12	42/42	78
D240303S-1W	24	6	56	3.3/3.3	150/150	75
D240505S-1W	24	7	55	5/5	100/100	74
D241212S-1W	24	8	55	12/12	42/42	76

**DXXXXXXS-1W**
**1KV ISOLATED, 1W UNREGULATED DUAL SEPARATE OUTPUT, SIP 7 PACKAGE, MTBF>1M HOURS**
**Dimensions**

**Derating Graph and Pinning**


Pin	Connection		
<b>1</b>	+	V <sub>in</sub>	<b>Input</b>
<b>2</b>	-	V <sub>in</sub>	<b>Input</b>
<b>3</b>			Omitted
<b>4</b>	-(+)	V <sub>o1</sub>	<b>Output</b>
<b>5</b>	+(-)	V <sub>o1</sub>	<b>Output</b>
<b>6</b>	-(+)	V <sub>o2</sub>	<b>Output</b>
<b>7</b>	+(-)	V <sub>o2</sub>	<b>Output</b>

- Specification may be changed without any notification.
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