

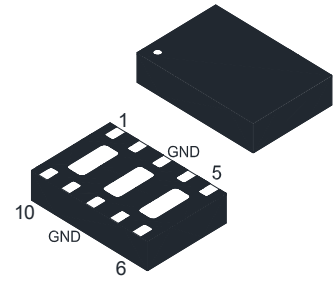
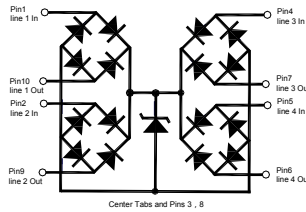
ESDLC3374MP

High Power Transient Voltage Suppressors

for Ethernet Interfaces

Features

- Protects up to eight lines
- Low capacitance for high-speed interfaces
- Low variation in capacitance vs. bias voltage
- Low clamping voltage
- Low operating voltage



Marking Code:3374
DFN3020-10 Plastic package

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Peak Pulse Current ($t_p = 8/20 \mu\text{s}$) ²⁾	I_{PP}	40	A
IEC61000-4-2 (ESD) Air Contact	V_{PP}	± 30 ± 30	KV
Peak Pulse Power ($t_p = 8/20 \mu\text{s}$) ²⁾	P_{PK}	1000	W
Operation Temperature Range	T_j	- 40 to + 125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
Reverse Stand-Off Voltage	V_{RWM}	-	-	3.3	V
Punch-Through Voltage at $I_{PT} = 2 \mu\text{A}$	V_{PT}	3.5	-	-	V
Reverse Breakdown Voltage at $I_{SB} = 50 \text{ mA}$	V_{SB}	2.8	-	-	V
Reverse Current at $V_{RWM} = 3.3 \text{ V}$	I_R	-	-	0.5	μA
Clamping Voltage ¹⁾ at $I_{PP} = 1 \text{ A}$, $t_p = 8/20 \mu\text{s}$, Any I/O to GND at $I_{PP} = 10 \text{ A}$, $t_p = 8/20 \mu\text{s}$, Any I/O to GND at $I_{PP} = 25 \text{ A}$, $t_p = 8/20 \mu\text{s}$, Any I/O to GND	V_C	-	-	5.5 10.5 18	V
Clamping Voltage ^{1),2)} at $I_{PP} = 40 \text{ A}$, $t_p = 8/20 \mu\text{s}$ Line-to-Line, two I/O pins connected together on each line	V_C	-	-	25	V
Junction Capacitance ¹⁾ at $V_R = 0 \text{ V}$, $f = 1 \text{ MHz}$, Any I/O to GND at $V_R = 0 \text{ V}$, $f = 1 \text{ MHz}$, Between I/O pins	C_j	-	- 1.7	5 -	pF

¹⁾ Guaranteed by design (not production tested)

²⁾ Ratings with 2 pins connected together per the recommended configuration (ie pin 1 connected to pin 10, pin 2 connected to pin 9, pin 4 connected to pin 7, and pin 5 connected to pin 6)

TOP DYNAMIC

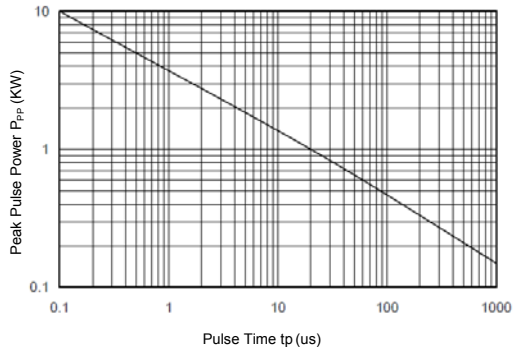


ISO 14001 : 2004 Certificate No. 121525007
ISO 9001 : 2008 Certificate No. 50114012
OHSAS 18001 : 2007 Certificate No. 05131508006
IECQ QC 080000 Certificate No. E52H100014002

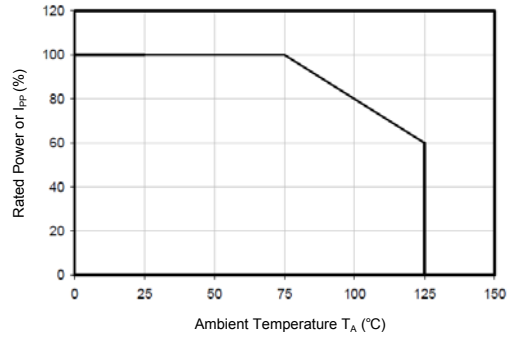
Dated: 13/08/2016 Rev: 02

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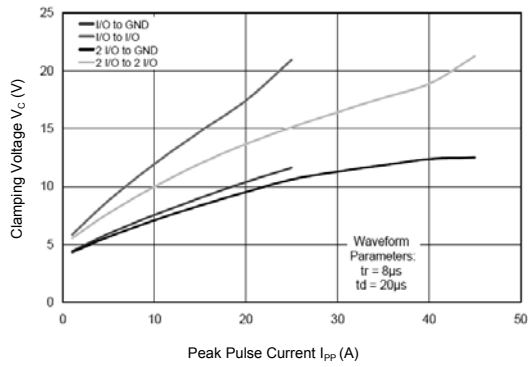
Non-Repetitive Peak Pulse Power vs Pulse Time



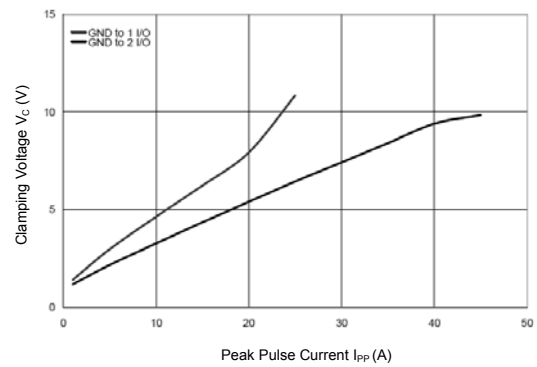
Power Derating Curve



Clamping Voltage vs Peak Pulse Current



Forward Clamping Voltage vs Peak Pulse Current



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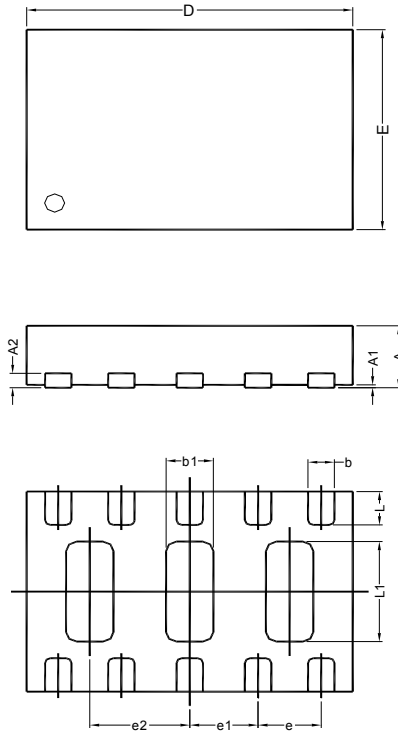
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PACKAGE OUTLINE

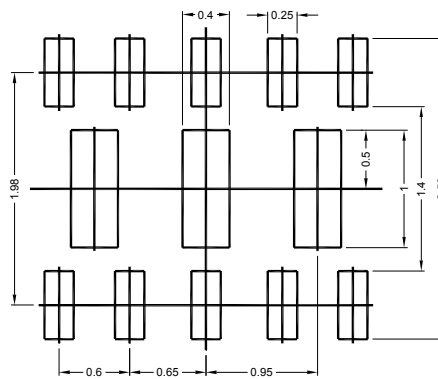
Plastic surface mounted package; 2 leads

DFN3020-10



UNIT	A	A1	A2	b	b1	D	E	e	e1	e2	L	l1
mm	0.5 0.65	0 0.05	0.15	0.15 0.25	0.25 0.45	2.9 3.1	1.9 2.1	0.6	0.65	0.95	0.25 0.35	0.95 1.05

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
DFN3020-10	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

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