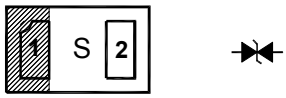
ESDBL1211BP

Transient Voltage Suppressors

for ESD Protection

PINNING

PIN	DESCRIPTION
1	Anode
2	Anode



Transparent top view
Marking Code: **S**
Simplified outline DFN1006-2B and symbol

Features

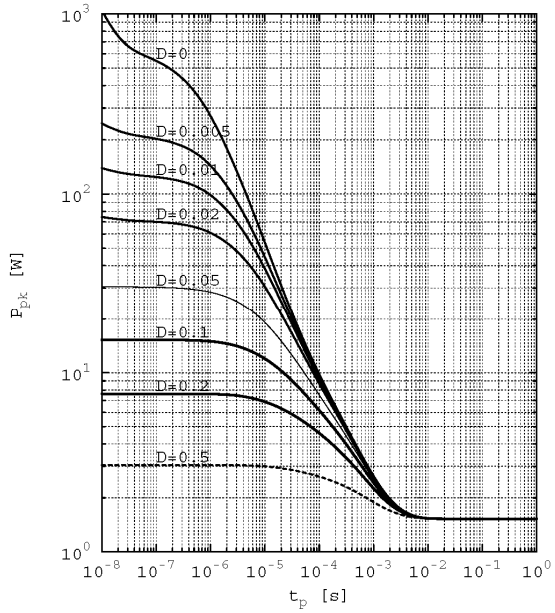
- Protects one data or power line
- Low leakage current
- Bi-direction high reliability
- Low junction capacitance

Absolute Maximum Ratings (T_a = 25℃)

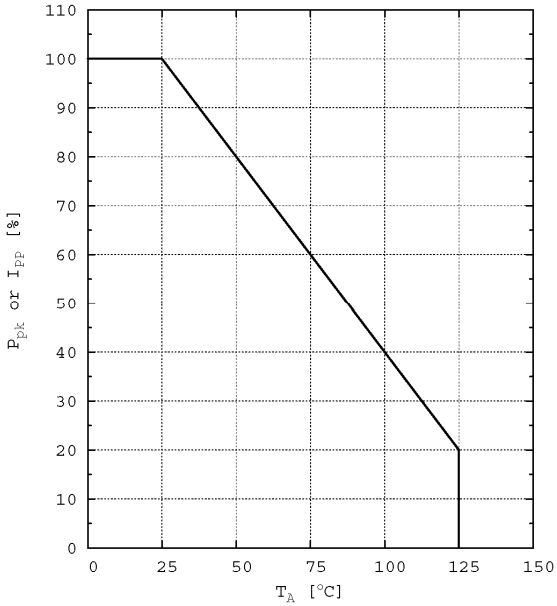
Parameter		Symbol	Value	Unit
Peak Pulse Current (tp = 8/20 μs)		I_{PP}	2	A
IEC61000-4-2 (ESD)	Air Contact	V_{PP}	± 20 ± 20	KV
Peak Pulse Power (tp = 8/20 μs)		P_{PK}	60	W
Operation Temperature Range		T_j	- 55 to + 125	℃
Storage Temperature Range		T_{stg}	- 55 to + 150	℃

Characteristics at T_a = 25℃

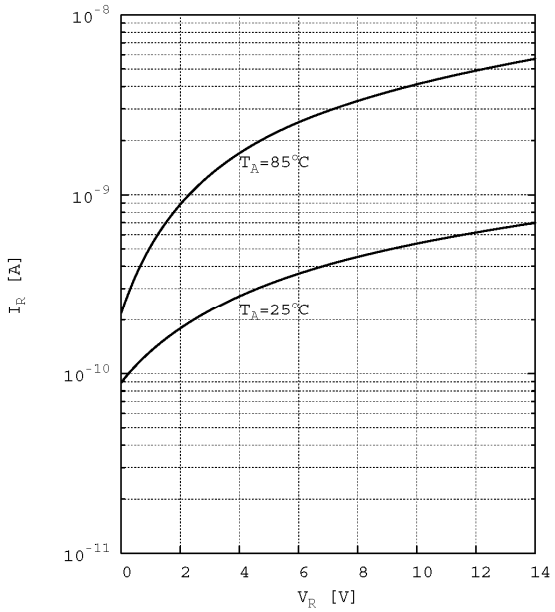
Parameter	Symbol	Min.	Max.	Unit
Reverse Stand-Off Voltage	V_{RWM}	-	12	V
Reverse Breakdown Voltage at I_R = 1 mA	$V_{(BR)R}$	13.3	19	V
Reverse Current at V_{RWM} = 12 V	I_R	-	50	nA
Clamping Voltage at I_{PP} = 1 A, tp = 8/20 μs at I_{PP} = 2 A, tp = 8/20 μs	V_C	- -	25 30	V
Junction Capacitance at V_R = 0 V, f = 1 MHz	C_j	-	12	pF



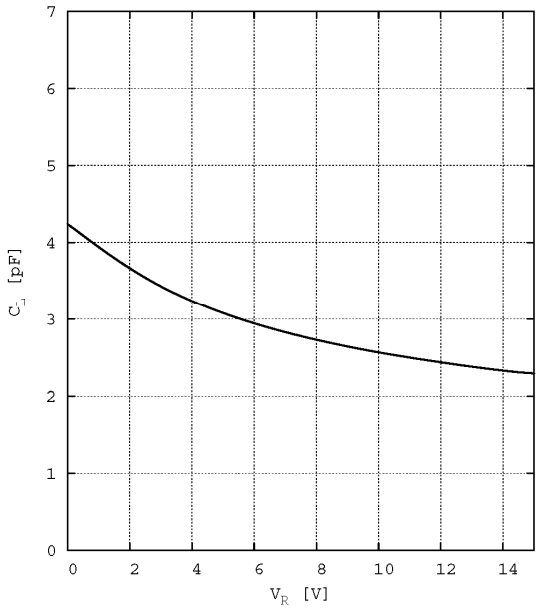
Non-repetitive peak pulse power: $P_{pk} = f(t_p)$



Power derating curve: $P_{pk} = f(T_A)$



Reverse characteristic, $I_R = (V_R)$, T_A = parameter



Line capacitance $C_L = f(V_R)$

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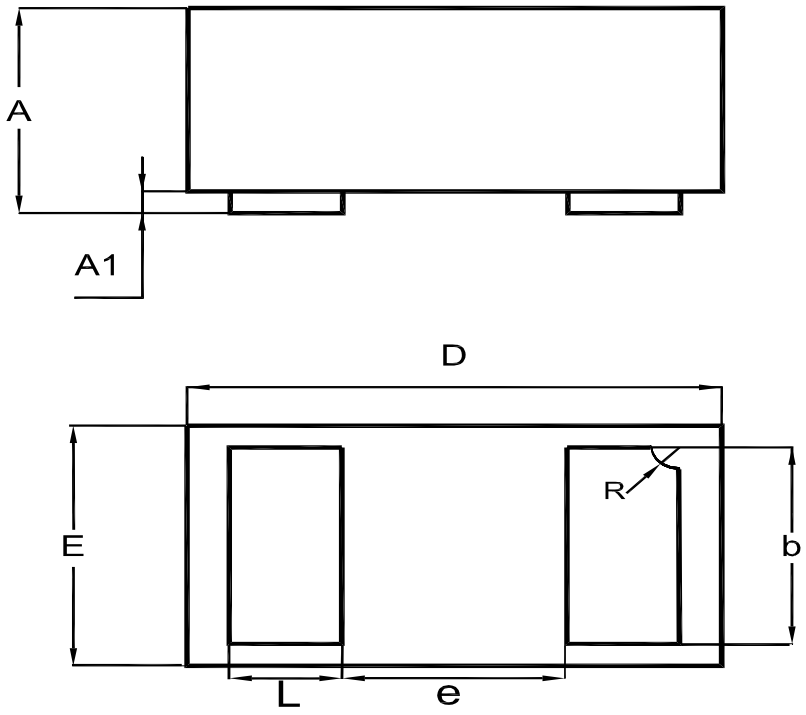
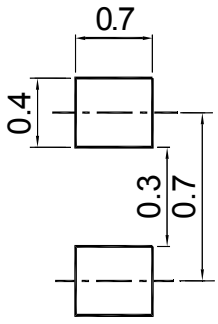
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads **DFN1006-2B**



UNIT	A	A1	b	D	E	e	L	R
mm	0.40 0.36	0.05 0	0.55 0.45	1.05 0.95	0.65 0.55	0.4	0.3 0.2	0.15 0.05

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
DFN1006-2B	8	4 ± 0.1	0.157 ± 0.004	178	7	5,000