FR101G THRU FR107G

GLASS PASSIVATED FAST RECOVERY RECTIFIER

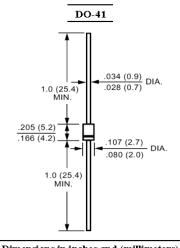
Reverse Voltage - 50 to 1000 V Forward Current - 1 A

Features

- · Fast switching for high efficiency
- · High current capability

Mechanical Data

- Case: Molded plastic, DO-41
- Epoxy: UL 94V-0 rate flame retardant
- Lead: Axial leads, solderable per MIL-STD-202, Method 208 guaranteed
- · Polarity: Color band denotes cathode end
- Mounting Position: Any



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half-wave, 60 Hz, resistive or inductive load, for capacitive load, derate current by 20%.

Parameter	Symbols	FR101G	FR102G	FR103G	FR104G	FR105G	FR106G	FR107G	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current 0.375" (9.5 mm) Lead Length at T _A = 55 °C	I _{F(AV)}	1							Α
Peak Forward Surge Current 8.3 ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	I _{FSM}	30							А
Maximum Forward Voltage at 1 A	V _F	1.3						V	
Maximum Reverse Current $T_A = 25 ^{\circ}\text{C}$ at Rated DC Blocking Voltage $T_A = 100 ^{\circ}\text{C}$	I _R	5 50							μА
Typical Junction Capacitance 1)	Сл		12						
Typical Thermal Resistance 2)	$R_{\theta JA}$		50						
Maximum Reverse Recovery Time 3)	t _{rr}		15	50		250	50	00	nS
Operating and Storage temperature range	T _j , T _{stg}		- 55 to + 150						°C

¹⁾ Measured at 1 MHz and applied reverse voltage of 4 V D.C.



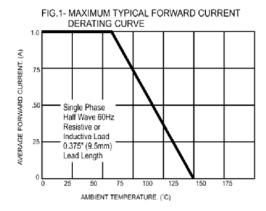


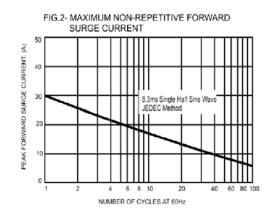


TOP DYNAMIC

²⁾ Thermal resistance from junction to ambient 0.375"(9.5 mm) lead length P.C.B mounted.

 $^{^{3)}}$ Reverse recovery test conditions: I_F = 0.5 A, I_R = 1 A, I_{rr} = 0.25 A.





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TJ=25°C
TJ=25°C
Pulse Width=300µs
1% Duty Cycle

1,0
0,1

FORWARD VOLTAGE. (V)

FIG.3- TYPICAL FORWARD CHARACTERISTICS

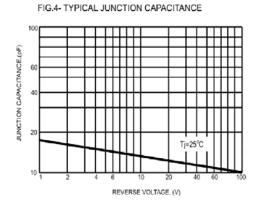


FIG.5- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

