## FR101G THRU FR107G

## GLASS PASSIVATED FAST RECOVERY RECTIFIER

## Reverse Voltage - 50 to 1000 V <br> Forward Current - 1 A



Dimensions in inches and (millimeters)

- Polarity: Color band denotes cathode end
- Mounting Position: Any


## Maximum Ratings and Electrical Characteristics

Ratings at $25^{\circ} \mathrm{C}$ ambient temperature unless otherwise specified. Single phase, half-wave, 60 Hz , resistive or inductive load, for capacitive load, derate current by 20\%.

| Parameter | Symbols | FR101G | FR102G | FR103G | FR104G | FR105G | FR106G | FR107G | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Maximum Recurrent Peak Reverse Voltage | $\mathrm{V}_{\text {RRM }}$ | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| Maximum RMS Voltage | $\mathrm{V}_{\text {RMS }}$ | 35 | 70 | 140 | 280 | 420 | 560 | 700 | V |
| Maximum DC Blocking Voltage | $V_{D C}$ | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| Maximum Average Forward Rectified Current 0.375" ( 9.5 mm ) Lead Length at $\mathrm{T}_{\mathrm{A}}=55^{\circ} \mathrm{C}$ | $I_{\text {F(AV) }}$ | 1 |  |  |  |  |  |  | A |
| Peak Forward Surge Current 8.3 ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method) | $\mathrm{I}_{\text {FSM }}$ | 30 |  |  |  |  |  |  | A |
| Maximum Forward Voltage at 1 A | $V_{F}$ | 1.3 |  |  |  |  |  |  | V |
| $\begin{array}{ll}\text { Maximum Reverse Current } & \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \\ \text { at Rated DC Blocking Voltage } & \mathrm{T}_{\mathrm{A}}=100^{\circ} \mathrm{C}\end{array}$ | IR | $\begin{gathered} 5 \\ 50 \end{gathered}$ |  |  |  |  |  |  | $\mu \mathrm{A}$ |
| Typical Junction Capacitance ${ }^{1)}$ | CJ | 12 |  |  |  |  |  |  | pF |
| Typical Thermal Resistance ${ }^{2)}$ | $\mathrm{R}_{\text {өJA }}$ | 50 |  |  |  |  |  |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Maximum Reverse Recovery Time ${ }^{3)}$ | $\mathrm{t}_{\text {r }}$ | 150 |  |  |  | 250 | 500 |  | nS |
| Operating and Storage temperature range | $\mathrm{T}_{\mathrm{j}, \mathrm{T}_{\text {stg }}}$ | -55 to +150 |  |  |  |  |  |  | ${ }^{\circ} \mathrm{C}$ |

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FIG.5- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM




[^0]:    ${ }^{1)}$ Measured at 1 MHz and applied reverse voltage of 4 V D.C.
    ${ }^{2)}$ Thermal resistance from junction to ambient 0.375 " $(9.5 \mathrm{~mm})$ lead length P.C.B mounted.
    ${ }^{3)}$ Reverse recovery test conditions: $I_{F}=0.5 \mathrm{~A}, I_{R}=1 \mathrm{~A}, I_{I r}=0.25 \mathrm{~A}$.

