

Automotive Half-Amp High-Side Switch

The MC3399 is a High–Side Switch designed to drive loads from the positive side of the power supply. The output is controlled by a TTL compatible input Enable pin. In the "on" state, the device exhibits very low saturation voltages for load currents in excess of 750 mA. The device isolates the load from positive or negative going high voltage transients by abruptly "opening" thus protecting the load from the transient voltage for the duration of the transient. The device automatically re–establishes its original operating state following the transient condition.

The MC3399 is fabricated on a power BIMOS process which combines the best features of Bipolar and MOS technologies. The mixed technology provides higher gain PNP output devices and results in Power Integrated Circuits having substantially reduced quiescent currents.

The device operates over a wide power supply voltage range and can withstand voltage transients (positive or negative) of ± 100 V. A rugged PNP output stage along with active clamp circuitry, output current limit and thermal shutdown permit the driving of all types of loads, including inductive. The MC3399 is offered in 5–lead TO–220 and 16–lead SOIC plastic packages to facilitate either "thru–hole" or surface mount use. In addition, it is specified over a wide ambient operating temperature of -40° C to $+125^{\circ}$ C and is ideally suited for industrial and automotive applications where harsh environments exist.

- Low Switch Voltage Drop
- Load Currents in Excess of 750 mA
- Low Quiescent Current
- Transient Protection Up to ±100 V
- TTL Compatible Enable Input
- On-Chip Current Limit and Thermal Shutdown Circuitry

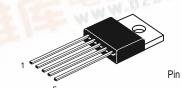
Representative Block Diagram MC3399T Output L O A D

This device contains 52 active transistors.

MC3399

AUTOMOTIVE HALF-AMP HIGH-SIDE SWITCH

SEMICONDUCTOR
TECHNICAL DATA



in 1. Ignition

- 2. Output
- 3. Output
- 4. Ground

5. Input

T SUFFIX
PLASTIC PACKAGE
CASE 314D

Pins 2 and 3 connected to package tab.



DW SUFFIX

PLASTIC PACKAGE

CASE 751G

SOP(8+8)L

Pin 1. Ignition

- 2. N.C.
- 3. N.C.
- 4. N.C.
- 5. Ground
- 6. N.C.
- 7. Input
- 8. N.C.
- 9. Output
- 10.Output
- 11. Output
- 12.Output
- 13.Output
- 14. Output
- 15. Output 16. Output

Rev 1

ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC3399DW	T 400 to 140500	SOP(8+8)L
MC3399T	$T_A = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	Plastic Power

MAXIMUM RATINGS

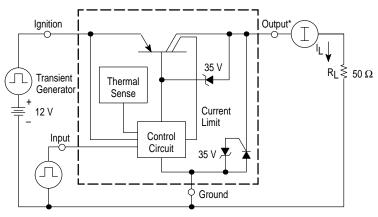
Rating	Symbol	Value	Unit
Ignition Input Voltage (Continuous)	VIGN		Vdc
Forward		25	
Reverse		-16	
Ignition Input Voltage (Transient)	Vign		V
		±60	
		±100	
Input Voltage	V _{in}	-0.3 to +7.0	V
Output Current	Ю	Internally Limited	Α
		Littilled	
Thermal Resistance			°C/W
Plastic Power Package (Case 314D)			
Junction-to-Ambient	R _θ JA1	65	
Junction-to-Tab	$R_{\theta JT}$	5.0	
SOP(8+8)L Plastic Package (Case 751G)			
Junction-to-Ambient	R _{0JA2}	138	
Junction-to-Lead 12	$R_{\theta JL}$	52	
Soldering Temperature (for 10 Seconds)	T _{solder}	260	°C
Junction Temperature	TJ	-40 to +150	°C
Storage Temperature	T _{stg}	-65 to +150	°C

ELECTRICAL CHARACTERISTICS ($V_{IGN} = 12 \text{ V}, I_L = 150 \text{ mA}, -40^{\circ}\text{C} \le T_A = +125^{\circ}\text{C}, \text{ V Input} = "1", unless otherwise noted.)$

Characteristic	Symbol	Min	Тур	Max	Unit
Operating Voltage	VIGN(min)	4.5	-	-	V
Switch Voltage Drop (Saturation) V _{IGN} = 4.5 V I _O = 150 mA, T _A = 25°C I _O = 200 mA, T _A = -40°C I _O = 125 mA, T _A = 125°C V _{IGN} = 12 V I _O = 425 mA, T _A = 25°C I _O = 550 mA, T _A = -40°C V _{IGN} = 16 V I _O = 375 mA, T _A = 125°C	VIGN-VO	- - - - -	0.2 0.3 0.3 0.3 0.3 0.4	0.5 0.5 0.5 0.7 0.7	V
Quiescent Current $V_{IGN} = 12 \text{ V } I_{O} = 150 \text{ mA}, T_{A} = 25^{\circ}\text{C}$ $I_{O} = 550 \text{ mA}, T_{A} = -40^{\circ}\text{C}$ $I_{O} = 300 \text{ mA}, T_{A} = 125^{\circ}\text{C}$	^I GND	- - -	12 25 10	50 100 50	mA
Output Current Limit (VO = 0 V)	I _{SC}	_	1.6	2.5	А
Output Leakage Current (V _{IGN} = 12 V, Input = "0")	l _{Leak}	-	10	150	μΑ
Input Voltage High Logic State Low Logic State	V _{IH} V _{IL}	2.0 –	_ _	0.8	V
Input Current High Logic State (V _{IH} = 5.5 V) Low Logic State (V _{IL} = 0.4 V)	I _{IH}	- -	120 20	- -	μΑ
Output Turn–On Delay Time Input = "0" → "1", T _A = +25°C (Figures 1 and 3)	tDLY(on)	_	50	_	μs
Output Turn–Off Delay Time Input = "1" → "0", T _A = +25°C (Figures 1 and 3)	tDLY(off)	-	5.0	-	μs
Overvoltage Shutdown Threshold	Vin(OV)	26	31	36	V
Output Turn–Off Delay Time (T_A = + 25°C) to Overvoltage Condition, V_{in} stepped from 12 V to 40 V, V \leq 0.9 V _O (Figures 1 and 3)	†DLY	-	2.0	-	μs
Output Recovery Delay Time (T _A = + 25°C) V _{IGN} stepped from 40 V to 12 V, V ≥ 0.9 V _O (Figures 1 and 3)	tRCVY	_	5.0	_	μs

NOTES: 1. Typical values represent characteristics of operation at $T_A = 25$ °C.

Figure 1. Transient Response Test Circuit



NOTE: * Depending on load current and transient duration, an output capacitor (C_O) of sufficient value may be used to hold up output voltage during the transient, and absorb turn–off delay voltage overshoot.

Figure 2. Timing Diagram

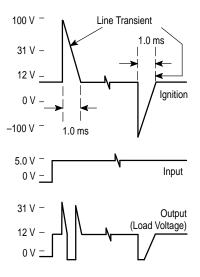


Figure 3. Response Time Diagram

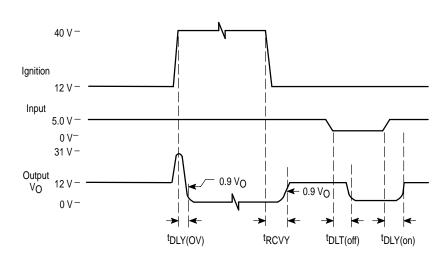
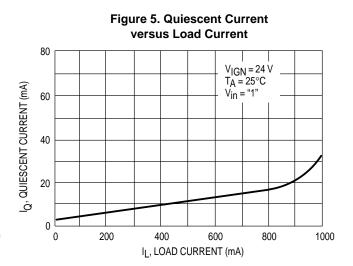
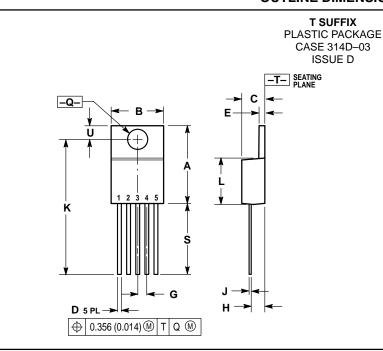


Figure 4. Switch Voltage Drop versus Load Current 800 V_{IGN} = 24 V V_{IGN}-V_O, IGNITION TO OUTPUT VOLTAGE DIFFERENTIAL (mV) T_A = 25°C V_{in} = "1" 600 400 200 0 0 200 600 800 1000 IL, LOAD CURRENT (mA)

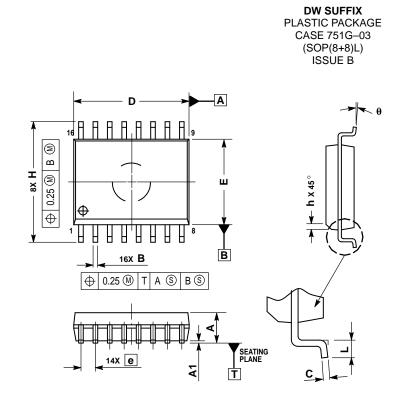


OUTLINE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
 DIMENSION D DOES NOT INCLUDE INTERCONNECT BAR (DAMBAR) PROTRUSION. DIMENSION D INCLUDING PROTRUSION SHALL NOT EXCEED 10.92 (0.043) MAXIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.572	0.613	14.529	15.570
В	0.390	0.415	9.906	10.541
С	0.170	0.180	4.318	4.572
D	0.025	0.038	0.635	0.965
Е	0.048	0.055	1.219	1.397
G	0.067 BSC		1.702 BSC	
Н	0.087	0.112	2.210	2.845
۲	0.015	0.025	0.381	0.635
K	1.020	1.065	25.908	27.051
L	0.320	0.365	8.128	9.271
Ø	0.140	0.153	3.556	3.886
U	0.105	0.117	2.667	2.972
S	0.543	0.582	13.792	14.783



- NOTES:
 1. DIMENSIONS ARE IN MILLIMETERS.
 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.

- PER ASME Y14.5M, 1994.
 3. DIMENSIONS D AND E DO NOT INLCUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
 5. DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL CONDITION. CONDITION.

	MILLIMETERS		
DIM	MIN	MAX	
Α	2.35	2.65	
A1	0.10	0.25	
В	0.35	0.49	
C	0.23	0.32	
D	10.15	10.45	
Е	7.40	7.60	
е	1.27 BSC		
Η	10.05	10.55	
h	0.25	0.75	
L	0.50	0.90	
θ	0 °	7 °	

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