

Solid State Relay OCMOS FET

PS7200E-1A

4-PIN SOP, $0.8~\Omega$ LOW ON-STATE RESISTANCE 1-ch Optical Coupled MOS FET

DESCRIPTION

The PS7200E-1A is a low on-state resistance solid state relay containing a GaAs LED on the light emitting side (input side) and MOS FETs on the output side.

It is suitable for high-frequency signal control due to its low $C \times R$, low on-state resistance, and low off-state leakage current.

FEATURES

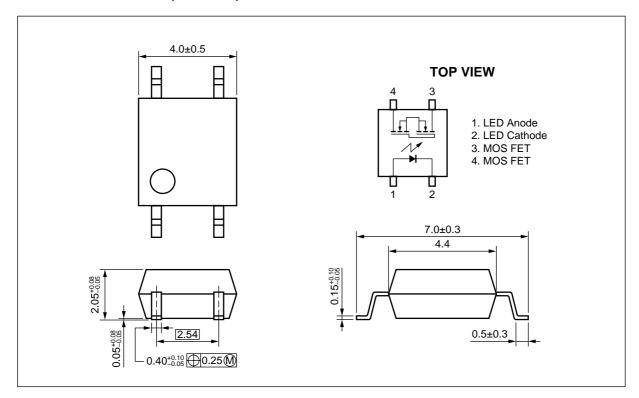
- Low ERT (ERT = 78 ps TYP.)
- Low $C \times R$ ($C \times R = 27 pF \cdot \Omega$)
- Low on-state resistance ($R_{on} = 0.8 \Omega \text{ TYP.}$)
- · Low off-state leakage current
- 1 channel type (1 a output)
- · Designed for AC/DC switching line changer
- Small and thin package (4-pin SOP, Height = 2.1 mm)
- High isolation voltage (BV = 1 500 Vr.m.s.)
- Low offset voltage
- Ordering number of tape product: PS7200E-1A-E3, E4, F3, F4

APPLICATIONS

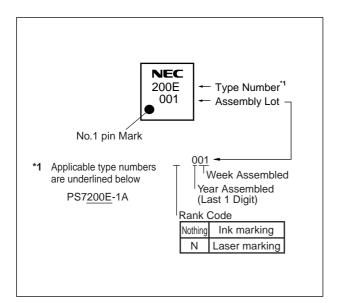
Measurement equipment

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

PACKAGE DIMENSIONS (Unit:mm)



MARKING EXAMPLE



ORDERING INFORMATION (Solder Contains Lead)

Part Number	Package	Packing Style	Application Part Number*1
PS7200E-1A	4-pin SOP	Magazine case 100 pcs	PS7200E-1A
PS7200E-1A-E3		Embossed Tape 900 pcs/reel	
PS7200E-1A-E4			
PS7200E-1A-F3		Embossed Tape 3 500 pcs/reel	
PS7200E-1A-F4			

^{*1} For the application of the Safety Standard, following part number should be used.

ORDERING INFORMATION (Pb-Free)

Part Number	Package	Packing Style	Application Part Number*1
PS7200E-1A-A	4-pin SOP	Magazine case 100 pcs	PS7200E-1A
PS7200E-1A-E3-A		Embossed Tape 900 pcs/reel	
PS7200E-1A-E4-A			
PS7200E-1A-F3-A		Embossed Tape 3 500 pcs/reel	
PS7200E-1A-F4-A			

^{*1} For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (TA = 25 °C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current (DC)	lF	50	mA
	Reverse Voltage	VR	5.0	٧
	Power Dissipation	Po	50	mW
	Peak Forward Current *1	I FP	1	Α
MOS FET	Break Down Voltage	VL	40	V
	Continuous Load Current	lL	250	mA
	Power Dissipation	Po	100	mW
Isolation Voltage *2		BV	1 500	Vr.m.s.
Total Power Dissipation		Рт	150	mW
Operating Ambient Temperature		TA	-40 to +85	°C
Storage Temperature		T _{stg}	-40 to +100	°C

^{*1} PW = 100 μ s, Duty Cycle = 1 %

^{*2} AC voltage for 1 minute at $T_A = 25$ °C, RH = 60 % between input and output

RECOMMENDED OPERATING CONDITIONS (TA = 25 °C)

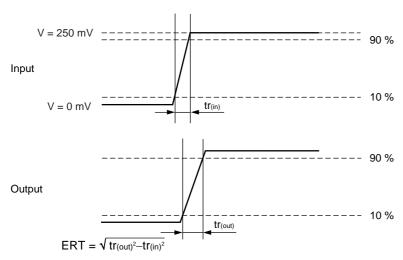
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
LED Operating Current	lF	2	5	20	mA
LED Off Voltage	VF	0		0.5	V

ELECTRICAL CHARACTERISTICS (TA = 25 °C)

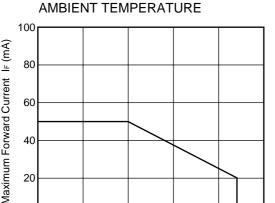
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	I _F = 5 mA		1.1	1.4	V
	Reverse Current	IR	V _R = 5 V			5.0	μА
MOS FET	Off-state Leakage Current	Loff	V _D = 40 V		0.1	10	nA
	Output Capacitance	Cout	V _D = 0 V, f = 1 MHz		33.5		pF
Coupled	LED On-state Current	I Fon	I∟ = 250 mA			2.0	mA
	On-state Resistance	R _{on1}	IF = 5 mA, IL = 10 mA		0.8	1.6	Ω
		Ron2	$I_F = 5 \text{ mA}, I_L = 250 \text{ mA}, t \le 10 \text{ ms}$		0.8	1.6	
	Turn-on Time*1	ton	If = 5 mA, Vo = 5 V, RL = 500 Ω ,		0.48	1.0	ms
	Turn-off Time*1	toff	PW ≥ 10 ms		0.15	0.5	
	Isolation Resistance	R _{I-O}	Vi-o = 1.0 kVpc	10 ⁹			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1 MHz		0.5		pF
	Equivalent Rise Time ^{*2}	ERT	I _F = 10 mA, $tr_{(in)}$ = 25 ps, V = 250 mV, 50 Ω termination		78		ps

- *1 The turn-on time and turn-off time are specified as input-pulse width ≥ 10 ms.

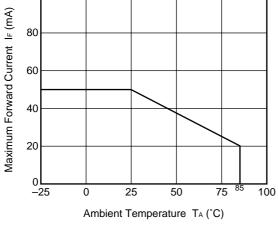
 Be aware that when the device operates with an input-pulse width of under 10 ms, the turn-on time and turn-off time will increase.
- *2 ERT waveform and equation



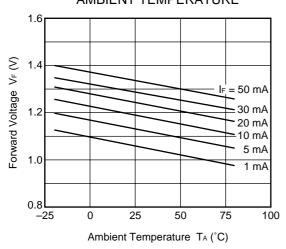
TYPICAL CHARACTERISTICS (TA = 25 °C, unless otherwise specified)



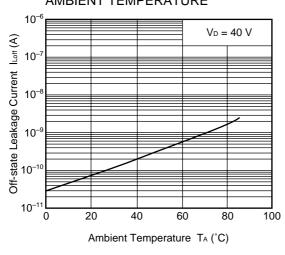
MAXIMUM FORWARD CURRENT vs.



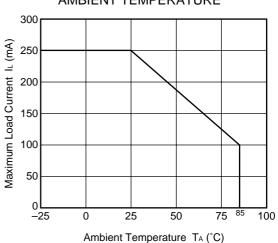
FORWARD VOLTAGE vs. AMBIENT TEMPERATURE



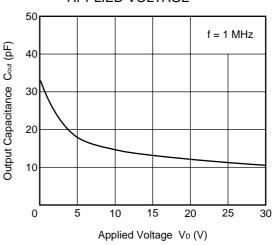
OFF-STATE LEAKAGE CURRENT vs. AMBIENT TEMPERATURE



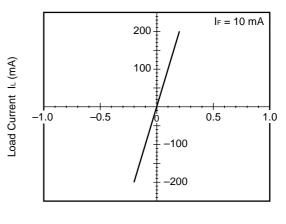
MAXIMUM LOAD CURRENT vs. AMBIENT TEMPERATURE



OUTPUT CAPACITANCE vs. APPLIED VOLTAGE

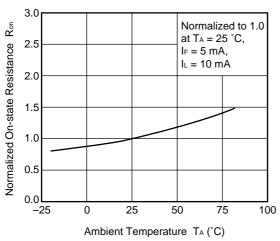


LOAD CURRENT vs. LOAD VOLTAGE

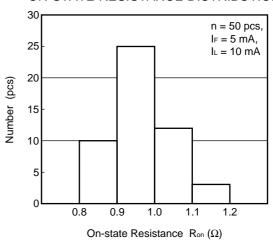


Load Voltage V_L (V)

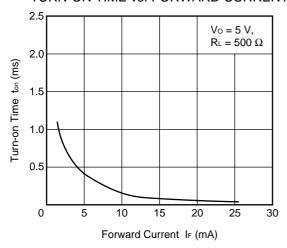
NORMALIZED ON-STATE RESISTANCE vs. AMBIENT TEMPERATURE



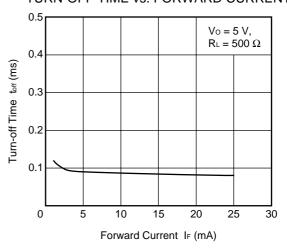
ON-STATE RESISTANCE DISTRIBUTION



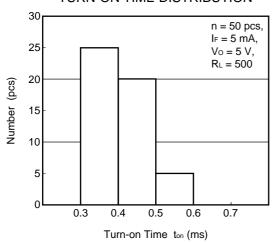
TURN-ON TIME vs. FORWARD CURRENT



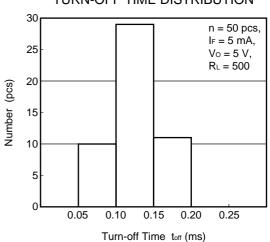
TURN-OFF TIME vs. FORWARD CURRENT



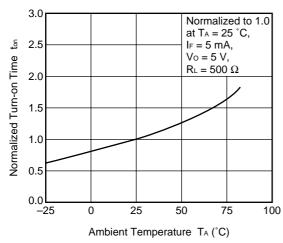
TURN-ON TIME DISTRIBUTION



TURN-OFF TIME DISTRIBUTION

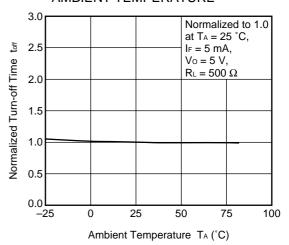


NORMALIZED TURN-ON TIME vs. AMBIENT TEMPERATURE

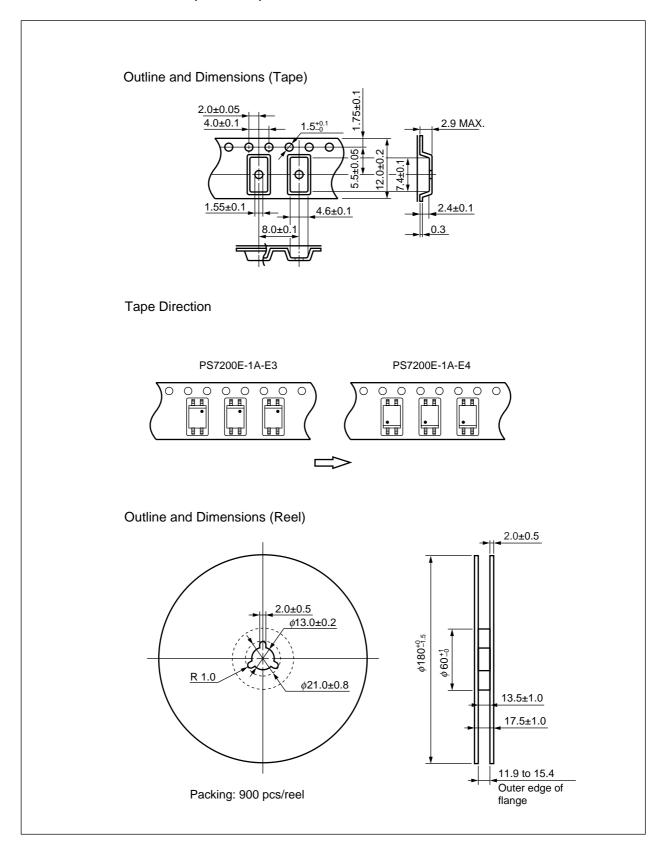


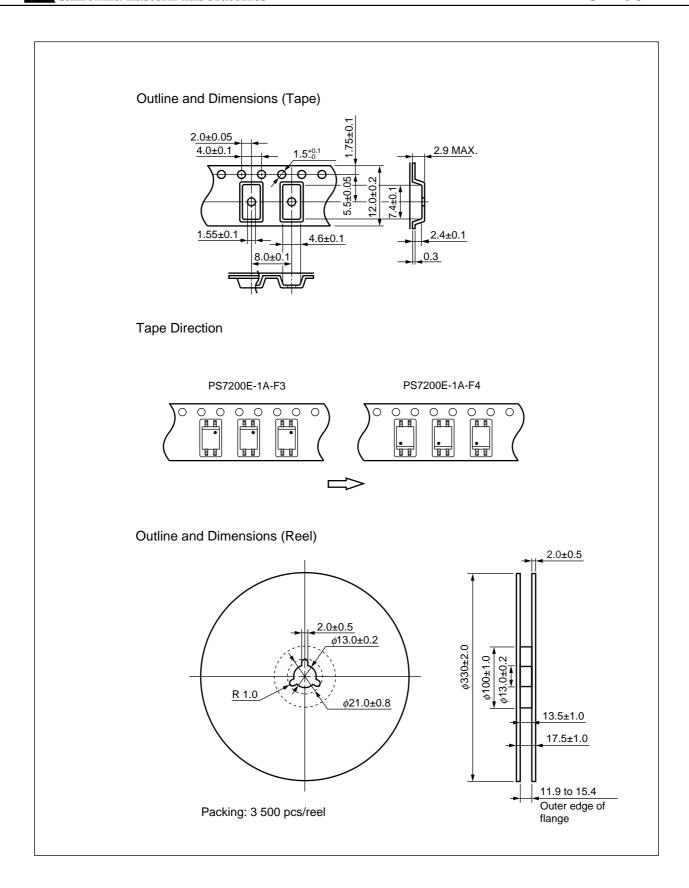
Remark The graphs indicate nominal characteristics.

NORMALIZED TURN-OFF TIME vs. AMBIENT TEMPERATURE



★ TAPING SPECIFICATIONS (Unit:mm)





* RECOMMENDED SOLDERING CONDITIONS

(1) Infrared reflow soldering

• Peak reflow temperature 260°C or below (package surface temperature)

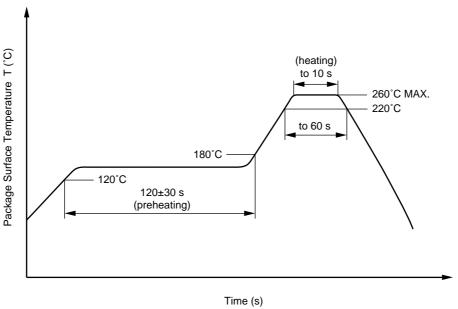
Time of peak reflow temperature
 Time of temperature higher than 220°C
 60 seconds or less

Time to preheat temperature from 120 to 180°C 120±30 s
 Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



(2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120°C or below (package surface temperature)

• Number of times One

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

(3) Cautions

• Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.



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Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices		
Lead (Pb)	< 1000 PPM	-A Not Detected	-AZ (*)	
Mercury	< 1000 PPM	Not Detected		
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not Detected		
PBB	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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