

# Dual and single low noise op amp

# NE5533/5533A/ NE/SA/SE5534/5534A

## DESCRIPTION

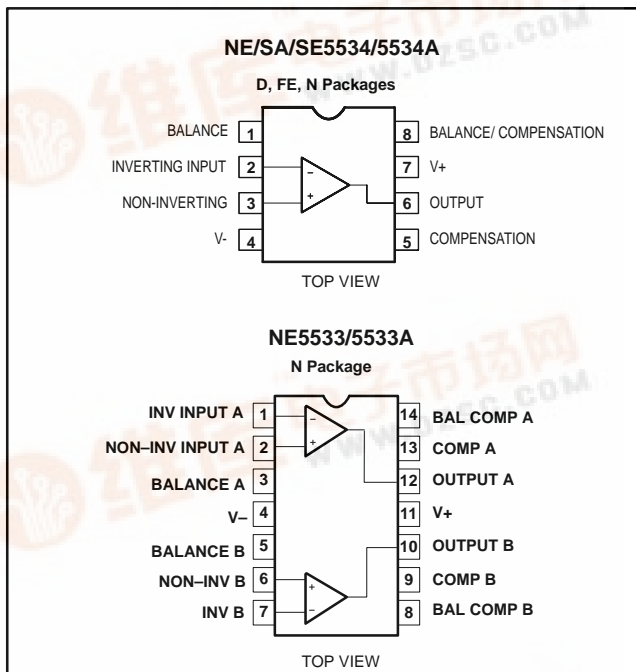
The 5533/5534 are dual and single high-performance low noise operational amplifiers. Compared to other operational amplifiers, such as TL083, they show better noise performance, improved output drive capability and considerably higher small-signal and power bandwidths.

This makes the devices especially suitable for application in high quality and professional audio equipment, in instrumentation and control circuits and telephone channel amplifiers. The op amps are internally compensated for gain equal to, or higher than, three. The frequency response can be optimized with an external compensation capacitor for various applications (unity gain amplifier, capacitive load, slew rate, low overshoot, etc.) If very low noise is of prime importance, it is recommended that the 5533A/5534A version be used which has guaranteed noise specifications.

## FEATURES

- Small-signal bandwidth: 10MHz
- Output drive capability: 600Ω, 10V<sub>RMS</sub> at VS=±18V
- Input noise voltage:  $4nV/\sqrt{Hz}$
- DC voltage gain: 100000
- AC voltage gain: 6000 at 10kHz
- Power bandwidth: 200kHz
- Slew rate: 13V/μs
- Large supply voltage range: ±3 to ±20V

## PIN CONFIGURATIONS



## APPLICATIONS

- Audio equipment
- Instrumentation and control circuits
- Telephone channel amplifiers
- Medical equipment

## ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
14-Pin Plastic Dual In-Line Package (DIP)	0 to +70°C	NE5533N	0405B
14-Pin Plastic Dual In-Line Package (DIP)	0 to +70°C	NE5533AN	0405B
8-Pin Plastic Small Outline (SO) package	0 to +70°C	NE5534D	0174C
8-Pin Hermetic Ceramic Dual In-Line Package (CERDIP)	0 to +70°C	NE5534FE	
8-Pin Plastic Dual In-Line Package (DIP)	0 to +70°C	NE5534N	0404B
8-Pin Plastic Small Outline (SO) package	0 to +70°C	NE5534AD	0174C
8-Pin Hermetic Ceramic Dual In-Line Package (CERDIP)	0 to +70°C	NE5534AF	
8-Pin Plastic Dual In-Line Package (DIP)	0 to +70°C	NE5534AN	0404B
8-Pin Plastic Dual In-Line Package (DIP)	-40°C to +85°C	SA5534N	0404B
8-Pin Plastic Small Outline (SO) package	-40°C to +85°C	SA5534AD	0174C
8-Pin Plastic Dual In-Line Package (DIP)	-55°C to +125°C	SE5534N	0404B
8-Pin Hermetic Ceramic Dual In-Line Package (CERDIP)	-55°C to +125°C	SE5534AF	
8-Pin Plastic Dual In-Line Package (DIP)	-55°C to +125°C	SE5534AN	0404B
8-Pin Plastic Dual In-Line Package (DIP)	-40°C to +85°C	SA5534AN	0404B



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NE5533/5533A/  
NE/SA/SE5534/5534A**ABSOLUTE MAXIMUM RATINGS**

SYMBOL	PARAMETER	RATING	UNIT
V <sub>S</sub>	Supply voltage	±22	V
V <sub>IN</sub>	Input voltage	±V supply	V
V <sub>DIFF</sub>	Differential input voltage <sup>1</sup>	±0.5	V
T <sub>A</sub>	Operating temperature range		
	SE	-55 to +125	°C
	SA	-40 to +85	°C
	NE	0 to +70	°C
T <sub>STG</sub>	Storage temperature range	-65 to +150	°C
T <sub>J</sub>	Junction temperature	150	°C
P <sub>D</sub>	Power dissipation at 25°C <sup>2</sup>		
	16D Pkg	1350	mW
	16N Pkg	1500	mW
	8D Pkg	750	mW
	8FE Pkg	800	mW
	8N Pkg	1150	mW
	Output short-circuit duration <sup>3</sup>	Indefinite	
T <sub>SOLD</sub>	Lead soldering temperature (10sec max)	300	°C

**NOTES:**

- Diodes protect the inputs against over voltage. Therefore, unless current-limiting resistors are used, large currents will flow if the differential input voltage exceeds 0.6V. Maximum current should be limited to ±10mA.
- For operation at elevated temperature, derate packages based on the following junction-to-ambient thermal resistance:
  - 8-pin ceramic DIP 150°C/W
  - 8-pin plastic DIP 105°C/W
  - 8-pin plastic SO 160°C/W
  - 16-pin plastic DIP 80°C/W
  - 16-pin plastic SO 90°C/W
- Output may be shorted to ground at V<sub>S</sub>=±15V, T<sub>A</sub>=25°C. Temperature and/or supply voltages must be limited to ensure dissipation rating is not exceeded.

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NE5533/5533A/  
NE/SA/SE5534/5534A**DC ELECTRICAL CHARACTERISTICS** $T_A=25^\circ\text{C}$ ,  $V_S=\pm 15\text{V}$ , unless otherwise specified. 1, 2, 3

SYMBOL	PARAMETER	TEST CONDITIONS	SE5534/5534A			NE5533/5533A NE/SA5534/5534A			UNIT
			Min	Typ	Max	Min	Typ	Max	
$V_{OS}$	Offset voltage	Over temperature		0.5	2		0.5	4	mV
$\Delta V_{OS}/\Delta T$				5	3		5	5	$\mu\text{V}/^\circ\text{C}$
$I_{OS}$	Offset current	Over temperature		10	200		20	300	nA
$\Delta I_{OS}/\Delta T$				200	500		200	400	nA
$I_B$	Input current	Over temperature		400	800		500	1500	nA
$\Delta I_B/\Delta T$				5	1500		5	2000	nA
$I_{CC}$	Supply current per op amp	Over temperature		4	6.5		4	8	mA
				9			10	10	mA
$V_{CM}$	Common mode input range		$\pm 12$	$\pm 13$		$\pm 12$	$\pm 13$		V
CMRR	Common mode rejection ratio		80	100		70	100		dB
PSRR	Power supply rejection ratio			10	50		10	100	$\mu\text{V}/\text{V}$
$A_{VOL}$	Large-signal voltage gain	$R_L \geq 600\Omega$ , $V_O = \pm 10\text{V}$	50	100		25	100		V/mV
		Over temperature	25			15			V/mV
$V_{OUT}$	Output swing	$R_L \geq 600\Omega$	$\pm 12$	$\pm 13$		$\pm 12$	$\pm 13$		V
		Over temperature	$\pm 10$	$\pm 12$		$\pm 10$	$\pm 12$		V
		$R_L \geq 600\Omega$ , $V_S = \pm 18\text{V}$	$\pm 15$	$\pm 16$		$\pm 15$	$\pm 16$		V
		$R_L \geq 2\text{k}\Omega$	$\pm 13$	$\pm 13.5$		$\pm 13$	$\pm 13.5$		V
	Over temperature	$\pm 12$	$\pm 12.5$		$\pm 12$	$\pm 12.5$		V	
$R_{IN}$	Input resistance		50	100		30	100		k $\Omega$
$I_{SC}$	Output short circuit current			38			38		mA

**NOTES:**

- For NE5533/5533A/5534/5534A,  $T_{MIN} = 0^\circ\text{C}$ ,  $T_{MAX} = 70^\circ\text{C}$
- For SE5534/5534A,  $T_{MIN} = -55^\circ\text{C}$ ,  $T_{MAX} = +125^\circ\text{C}$
- For SA5534/5534A,  $T_{MIN} = -40^\circ\text{C}$ ,  $T_{MAX} = +125^\circ\text{C}$

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NE5533/5533A/  
NE/SA/SE5534/5534A**AC ELECTRICAL CHARACTERISTICS** $T_A=25^\circ\text{C}$ ,  $V_S=\pm 15\text{V}$ , unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	SE5534/5534A			NE5533/5533A NE/SA5534/5534A			UNIT
			Min	Typ	Max	Min	Typ	Max	
$R_{OUT}$	Output resistance	$A_V=30\text{dB}$ closed-loop $f=10\text{kHz}$ , $R_L=600\Omega$ , $C_C=22\text{pF}$		0.3			0.3		$\Omega$
	Transient response	Voltage-follower, $V_{IN}=50\text{mV}$ $R_L=600\Omega$ , $C_C=22\text{pF}$ , $C_L=100\text{pF}$							
$t_R$	Rise time			20			20		ns
	Overshoot			20			20		%
	Transient response	$V_{IN}=50\text{mV}$ , $R_L=600\Omega$ $C_C=47\text{pF}$ , $C_L=500\text{pF}$							
$t_R$	Rise time			50			50		ns
	Overshoot			35			35		%
$A_V$	Gain	$f=10\text{kHz}$ , $C_C=0$ $f=10\text{kHz}$ , $C_C=22\text{pF}$		6 2.2			6 2.2		V/mV V/mV
GBW	Gain bandwidth product	$C_C=22\text{pF}$ , $C_L=100\text{pF}$		10			10		MHz
SR	Slew rate	$C_C=0$ $C_C=22\text{pF}$		13 6			13 6		V/ $\mu\text{s}$ V/ $\mu\text{s}$
	Power bandwidth	$V_{OUT}=\pm 10\text{V}$ , $C_C=0$ $V_{OUT}=\pm 10\text{V}$ , $C_C=22\text{pF}$ $V_{OUT}=\pm 14\text{V}$ , $R_L=600\Omega$ $C_C=22\text{pF}$ , $V_{CC}=\pm 18\text{V}$		200 95 70			200 95 70		kHz kHz kHz

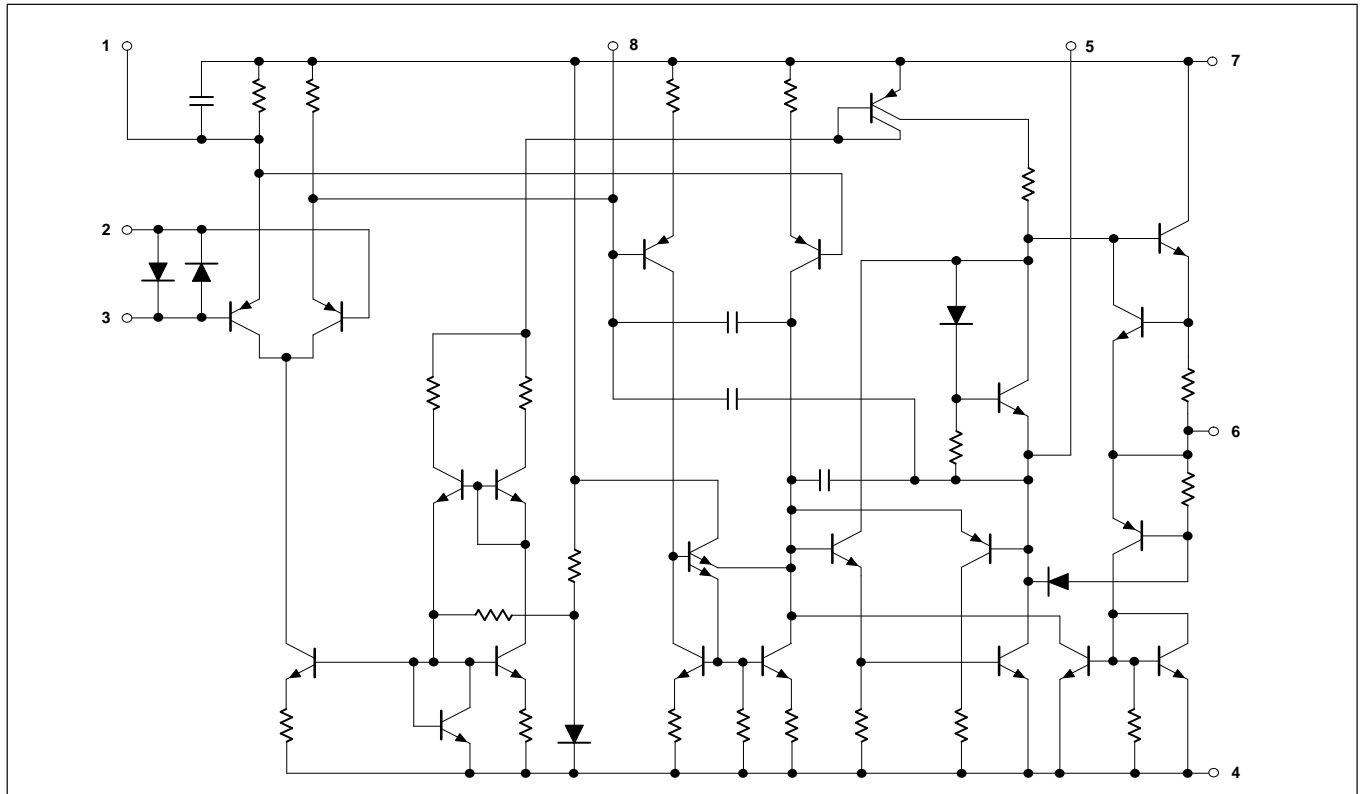
**ELECTRICAL CHARACTERISTICS** $T_A=25^\circ\text{C}$ ,  $V_S = 15\text{V}$ , unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	5533/5534			5533A/5534A			UNIT
			Min	Typ	Max	Min	Typ	Max	
$V_{NOISE}$	Input noise voltage	$f_O=30\text{Hz}$ $f_O=1\text{kHz}$		7 4			5.5 3.5	7 4.5	nV/ $\sqrt{\text{Hz}}$ nV/ $\sqrt{\text{Hz}}$
$I_{NOISE}$	Input noise current	$f_O=30\text{Hz}$ $f_O=1\text{kHz}$		2.5 0.6			1.5 0.4		pA/ $\sqrt{\text{Hz}}$ pA/ $\sqrt{\text{Hz}}$
	Broadband noise figure	$f=10\text{Hz}-20\text{kHz}$ , $R_S=5\text{k}\Omega$					0.9		dB
	Channel separation	$f=1\text{kHz}$ , $R_S=5\text{k}\Omega$		110			110		dB

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## EQUIVALENT SCHEMATIC

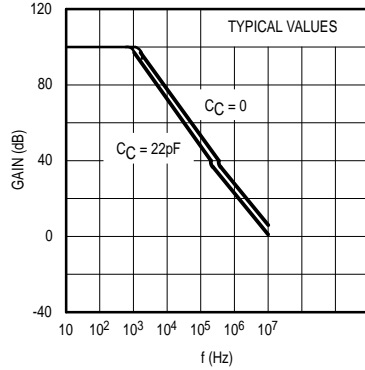


# Dual and single low noise op amp

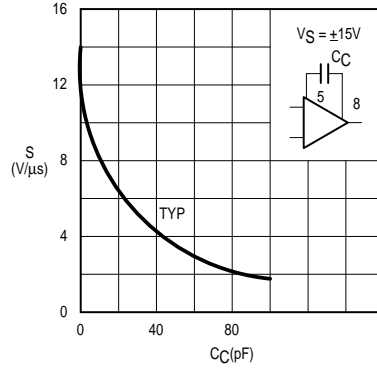
## NE5533/5533A/ NE/SA/SE5534/5534A

### TYPICAL PERFORMANCE CHARACTERISTICS

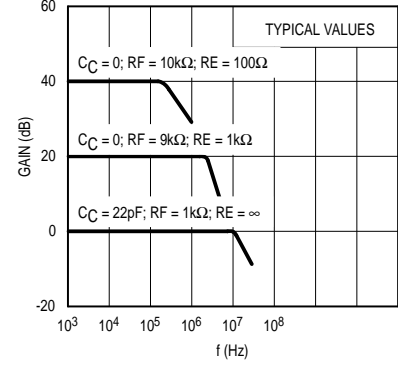
**Open-Loop Frequency Response**



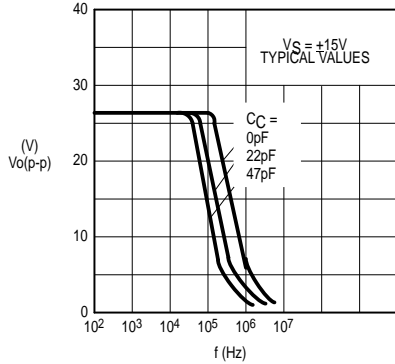
**Slew Rate as a Function of Compensation Capacitance**



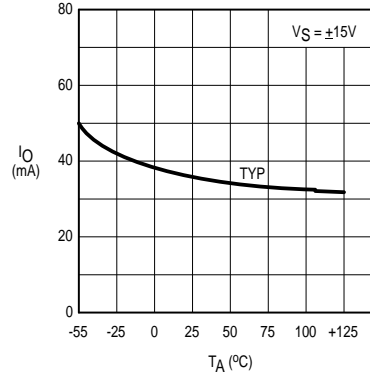
**Closed-Loop Frequency Response**



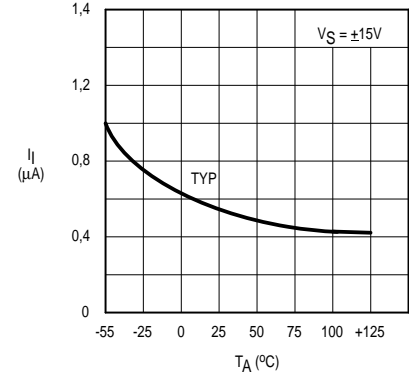
**Large-Signal Frequency Response**



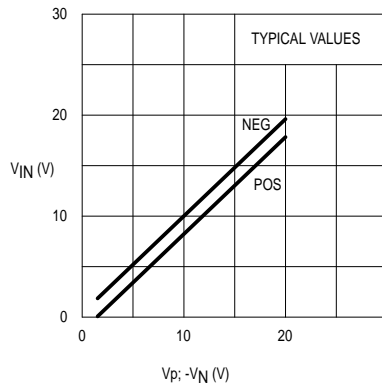
**Output Short-Circuit Current**



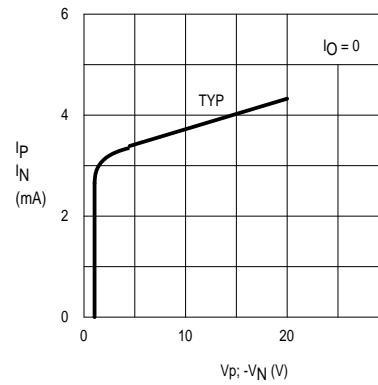
**Input Bias Current**



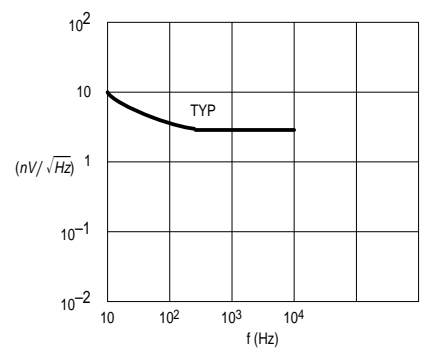
**Input Common-Mode Voltage Range**



**Supply Current per Op Amp**



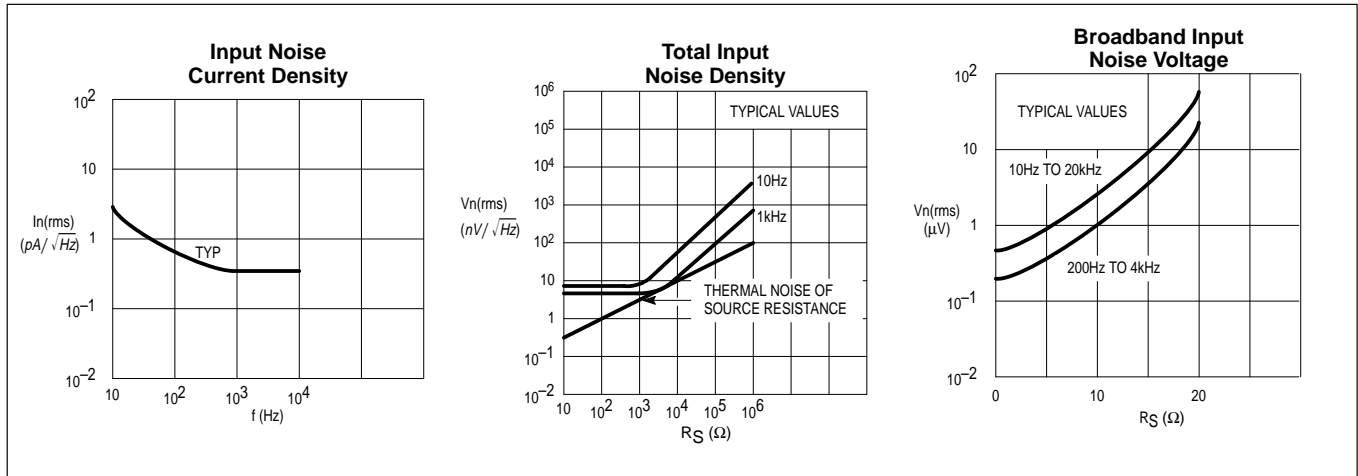
**Input Noise Voltage Density**



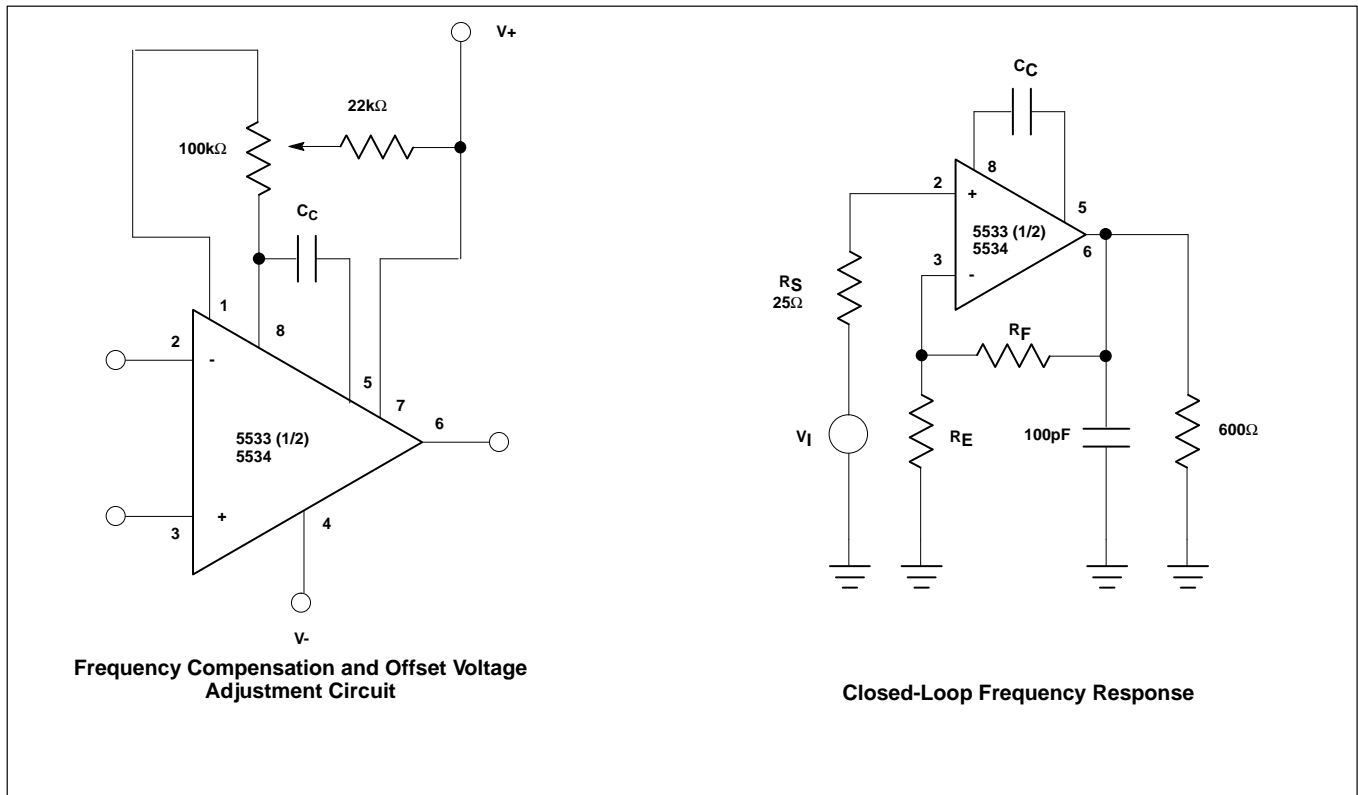
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### TYPICAL PERFORMANCE CHARACTERISTICS (Continued)



### TEST LOAD CIRCUITS



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## NOISE TEST BLOCK DIAGRAM

