

AC12DSMA,AC12FSMA

12 A RESIN INSULATION TYPE TRIAC

DESCRIPTION

The AC12DSMA and AC12FSMA are resin insulation type TRIACs with an effective current of 12 A (Tc = 74°C).

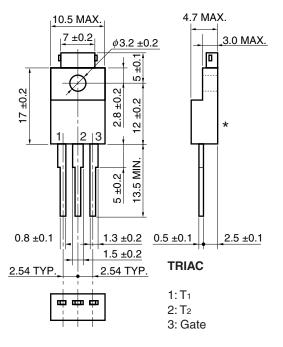
These products are covered with resin mold on the entire case and are electrically insulated with electrodes, giving them a considerable advantage over conventional TRIACs when mounting on a heatsink board or performing high-density mounting.

These products features ratings and electrical characteristics equal to TO-220AB package TRIAC and a high reliability design.

FEATURES

- Insulation type TRIAC fully covered with resin on the entire case other than electrode leads
- Insulation voltage and conduction equal to conventional mica and polyester film
- Can be replaced with TO-220AB package
- High allowable on-current when using a single unit

★ PACKAGE DRAWING (Unit: mm)



*: Tc test bench-mark

Standard weight: 2 g

APPLICATIONS

Non-contact switches of motor speed control, heater temperature control, lamp light control

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MAXIMUM RATINGS

Parameter	Symbol	AC12DSMA	2DSMA AC12FSMA		Remarks
Non-repetitive Peak Off-state Voltage	V _{DSM}	500	700	V	-
Repetitive Peak Off-state Voltage	V_{DRM}	400	600	V	-
Effective On-state Current	I _{T(RMS)}	12 (T c :	= 74°C)	Α	Refer to Figure 11 and 12.
Surge On-state Current	Ітѕм	100 (50 Hz 1 cycle)			Refer to Figure 2.
		110 (60 Hz 1 cycle)			
Fusing Current	∫i⊤²dt	45 (1 ms ≤ t ≤ 10 ms)			-
Critical Rate Rise of On-state Current	dl⊤/dt	50		A/μs	_
Peak Gate Power Dissipation	Р _{GМ}	5.0 (f ≥ 50 Hz, Duty ≤ 10%)		W	-
Average Gate Power Dissipation	P _{G(AV)}	0.5		W	-
Peak Gate Current	Ідм	±3 (f ≥ 50 Hz, Duty ≤ 10%)		Α	-
Junction Temperature	Tj	-40 ∼ +125		°C	-
Storage Temperature	T _{stg}	-55∼+150		°C	_

ELECTRICAL CHARACTERISTICS (Tj = 25°C)

Parameter		Symbol	Conditions		MIN.	TYP.	MAX.	Unit	Remarks
Repetitive Peak Off-state Current		IDRM	V _{DM} = V _{DRM}	T _j = 25°C	_	-	100	μΑ	_
				T _j = 125°C	_	-	2	mA	_
On-state Voltage		Vтм	I _{TM} = 10 A		_	-	1.3	V	Refer to Figure 1.
Gate Trigger Current	Mode I	Іст	V _{DM} = 12 V,	T ₂ +, G+	_	_	20	mA	Refer to Figure 4.
	П		R _L = 30 Ω	T ₂ –, G+	_	-	_		
	III			T2-, G-	_	_	20		
	IV			T ₂ +, G–	_	-	20		
Gate Trigger Voltage	Mode I	V _{GT}	V _{DM} = 12 V,	T ₂ +, G+	_	_	1.5	V	Refer to Figure 4.
	II		R _L = 30 Ω	T ₂ , G+	_	_	_		
	III			T2-, G-	_	_	1.5		
	IV			T ₂ +, G–	_	_	1.5		
Gate Non-trigger Voltage		V _{GD}	$T_j = 125^{\circ}C, V_{DM} = \frac{1}{2} V_{DRM}$		0.3	-	_	V	_
Holding Current		lн	V _{DM} = 24 V, I _{TM} = 10 A		-	30	_	mA	_
Critical Rate Rise of Off-state Voltage		dv/dt	$T_j = 125^{\circ}C, V_{DM} = \frac{2}{3} V_{DRM}$		_	100	_	V/μs	_
Commutating Critical Rate Rise of		(dv/dt)c	T _j = 125°C,		10	-	_	V/μs	-
Off-state Voltage			$(di\tau/dt)c = -6 \text{ A/ms}, V_D = 400 \text{ V}$						
Thermal Resistance Note		Rth(j-c)	Junction-to-case AC		_	_	3.5	°C/W	Refer to Figure 13.

Note The thermal resistance with a 50 Hz or 60 Hz sine wave current, as shown in the following expression:

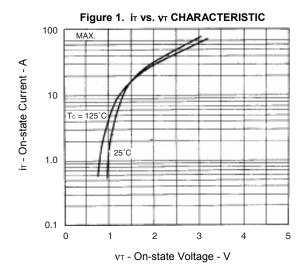
 $R_{th(j-c)} = \frac{T_{j(max)} - T_{c}}{P_{T(AV)}}$ $T_{j(max)}$: Maximum junction temperature

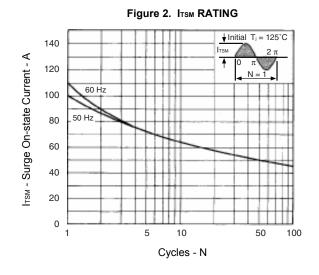
Tc: Case temperature

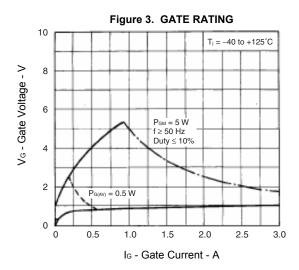
P_{T(AV)}: Average on-dissipation

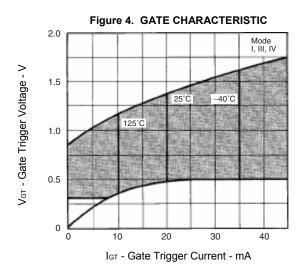


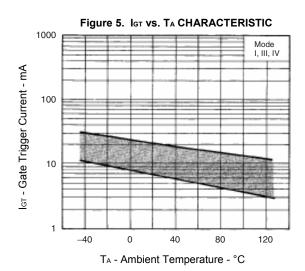
TYPICAL CHARACTERISTICS

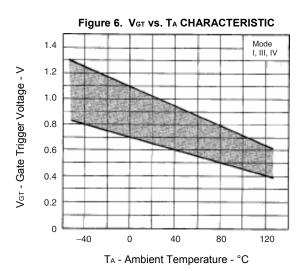


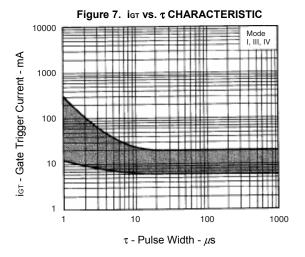


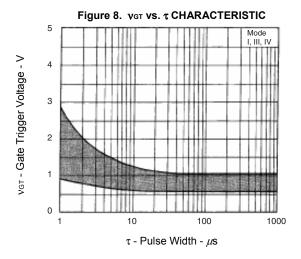


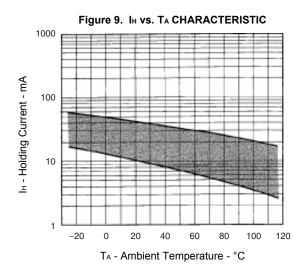


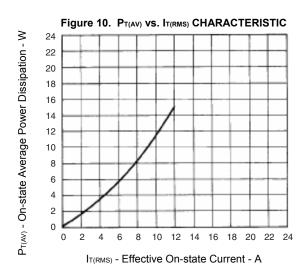


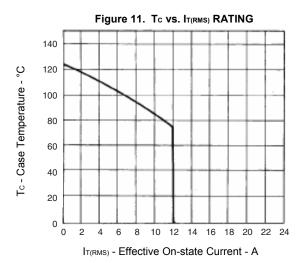


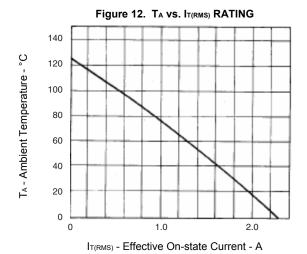


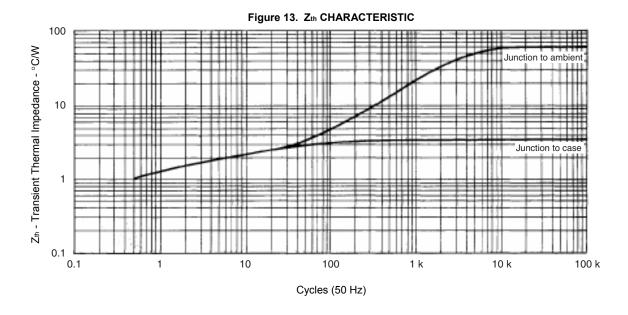












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