6-Channel, Muxed Input Line Inversion LCD Gamma Buffer

ADD8506

FEATURES

Single-supply operation: 3.3 V to 6.5 V
Rail-to-rail input, rail-to-rail output
High output current: 380 mA
Low supply current: 3.9 mA
Stable with 1 nF loads
Wide temperature range: −40°C to +105°C
24-lead, Pb-free, TSSOP package

APPLICATIONS

LCD line inversion gamma references
Car navigation panels
Personal media player panels

GENERAL DESCRIPTION

The ADD8506 has 6-channel LCD gamma reference buffers designed to drive column driver gamma inputs in line inversion panels. Each buffer channel has an A/B input to select between two gamma voltage curves. These buffer channels drive the resistor ladders of LCD column drivers for gamma correction. The ADD8506 outputs have high slew rates and output drives that increase the stability of the reference ladder, resulting in optimal gray scale and visual performance.

The ADD8506 is specified over the −40°C to +105°C temperature range. It is available in a 24-lead thin shrink small outline (TSSOP), surface-mount, Pb-free package.

PIN CONFIGURATION DIAGRAM

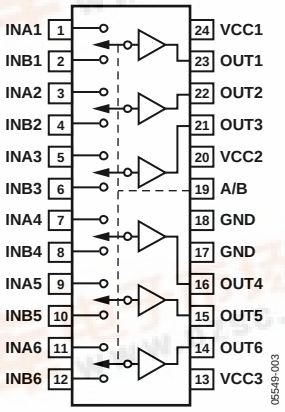

Figure 1.

One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106, U.S.A.
Tel: 781.329.4700 www.analog.com

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REVISION HISTORY

9/05—Revision 0: Initial Version

SPECIFICATIONS

ELECTRICAL CHARACTERISTICS

V_{CC} = 5 V, T_A = 25°C, unless otherwise noted. V_{IN} denotes buffer input voltage; I_{LOAD} denotes load current; R_L denotes load resistance; C_L denotes load capacitance.

Table 1.

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
INPUT CHARACTERISTICS						
Offset Voltage	V_{OS}	0 V ≤ V_{IN} ≤ 5 V			20	mV
Input Common-Mode Voltage Range	V_{CM}		0		5	V
Input Bias Current	I_B	V_{IN} = 2.5 V		2	50	nA
Voltage Gain	A_{VO}		0.985			V/V
OUTPUT CHARACTERISTICS						
Output Voltage High	V_{OH}	I_{LOAD} = +20 mA	4.75			V
Output Voltage Low	V_{OL}	I_{LOAD} = −20 mA			0.2	V
Output Resistance	R_{OUT}	−20 mA ≤ I_{LOAD} ≤ +20 mA; 0.5 V ≤ V_{IN} ≤ 4.5 V		0.20		Ω
Output Short Circuit Current	I_{SC}		120	380		mA
POWER SUPPLY						
Supply Current	I_{SY}	V_{IN} = 2.5 V		3.9	5.1	mA
Supply Voltage Range	V_{CC}		3.3		6.5	V
DYNAMIC PERFORMANCE						
Slew Rate	SR	C_L = 15 pF		7.0		V/µs
		R_L = 250 Ω		6.2		V/µs
Settling Time	t_S	C_L = 200 pF, R_L = 10 kΩ		2.5	6	µs
LOGIC INPUT CHARACTERISTICS						
Input Current Low	I_{IL}	V_{IN} = 0.0 V			100	nA
Input Current High	I_{IH}	V_{IN} = 5.0 V			100	nA
Input Voltage Low	V_{IL}	V_{CC} = 5.0 V, −40°C ≤ T_A ≤ 105°C			0.8	V
Input Voltage Low	V_{IL}	V_{CC} = 3.3 V, −40°C ≤ T_A ≤ 105°C			0.7	V
Input Voltage High	V_{IH}	V_{CC} = 5.0 V, −40°C ≤ T_A ≤ 105°C	1.7			V
Input Voltage High	V_{IH}	V_{CC} = 3.3 V, −40°C ≤ T_A ≤ 105°C	1.4			V

ABSOLUTE MAXIMUM RATINGS

Table 2.

Parameter	Rating
Supply Voltage	7 V
Input Voltage	GND to V_{CC}
Storage Temperature Range	−65°C to +150°C
Junction Temperature Range	−65°C to +150°C
Lead Temperature (Soldering, 60 sec)	300°C

Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

THERMAL RESISTANCE

Table 3. Thermal Package Characteristics

Model	Package Type	θ_{JA}[1]	θ_{JC}[2]	Unit
ADD8506WRUZ	24-Lead Pb-Free TSSOP	128	45	°C/W

[1] θ_{JA} is specified for natural convection on a two-layer board.
[2] θ_{JC} is specified for natural convection on a two-layer board.

ESD PERFORMANCE

Table 4.

Model	HBM[1]	MM[2]	FICDM[3]
ADD8506WRUZ	3.5 kV	200 V	1.0 kV

[1] Human body model.
[2] Machine model.
[3] Field induced charge device model.

ESD CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although this product features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.

TYPICAL PERFORMANCE CHARACTERISTICS

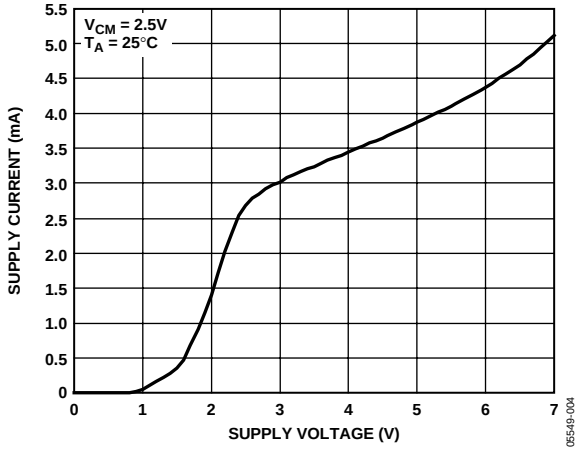

Figure 2. Supply Current vs. Supply Voltage

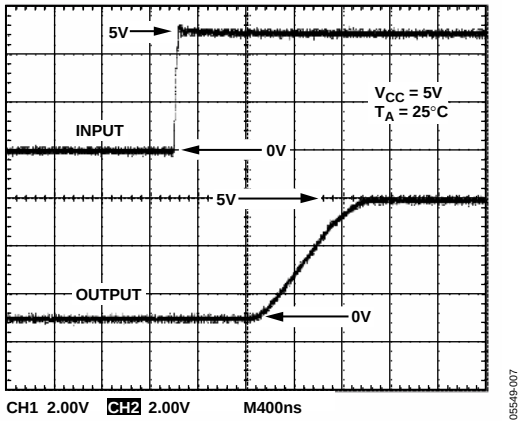

Figure 5. Transient Response—Rising

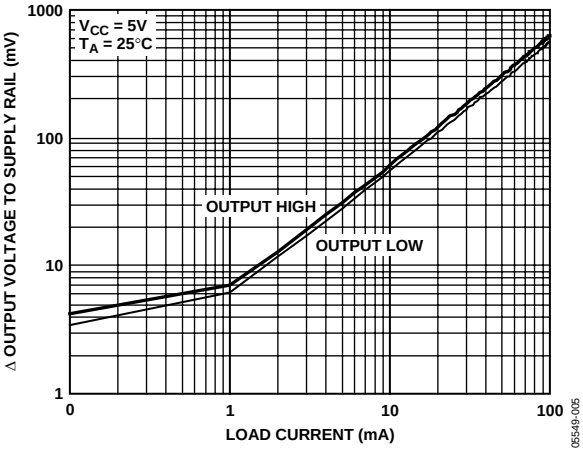

Figure 3. Δ Output Voltage to Supply Rail vs. Load Current

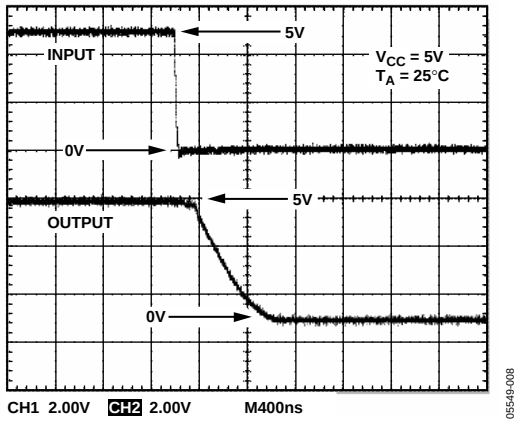

Figure 6. Transient Response—Falling

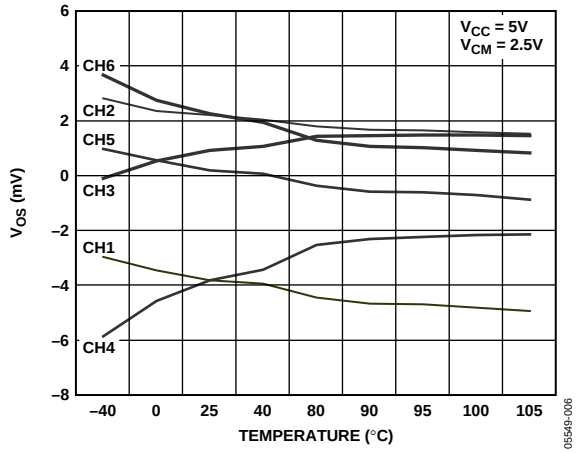

Figure 4. Offset Voltage vs. Temperature

APPLICATIONS

The ADD8506 has CMOS buffers with A/B inputs to select between two different reference voltages set up by an external resistor ladder. Input bias currents are orders of magnitude less than competitive parts. This allows the use of a very large resistor ladder to save supply current.

The buffer outputs are designed to drive resistive or capacitive loads. Therefore, to attain the best display performance, do not use resistors in series with these outputs. Outputs have high slew rates and 6 μs settling times. Each output delivers a minimum of 120 mA, ensuring a fast response to varying loads.

Power supply pins on the ADD8506 have multiple ground (GND) and supply (V_{CC}) connections. Because of the high peak currents that these buffers deliver, it is recommended that all GND and V_{CC} pins be connected and suitably bypassed.

Table 5. MUX Function

A/B Select	Input
Logic High	INAx
Logic Low	INBx

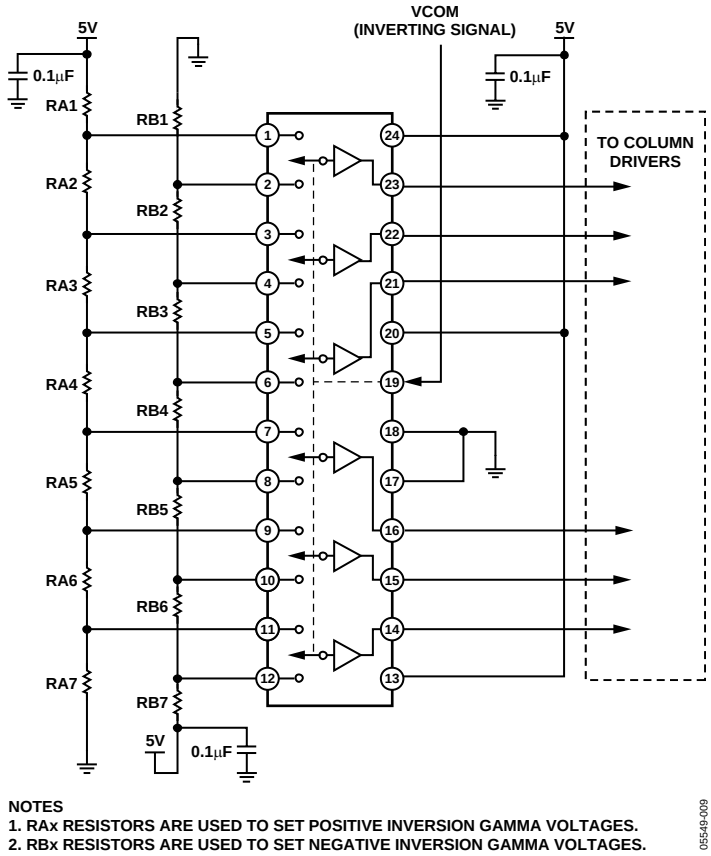

Figure 7. Typical Application

OUTLINE DIMENSIONS

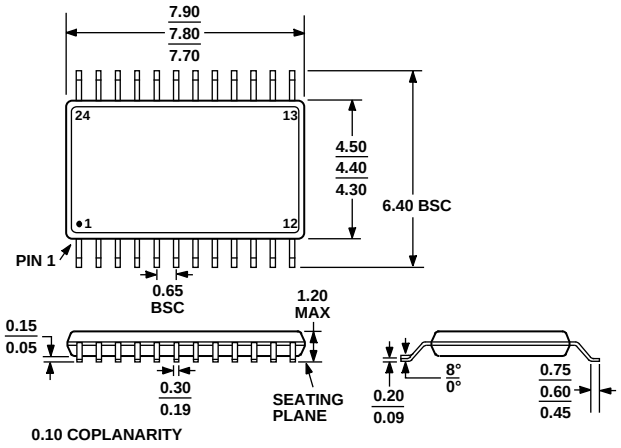

Figure 8. 24-Lead Thin Shrink Small Outline Package [TSSOP]
(RU-24)
Dimensions shown in millimeters

ORDERING GUIDE

Model	Temperature Range	Package Description	Package Option	Ordering Quantity
ADD8506WRUZ[1]	−40°C to +105°C	24-Lead Thin Shrink Small Outline Package [TSSOP], Tube	RU-24	96
ADD8506WRUZ-REEL7[1]	−40°C to +105°C	24-Lead Thin Shrink Small Outline Package [TSSOP], 7″ Reel	RU-24	1,000
ADD8506WRUZ-REEL[1]	−40°C to +105°C	24-Lead Thin Shrink Small Outline Package [TSSOP], 13″ Reel	RU-24	2,500

[1] Z = Pb-free part.

NOTES

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