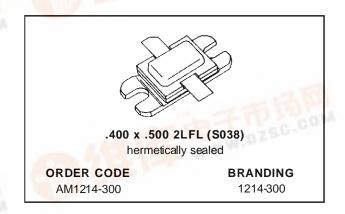


AM1214-300

RF & MICROWAVE TRANSISTORS L-BAND RADAR APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- 5:1 VSWR CAPABILITY
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- Pout = 270 W MIN. WITH 6.3 dB GAIN

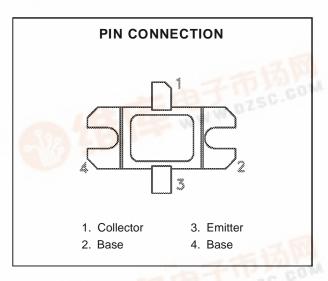


DESCRIPTION

The AM1214-300 device is a high power transistor specifically designed for L-Band radar pulsed output and driver applications.

This device is designed for operation under moderate pulse width and duty cycle pulse conditions and is capable of withstanding 5:1 output VSWR at rated RF conditions. Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.

The AM1214-300 is supplied in the BIGPAC™ Hermetic Metal/Ceramic package with internal Input/Output matching structures.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
Poiss	Power Dissipation* (Tc ≤ 100°C)	730	W
lc	Device Current*	18.75	А
Vcc	Collector-Supply Voltage*	55	V
TJ T	Junction Temperature (Pulsed RF Operation)	250	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

PF∓H(j-c)	Junction-Case Thermal Resistance*	0.24	°C/W

Applies only to rated RF amplifier operation dzsc.com

Destruction 4000

AM1214-300

ELECTRICAL SPECIFICATIONS $(T_{case} = 25^{\circ}C)$

STATIC

Symbol	Test Conditions		Value			11:4	
			Min.	Тур.	Max.	Unit	
BV _{CBO}	$I_C = 50mA$	$I_E = 0mA$		65	_	1	V
BV _{EBO}	I _E = 15mA	$I_C = 0mA$		3.0	_	_	V
BVces	IC = 50mA			65	_	-	V
I _{CES}	V _{CE} = 50V			_		30	mA
h _{FE}	V _{CE} = 5V	$I_C = 5A$		10	_	_	_

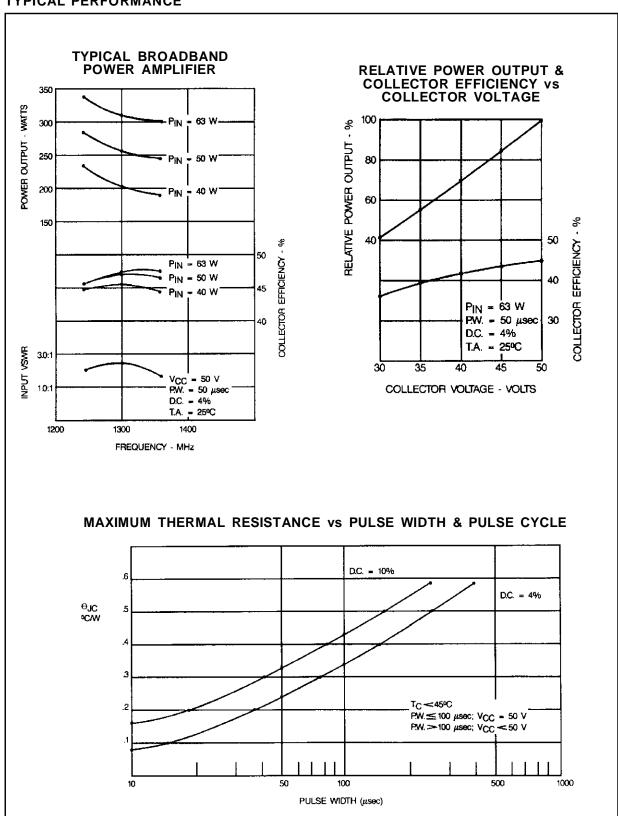
DYNAMIC

Cumbal	-	ant Condition	_	Value		Unit	
Symbol	Test Conditions			Min.	Тур.	Max.	Unit
Pout	f = 1235 — 1365MHz	$P_{IN} = 63W$	$V_{CC} = 50V$	270	300	_	W
ης	f = 1235 — 1365MHz	PIN = 63W	Vcc = 50V	40	45	_	%
G _P	f = 1235 — 1365MHz	P _{IN} = 63W	Vcc = 50V	6.3	6.8	_	dB

Note: Pulse Width = 50μ Sec Duty Cycle = 4%

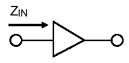
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TYPICAL PERFORMANCE

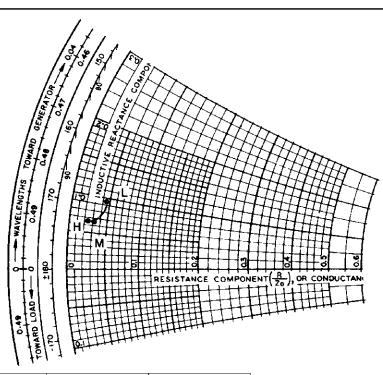


IMPEDANCE DATA

TYPICAL INPUT IMPEDANCE

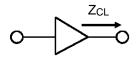


 $\begin{aligned} P_{IN} &= 63 \text{ W} \\ V_{CC} &= 50 \text{ V} \\ Z_{0}^{*} &= 50 \text{ ohms} \end{aligned}$



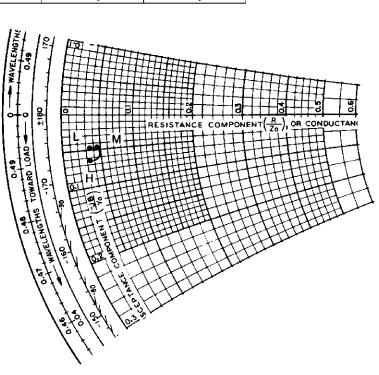
FREQ.	Z _{IN} (Ω)	Z _{CL} (Ω)
L = 1235 MHz	2.5 + j 5.0	2.0 – j 2.5
M = 1300 MHz	1.5 + j 3.5	2.5 – j 2.5
H = 1365 MHz	1.0 + j 3.5	2.0 – j 3.0

TYPICAL COLLECTOR LOAD IMPEDANCE

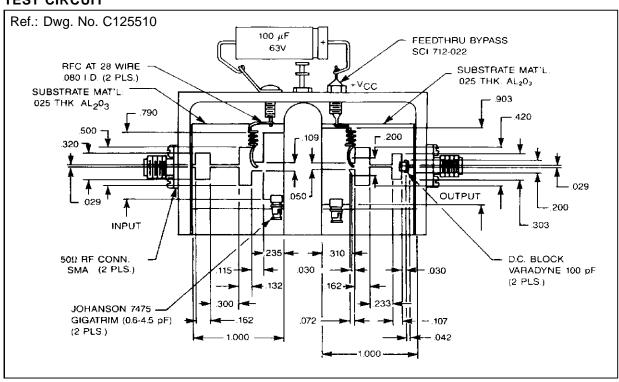


 $\begin{aligned} P_{IN} &= 63 \text{ W} \\ V_{CC} &= 50 \text{ V} \\ Z_0^* &= 50 \text{ ohms} \end{aligned}$

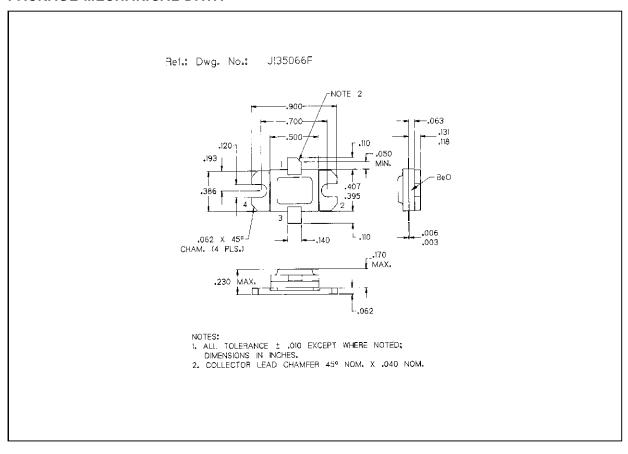
*Normalized Impedance



TEST CIRCUIT



PACKAGE MECHANICAL DATA





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