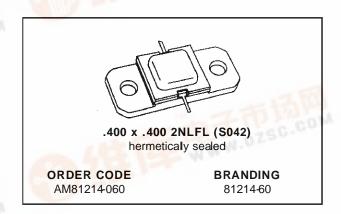


AM81214-060

RF & MICROWAVE TRANSISTORS L-BAND RADAR APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- RUGGEDIZED VSWR ∞:1
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- P_{OUT} = 55 W MIN. WITH 6.6 dB GAIN

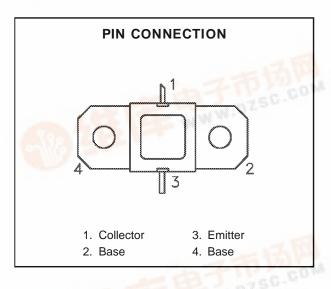


DESCRIPTION

The AM81214-060 device is a high power transistor specifically designed for L-Band radar pulsed output and driver applications.

The device is capable of operation over a wide range of pulse widths, duty cycles, and temperatures and is capable of withstanding ∞:1 output VSWR at rated RF conditions. Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.

The AM81214-060 is supplied in the AMPACTM Hermetic Metal/Ceramic package with internal Input/Output matching structures.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Symbol Parameter		Unit
P _{DISS}	Power Dissipation* (T _C ≤ 100°C)	107	W
Ic	Device Current*	5.0	А
Vcc	Collector-Supply Voltage*	32	V
TJ TJ	Junction Temperature (Pulsed RF Operation)	250	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

L	PRTH(j-c)	Junction-Case Thermal Resistance*	1.4	°C/W

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Applies only to rated RF amplifier operation

AM81214-060

ELECTRICAL SPECIFICATIONS $(T_{case} = 25^{\circ}C)$

STATIC

			Value				
Symbol	Test Conditions		Min.	Тур.	Max.	Unit	
ВУсво	I _C = 20mA	$I_E = 0mA$		55	_	_	V
BV _{EBO}	I _E = 2mA	$I_C = 0mA$		3.5	_		V
BV _{CER}	IC = 40mA	$R_{BE} = 10\Omega$		55	_	_	V
ICES	VBE = 0V	Vce = 28V		_	_	10	mA
h _{FE}	V _{CE} = 5V	I _C = 2A		15	_	150	_

DYNAMIC

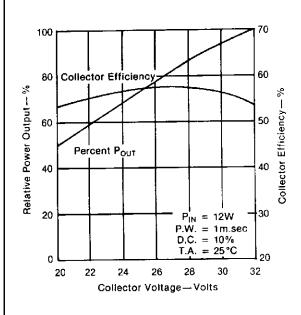
				Value			
Symbol	Test Conditions			Min.	Тур.	Max.	Unit
Pout	f = 1215 — 1400MHz Pı	N = 12W	$V_{CC} = 28V$	55	63		W
ης	f = 1215 — 1400MHz P _I	N = 12W	$V_{CC} = 28V$	50	57	_	%
G _P	f = 1215 — 1400MHz P _I	N = 12W	$V_{CC} = 28V$	6.6	7.2	_	dB

Note: Pulse Width = 1000μ S Duty Cycle = 10%

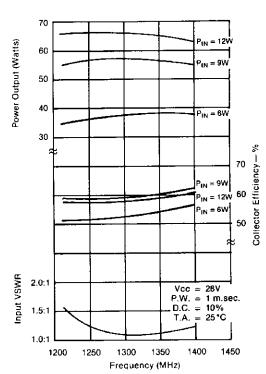
0

TYPICAL PERFORMANCE

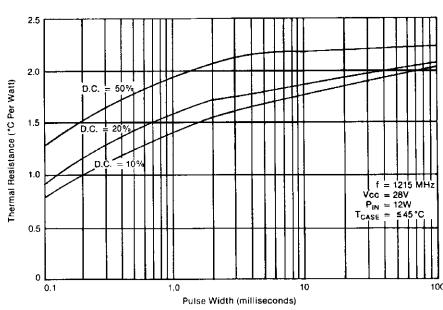




TYPICAL BROADBAND POWER AMPLIFIER

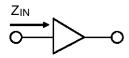


MAXIMUM THERMAL RESISTANCE vs PULSE WIDTH



IMPEDANCE DATA

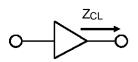
TYPICAL INPUT IMPEDANCE



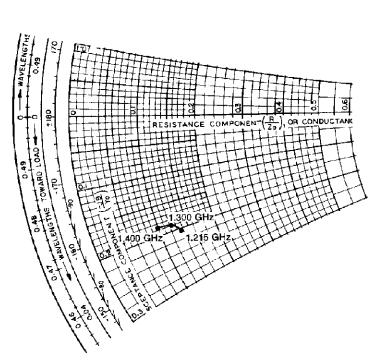
$$\begin{split} P_{IN} &= 12.0 \ W \\ V_{CC} &= 28 \ V \\ Z_0 &= 50 \ ohms \end{split}$$

FREQ.	Z _{IN} (Ω)	$Z_{CL}\left(\Omega\right)$		
L = 1.2 GHz	6.0 + j 10.0	7.0 – j 10.0		
M = 1.3 GHz	4.5 + j 11.0	6.0 – j 9.5		
H = 1.4 GHz	4.0 + j 9.0	5.0 – j 9.0		

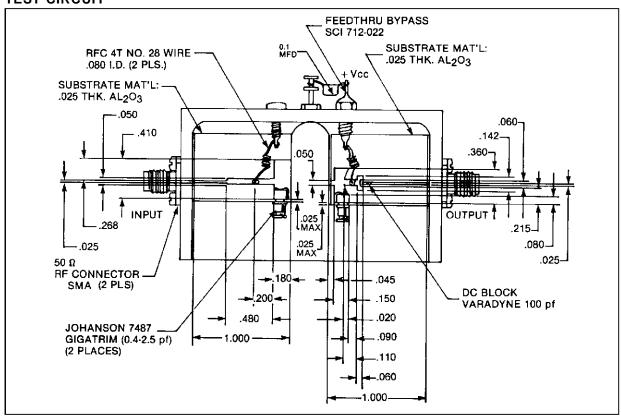
TYPICAL COLLECTOR LOAD IMPEDANCE



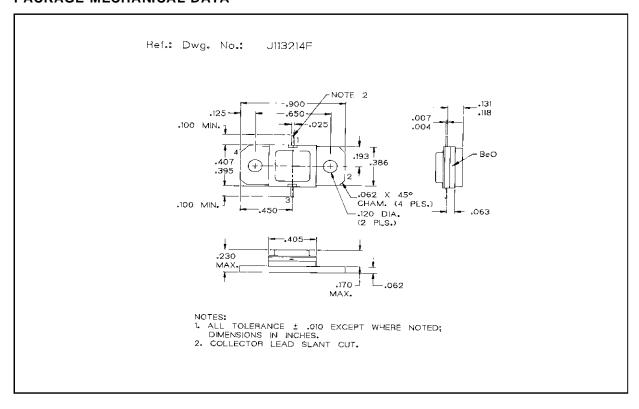
 $\begin{aligned} P_{IN} &= 12.0 \ W \\ V_{CC} &= 28 \ V \\ Z_0 &= 50 \ ohms \end{aligned}$



TEST CIRCUIT



PACKAGE MECHANICAL DATA





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