# **AN7333S**

## 4-Element Graphic Equalizer IC for Radio/Radio Cassette Recorder

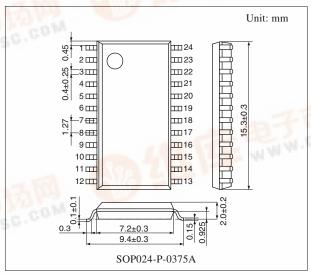
#### ■ Overview

The AN7333S is an integrated circuit for 4element graphic equalizers most suitable for radio cassette / portable component stereo equipment.

2-channel 4-element graphic equalizer can be configured by applying frequency setting capacitors and variable resistors externally. Non-step adjustment of the boost and the cutting quantitiy is possible by variable resistors.

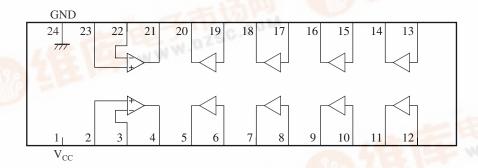
#### ■ Features

- The resonance frequency can be freely set by optional selection of the capacitor capacitance.
- Low distortion rate: THD = 0.04% ( $V_{CC} = 5 \text{ V}$ )
- Wide dynamic range:  $V_0 = 800 \text{ mV} [\text{rms}]$  (at Boost THD = 1%)
- Low noise level:  $V_{no} = 10 \mu V (V_{CC} = 5V)$



Note) The package of this product will be changed to lead-free type (SOP024-P-0375C). See the new package dimensions section later of this datasheet.

### ■ Block Diagram





### ■ Pin Descriptions

Pin No.	Description	Pin No.	Description
1	Power supply	13	Input pin
2	Non inverting input	14	Negative feedback
3	Inverting input	15	Input pin
4	Output	16	Negative feedback
5	Negative feedback	17	Input pin
6	Input pin	18	Negative feedback
7	Negative feedback	19	Input pin
8	Input pin	20	Negative feedback
9	Negative feedback	21	Output
10	Input pin	22	Inverting input
11	Negative feedback	23	Non inverting input
12	Input pin	24	GND

### ■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	14.4	V	
Supply current	I <sub>CC</sub>	30	mA	
Power dissipation	$P_{\mathrm{D}}$	432	mW	
Operating ambient temperature *1	$T_{\mathrm{opr}}$	-20 to +75	°C	
Storage temperature *1	T <sub>stg</sub>	-55 to +125	°C	

Note) \*1: Except for the operating ambient temperature and storage temperature, all ratings are for  $T_a = 25$  °C.

### ■ Recommended Operating Range

Parameter	Symbol	Range	Unit	
Supply voltage	$V_{CC1}, V_{CC2}$	4 to 14	V	

## $\blacksquare$ Electrical Characteristics at $V_{CC}$ = 5 $V,\,T_a$ = 25°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Total circuit current	I <sub>tot</sub>	$V_{IN} = 0 \text{ mV}$	_	7.5	_	mA
Voltage gain	$G_{V}$	$f = 1 \text{ kHz}, V_O = -10 \text{ dBV}$	_	-1.5	_	dB
Boost quantity	Boost	$V_O = -20 \text{ dBV}$ is set to $0 \text{ dBV}$	_	9.5	_	dB
Cut quantity	Cut	$V_O = -20 \text{ dBV}$ is set to $0 \text{ dBV}$	_	-9.5	_	dB
Total harmonic distortion	THD	$f = 1 \text{ kHz}, V_O = -20 \text{ dBV}$	_	0.04	_	%
Output noise voltage	V <sub>no</sub>	$R_g = 0 \Omega$ , Total Flat, DIN/AUDIO	_	10	_	μV
Crosstalk	CT	$f = 1 \text{ kHz}, R_g = 0 \Omega$ , Total Flat, $V_O = -20 \text{ dBV}$	_	12	_	μV

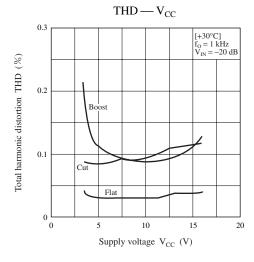
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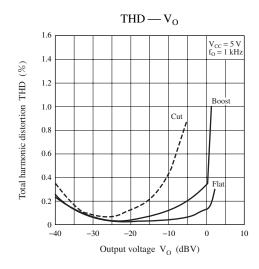
### ■ Terminal Equivalent Circuits

Pin No.	Equivalent circuit	Description	Pin voltage (V)	
1	<del>-</del>	Power supply: Supply pin	5	
2 23	In ○────────────────────────────────────	Non inverting input: Output buffer circuit non-inverting input pin	$\frac{1}{2}$ V <sub>CC</sub>	
3 22		Inverting input: Output buffer circuit inverting input pin	Center electric potential	
4 21	Out  V <sub>CC</sub>	Output: Output buffer output pin	Center electric potential	
5, 7, 9, 11, 14, 16, 18, 20	22 V <sub>CC</sub>	Negative feedback: Resonance circuit negative feedback pin	Center electric potential	
6, 8, 10, 12, 13, 15, 17, 19		Input pin: Resonance circuit input pin	Center electric potential	
24	_	GND pin:	0	

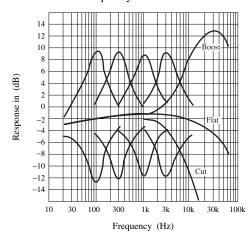
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#### ■ Main Characteristics



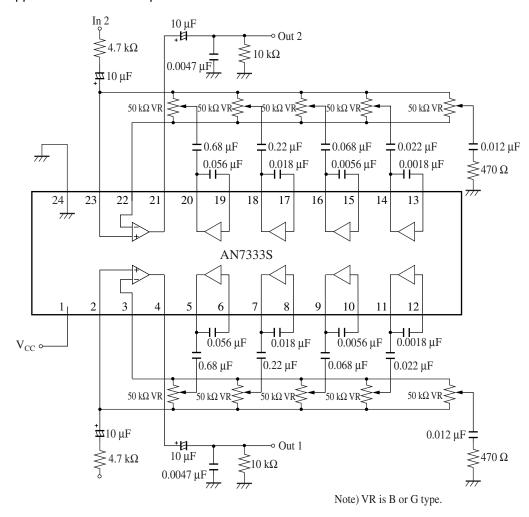


### Frequency characteristics

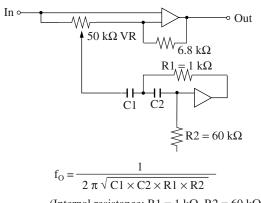


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### ■ Application Circuit Example



#### • Resonant frequency fo



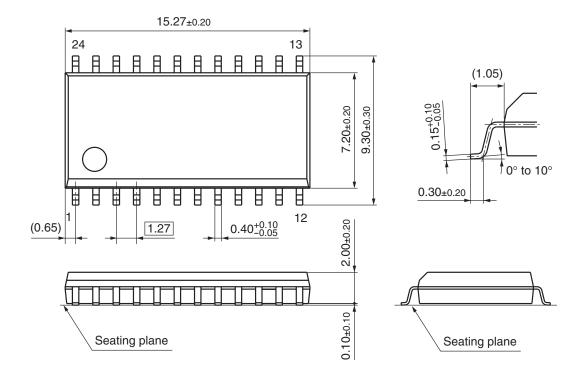
(Internal resistance: R1 = 1 k $\Omega$ , R2 = 60 k $\Omega$ )

<sup>\*</sup> Internal resistance: As R1 and R2 of internal resistors are common in each oscillation circuit, f<sub>O</sub> can be adjustable from an external capacitor.

<sup>\*</sup> Gain can be adjusted by the 50 k $\Omega$  variable resistor.

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- New Package Dimensions (Unit: mm)
- SOP024-P-0375C (Lead-free package)



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