

ICs for Cassette, Cassette Deck

Panasonic

AN7367K

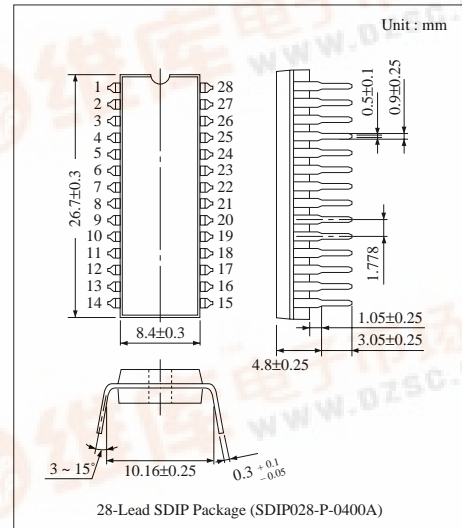
dbx II Noise Reduction IC for Cassette Deck

Overview

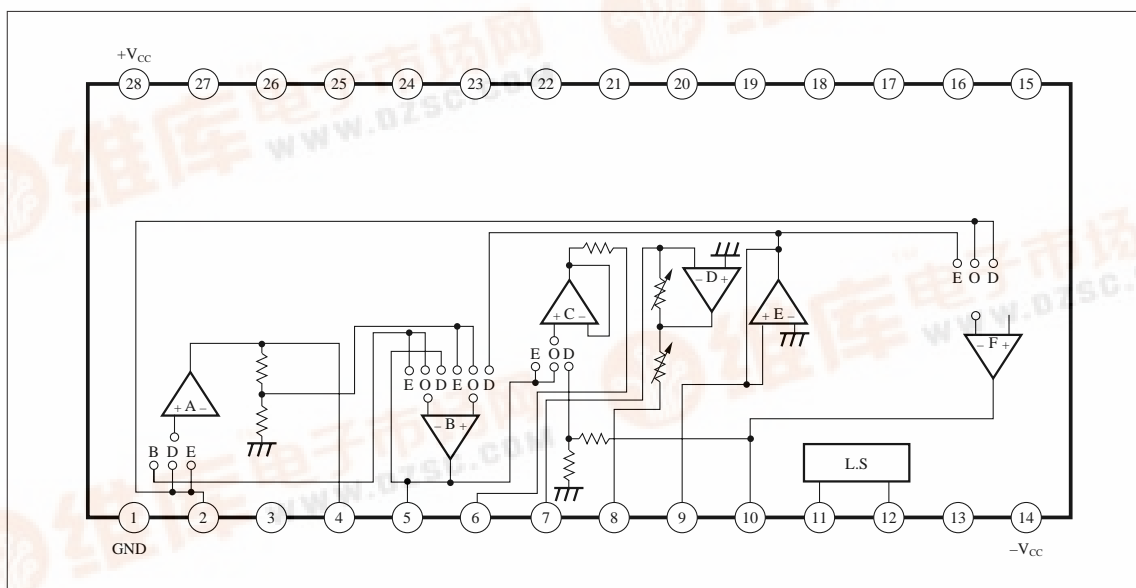
The AN7367K is an integrated circuit available for stereo operation on a single chip developed for dbx II type noise reduction, which can also use for U.S. sound multiplex TV. And it is designed for easy use of reference signal level, supply voltage and logic input level.

Features

- Dynamic range : 110dB
- 2-channel
- Reference input-output signal level : 90mV
- 2 power supply type (+, -)



Block Diagram



■ Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply Voltage	V _{CC}	±7.2	V
Supply Current	I _{CC}	30	mA
Power Dissipation (Ta=75°C)	P _D	450	mW
Operating Ambient Temperature	T _{opr}	-20 ~ +75	°C
Storage Temperature	T _{stg}	-55 ~ +150	°C

■ Recommended Operating Range (Ta=25°C)

Parameter	Symbol	Range
Operating Supply Voltage Range	V _{CC}	±5V ~ ±7V

■ Electrical Characteristics (V_{CC}= ±6V, Ta=25°C)

Parameter	Symbol	Condition	min.	typ.	max.	Unit
Encode Output Voltage	V _{OE}	V _{in} = 1kHz, 90mV (Reference Level)	67.5	85	107	mV
Encode Linearity -1 *1	L _{E-1}	V _{in} = 1kHz, 900mV (Encode)	8	10	12	dB
Encode Linearity -2 *1	L _{E-2}	V _{in} = 1kHz, 0.9mV (Encode)	-22	-20	-18	dB
Decode Output Voltage	V _{OD}	V _{in} = 1kHz, 90mV (Reference Level)	67.5	85	107	mV
Decode Linearity -1 *2	L _{D-1}	V _{in} = 1kHz, 285mV (Decode)	17	20	23	dB
Decode Linearity -2 *2	L _{D-2}	V _{in} = 1kHz, 9mV (Decode)	-43	-40	-37	dB
Encode Max. Output Voltage	V _{OME}	THD= 3%, f= 1kHz	300	—	—	mV
Decode Max. Output Voltage	V _{OMD}	THD= 3%, f= 1kHz	1100	—	—	mV
Encode Total Harmonic Distortion	THD _E	At Encode, 400Hz ~ 30kHz, f= 1kHz, 90mV	—	0.35	0.45	%
Decode Total Harmonic Distortion	THD _D	At Decode, 400Hz ~ 30kHz, f= 1kHz, 90mV	—	0.1	0.3	%
Encode Noise Output Voltage	V _{NOE}	Input short, DIN/AUDIO	—	-62	-58	dBV
Decode Noise Output Voltage	V _{NOD}	Input short, DIN/AUDIO	—	-96	-92	dBV
Encode Output Difference between Channels	CD _E	V _{in} = 1kHz, 90mV	-1.3	0	1.3	dB
Decode Output Difference between Channels	CD _D	V _{in} = 1kHz, 90mV	-2	0	2	dB
Encode Crosstalk between Channels	CT _E	At Encode *3, V _{in} = 1kHz, 900mV	—	-52	-45	dBV
Decode Crosstalk between Channels	CT _D	At Decode, V _{in} = 1kHz, 90mV	—	-91	-85	dBV
Monitor Output Voltage	V _{OM}	At Encode, V _{in} = 1kHz, 90mV	75	90	107	mV
Monitor Output Voltage Output Difference between Channels	CD _M	At Encode, V _{in} = 1kHz, 90mV	-1.5	0	1.5	dB
Monitor Output Noise Output Voltage	V _{NOM}	Encode input, Short DIN/AUDIO	—	-110	-106	dBV
Monitor Output Total Harmonic Distortion	THD _M	At Encode, V _{in} = 1kHz, 90mV	—	0.01	0.1	%
dbx OFF Line Output Voltage	V _{LOFF}	V _{in} = 1kHz, 180mV	151	180	214	mV
dbx OFF Rec. Output Voltage	V _{ROFF}	V _{in} = 1kHz, 180mV	151	180	214	mV
dbx OFF line Output Total Harmonic Distortion	THD _{LOFF}	V _{in} = 1kHz, 180mV	—	0.01	0.1	%
dbx OFF Rec Output Total Harmonic Distortion	THD _{ROFF}	V _{in} = 1kHz, 180mV	—	0.01	0.1	%
dbx OFF Line Output Noise Output Voltage	V _{NLOFF}	Input Short, DIN/AUDIO	—	-110	-104	dBV
dbx OFF Rec. Output Noise Output Voltage	V _{NROFF}	Input Short, DIN/AUDIO	—	-110	104	dBV
dbx OFF Line Output Output Difference between Channels	CD _{LOFF}	V _{in} = 1kHz, 180mV	-1.5	0	1.5	dB
dbx OFF Rec. Output Output Difference between Channels	CD _{ROFF}	V _{in} = 1kHz, 180mV	-1.5	0	1.5	dB
Input Impedance	Z _{IN}	V _{in} = 1kHz, 90mV	500	—	—	kΩ
Total Circuit Current	I _{tot}	Input Short at Encode	—	12	16	mA

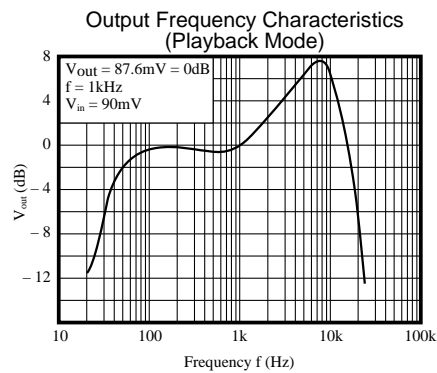
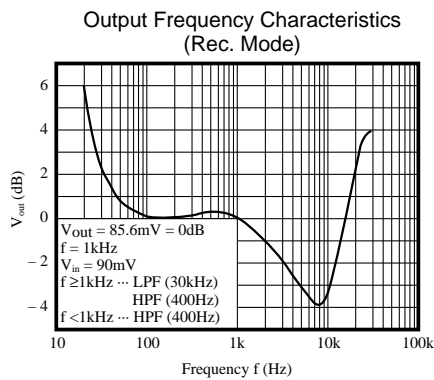
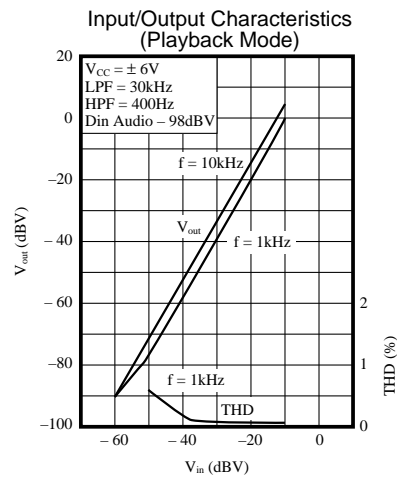
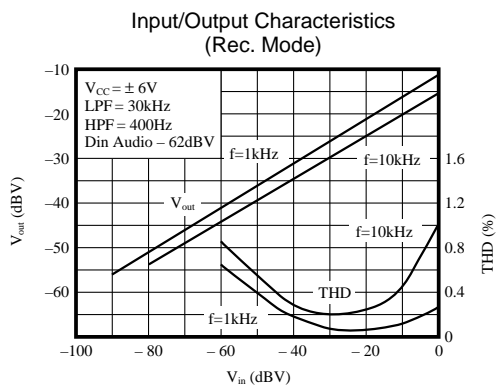
*1 Level Ratio to V_{OE}*2 Level Ratio to V_{OD}

*3 Measure output leak by short of measuring channel input and input to reverse channel.

■ Electrical Characteristics (Cont.) ($V_{CC} = \pm 6V$, $T_a = 25^\circ C$)

Parameter	Symbol	Condition	min.	typ.	max.	Unit
dbx ON/OFF Control dbx ON	V_{15ON}	Satisfy specifications of the preceding page $V_{OE} \sim V_{NLOFF}$	3.2	—	6	V
dbx ON/OFF Control dbxOFF	V_{15OFF}	Satisfy specifications of the preceding page $V_{OE} \sim V_{NLOFF}$	$-V_{EE}$ $+ 0.4V$	—	1.8	V
Encode/Decode Control Encode	V_{16EN}	Satisfy specifications of the preceding page $V_{OE} \sim V_{NLOFF}$	3.2	—	6	V
Encode/Decode Control Decode	V_{16DE}	Satisfy specifications of the preceding page $V_{OE} \sim V_{NLOFF}$	$-V_{EE}$ $+ 0.4V$	—	1.8	V
dbx ON/OFF Control "L" Overflow Current	I_{15L}	$V_{15} = 1.8V$	-2.1	—	-0.2	μA
Encode/Decode Control "L" Overflow Current	I_{16L}	$V_{16} = 1.8V$	-2.1	—	-0.2	μA
Pulse Noise at Encode	V_{NPE}	Input Short, Time : a second, number : one	—	—	13	mV _{P-P}


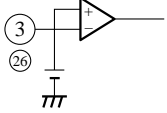
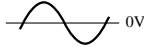
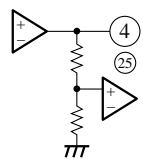
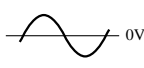
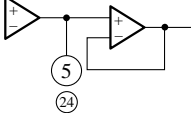

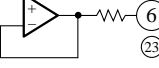

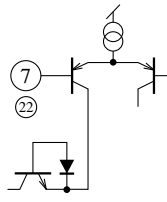

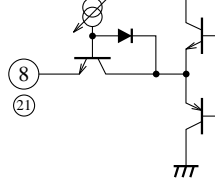

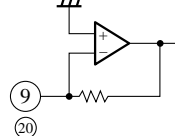
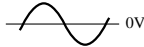
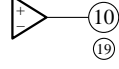
■ Characteristics Curve



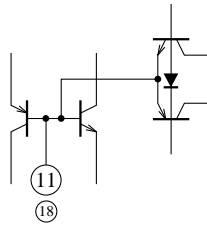
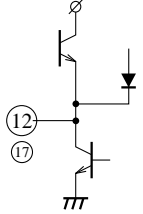
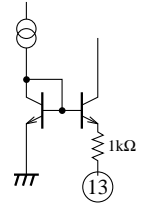
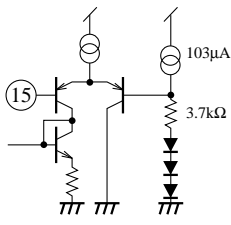
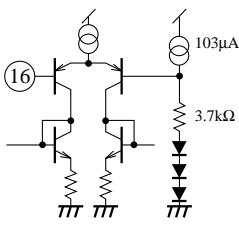
■ Pin Description

Pin No.	Pin Name	Typ. Waveform	Description	Equivalent Circuit
2 27	Recording Signal Input		Pin for inputting signal	

■ Pin Description (Cont.)

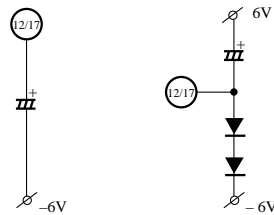
Pin No.	Pin Name	Typ. Waveform	Description	Equivalent Circuit
3 · 26	Emphasis		Emphasis amp. – side input pin	
4 · 25	Line Amp. Output		Line amp. output pin	
5 · 24	Emphasis Amp. Output		Emphasis amp. output pin	
6 · 23	Buffer Output		Buffer output pin	
7 · 22	CCA Input		CCA input pin	
8 · 21	CCA Output		CCA output pin	
9 · 20	CCA Amp. Input		CCA amp. input pin	
10 · 19	Rec. Out Output		Rec. out output pin	

■ Pin Description (Cont.)

Pin No.	Pin Name	Typ. Waveform	Description	Equivalent Circuit
11 · 18	Level Sensor Input	-5.2V	Level sensor input pin	
12 · 17	Timing Capacitor	-5.3V	Level sensor smoothing pin	
13	Timing Current Adjusting Pin	_____	Adjust the timing current by external variable resistance.	
15	dbx ON/OFF Switching	_____	High- dbx ON Low- dbx OFF	
16	Encode/ Decode Switching	_____	High - Encode Low - Decode	
1	GND	_____	_____	_____
14	(-) Power Supply	_____	_____	_____
28	(+) Power Supply	_____	_____	_____

■ Precautions on Use

- 1) For the emphasis, RMS filter, and waiting C, R, use those resistors and capacitors which satisfy the following error ranges. (R ; $\pm 2\%$, C ; $\pm 5\%$)
- 2) For the timing capacitor, use a suitable one whose error is within $\pm 5\%$ and whose $\tan \delta$ is small enough. The selected timing capacitor must also has excellent temperature characteristics.
- 3) Adjust the timing adjustment pin so that the level on both $1k\Omega$ ends becomes 18.4mV.
- 4) If shortening of the power ON rise time is necessary, connect the timing capacitor as shown in the sketch below.



Note) Remember to make the power impedance low enough.

- 5) Encode THD

The product specification is based on the data confirmed by use of a band-pass filter (400Hz ~ 30kHz).

The distortion rate in the secondary to quintic higher harmonics measured by a spectrum analyzer is about 0.16%.

- 6) When users use the AN7367K, contract should be made between the dbx Technology Licensing. dbx license and trademark should be contacted with the following.

the U.S.A. : dbx Technology Licensing
 433 California Street. Third Floor
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