

■ Absolute Maximum Rating (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply voltage	V _{CC}	− 0.3 to + 7.0	V
Supply current	I _{CC}	500	mA
Power dissipation ^{Note)}	P _D	2250	mW
Operating ambient temperature	T _{opr}	−20 to + 60	°C
Storage temperature	T _{stg}	−55 to + 150	°C

Note) Ta=60°C and printed board (80.0 × 80.0 × 1.2mm)

■ Recommended Operation Range (Ta=25°C)

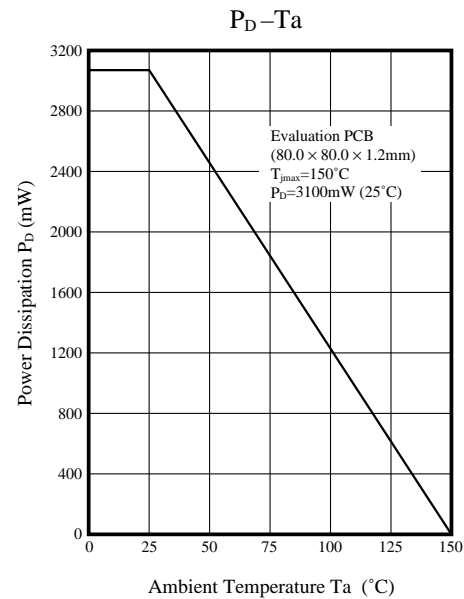
Parameter	Symbol	Range
Operating supply voltage	V _{CC}	4.0V to 5.5V

■ Electrical Characteristics (V_{CC}=5.0V, Ta=25±2°C)

Parameter	Symbol	Condition	min	typ	max	Unit
Supply current 1	I _{CC1}	STANDBY= “L” All SGLINE=open	—	44	54	mA
Supply current 2	I _{CC2}	STANDBY= “L” All SGLINE=0.2V	—	458	530	mA
Supply current 3	I _{CC3}	STANDBY=open	—	100	200	μA
STANDBY terminal “L” input current	I _{STBYL}	V _{CC} =5.5V, V _I =0V	−150	−50	—	μA
STANDBY terminal “L” input current	I _{STBYH}	V _{CC} =5.5V, V _I =5.5V	—	—	1	μA
STANDBY terminal positive direction Threshold voltage	V _{TH+}		1.3	1.5	1.7	V
STANDBY terminal negative direction Threshold voltage	V _{TH−}		1.1	1.3	1.5	V
Maximum output current SGLINE1-18	I _{SG}	V _{SG} =0.2V	19.8	23.0	26.2	mA
Output leak current 1 SGLINE1 to 18	I _{LK1}	V _{CC} =5.5V STANDBY=open, V _{SG} =0V	−1	—	—	μA
Output leak current 2 SGLINE1 to 18	I _{LK2}	V _{CC} =5.5V STANDBY=open, V _{SG} =2.85V	−1	—	1	μA
REG · output voltage	V _{REG1}	V _{CC} =4.0 to 5.5V All SGLINE=open	2.79	2.85	2.91	V
REG · output voltage	V _{REG2}	V _{CC} =3.2V All SGLINE=open	2.00	—	—	V
Termination resistance value SGLINE1 to 18	R _{SG}	I _{SG} =5mA to 15mA	107	115	123	Ω
“H” level output voltage SGLINE1 to 18	V _{SGH}	V _{CC} =4.0 to 5.5V All SGLINE=open	2.78	2.85	2.92	V
STANDBY terminal hysteresis width	V _{HYS}		—	(200)	—	mV
Output terminal capacitance	C _{SG}		—	(4.5)	8	pF
Thermal shut-down temperature	T _{TSD}		—	(170)	—	°C

Note) Number in () is a design reference value but not guaranteed one.

■ Package Power Dissipation



■ Pin Name

Pin No.	Pin name	Pin No.	Pin name
1	REGOUT	13	V_{CC} (TERMPWR) <small>Note 2)</small>
2	SGLINE14	14	SGLINE5
3	SGLINE13	15	SGLINE4
4	SGLINE12	16	SGLINE3
5	SGLINE11	17	SGLINE2
6	GND <small>Note 1)</small>	18	SGLINE1
7	SGLINE10	19	GND <small>Note 4)</small>
8	SGLINE9	20	SGLINE18
9	SGLINE8	21	SGLINE17
10	SGLINE7	22	SGLINE16
11	SGLINE6	23	SGLINE15
12	V_{CC} (TERMPWR) <small>Note 2)</small>	24	STANDBY <small>Note 3)</small>

Note 1) On PCB, the same wide pattern as Pin6 GND terminal and Pin19GND terminal (One layer one surface GND of multi-layer board is recommended for PCB.)

Note 2) V_{CC} should be connected with SCSI termination resistive power supply TERMPWR.

Note 3) STANDBY terminal input voltage : $V_I < V_{TH-}$ for active mode and $V_I > V_{TH+}$ for stand-by mode Opening the STANDBY terminal also brings the stand-by mode.

Pin Descriptions

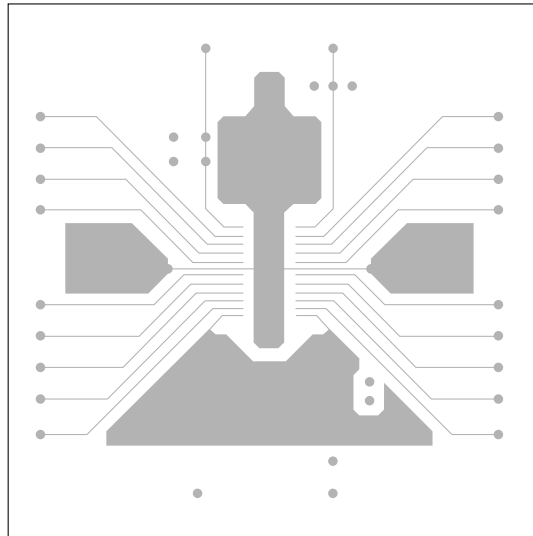
Pin No.	Pin name	Equivalent circuit
24	STANDBY	
2, 3 4, 5 7, 8 9, 10 11, 14 15, 16 17, 18 20, 21 22, 23	SGLINE 1 SGLINE 18	
12, 13	VCC	
1	REGOUT	

■ Precautions on Input Capacitance

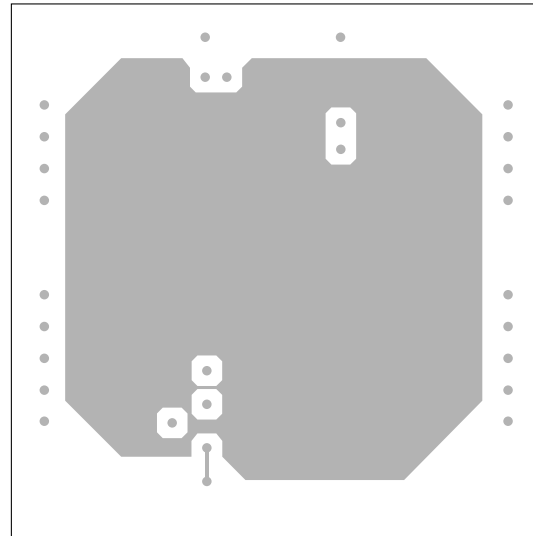
The Interface Terminal Input Capacitance MAX25pF Standard are added from the SCSI II. This is because large stamp input capacitance may prevent the normal signal transmission. The capacitance value which does not cause the transmission error differs, depending on the connection conditions of SCSI equipment (such as distance between equipment, number of equipment units and cable impedance), so, its proper value for the AN8612NSR can not be identified. However, when the input capacitance value specified in SCSIII Standard, MAX25pF is used, transmission error due to input capacitance can hardly occur.

The input capacitance value is determined by the sum of the following capacitances : active terminator (ex. 4.5 pF), printed board (ex. 10 pF), driver receiver incorporated SCSI controller (ex. 10 pF) and connector (ex. 0.5 pF).

[Evaluation Printed Board]



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■ Application Circuit

