

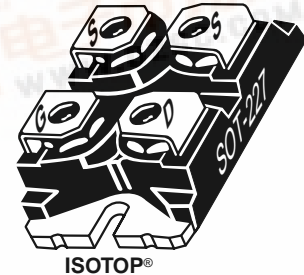


# APT20M11JVFR

200V 175A 0.011Ω

## POWER MOS V®

FREDFET



Power MOS V® is a new generation of high voltage N-Channel enhancement mode power MOSFETs. This new technology minimizes the JFET effect, increases packing density and reduces the on-resistance. Power MOS V® also achieves faster switching speeds through optimized gate layout.

- Fast Recovery Body Diode
- Lower Leakage
- Faster Switching
- 100% Avalanche Tested
- Popular SOT-227 Package



### MAXIMUM RATINGS

All Ratings:  $T_C = 25^\circ\text{C}$  unless otherwise specified.

Symbol	Parameter	APT20M11JVFR	UNIT
$V_{DSS}$	Drain-Source Voltage	200	Volts
$I_D$	Continuous Drain Current @ $T_C = 25^\circ\text{C}$	175	Amps
$I_{DM}$	Pulsed Drain Current <sup>①</sup>	700	
$V_{GS}$	Gate-Source Voltage Continuous	$\pm 30$	Volts
$V_{GSM}$	Gate-Source Voltage Transient	$\pm 40$	
$P_D$	Total Power Dissipation @ $T_C = 25^\circ\text{C}$	700	Watts
	Linear Derating Factor	5.6	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to 150	$^\circ\text{C}$
$T_L$	Lead Temperature: 0.063" from Case for 10 Sec.	300	
$I_{AR}$	Avalanche Current <sup>①</sup> (Repetitive and Non-Repetitive)	175	Amps
$E_{AR}$	Repetitive Avalanche Energy <sup>①</sup>	50	mJ
$E_{AS}$	Single Pulse Avalanche Energy <sup>④</sup>	3600	

### STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
$BV_{DSS}$	Drain-Source Breakdown Voltage ( $V_{GS} = 0V, I_D = 250\mu\text{A}$ )	200			Volts
$I_{D(on)}$	On State Drain Current <sup>②</sup> ( $V_{DS} > I_{D(on)} \times R_{DS(on)}$ Max, $V_{GS} = 10V$ )	175			Amps
$R_{DS(on)}$	Drain-Source On-State Resistance <sup>②</sup> ( $V_{GS} = 10V, 0.5 I_{D(Cont.)}$ )			0.011	Ohms
$I_{DSS}$	Zero Gate Voltage Drain Current ( $V_{DS} = V_{DSS}, V_{GS} = 0V$ )			250	$\mu\text{A}$
	Zero Gate Voltage Drain Current ( $V_{DS} = 0.8 V_{DSS}, V_{GS} = 0V, T_C = 125^\circ\text{C}$ )			1000	
$I_{GSS}$	Gate-Source Leakage Current ( $V_{GS} = \pm 30V, V_{DS} = 0V$ )			$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage ( $V_{DS} = V_{GS}, I_D = 5mA$ )	2		4	Volts

**CAUTION:** These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

APT Website - <http://www.advancedpower.com>

**DYNAMIC CHARACTERISTICS**

**APT20M11JVFR**

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V V <sub>DS</sub> = 25V f = 1 MHz		18000	21600	pF
C <sub>oss</sub>	Output Capacitance			4100	5740	
C <sub>riss</sub>	Reverse Transfer Capacitance			1350	2025	
Q <sub>g</sub>	Total Gate Charge ③	V <sub>GS</sub> = 10V V <sub>DD</sub> = 0.5 V <sub>DSS</sub> I <sub>D</sub> = I <sub>D</sub> [Cont.] @ 25°C		690	1035	nC
Q <sub>gs</sub>	Gate-Source Charge			95	140	
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge			290	435	
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>GS</sub> = 15V V <sub>DD</sub> = 0.5 V <sub>DSS</sub> I <sub>D</sub> = I <sub>D</sub> [Cont.] @ 25°C R <sub>G</sub> = 0.6Ω		20	40	ns
t <sub>r</sub>	Rise Time			40	80	
t <sub>d(off)</sub>	Turn-off Delay Time			75	115	
t <sub>f</sub>	Fall Time			10	20	

**SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS**

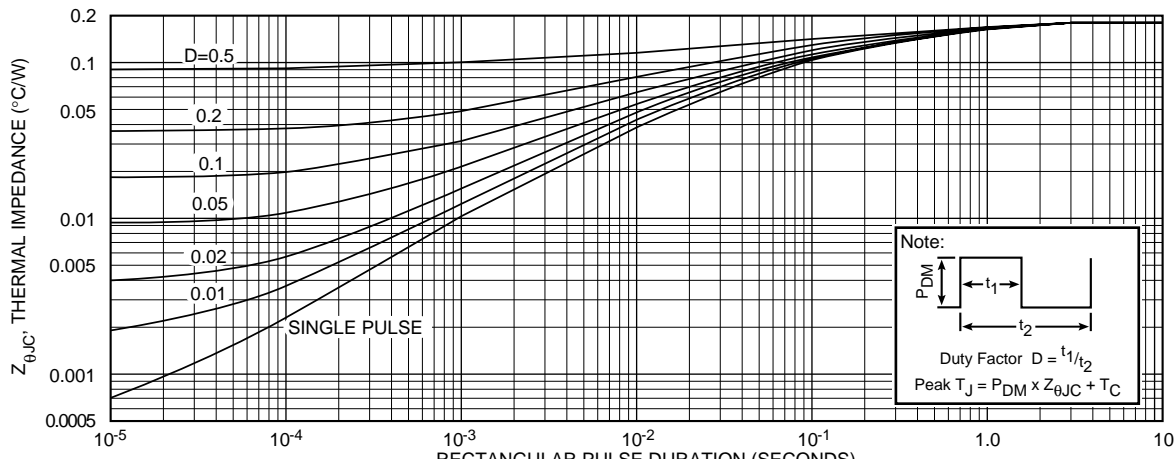
Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
I <sub>S</sub>	Continuous Source Current (Body Diode)			175	Amps
I <sub>SM</sub>	Pulsed Source Current ① (Body Diode)			700	
V <sub>SD</sub>	Diode Forward Voltage ② (V <sub>GS</sub> = 0V, I <sub>S</sub> = -I <sub>D</sub> [Cont.])			1.3	Volts
dv/dt	Peak Diode Recovery dv/dt ⑤			5	V/ns
t <sub>rr</sub>	Reverse Recovery Time (I <sub>S</sub> = -I <sub>D</sub> [Cont.], di/dt = 100A/μs)	T <sub>j</sub> = 25°C	150	250	ns
		T <sub>j</sub> = 125°C	250	500	
Q <sub>rr</sub>	Reverse Recovery Charge (I <sub>S</sub> = -I <sub>D</sub> [Cont.], di/dt = 100A/μs)	T <sub>j</sub> = 25°C	0.9		μC
		T <sub>j</sub> = 125°C	2.5		
I <sub>RRM</sub>	Peak Recovery Current (I <sub>S</sub> = -I <sub>D</sub> [Cont.], di/dt = 100A/μs)	T <sub>j</sub> = 25°C	12		Amps
		T <sub>j</sub> = 125°C	20		

**THERMAL/PACKAGE CHARACTERISTICS**

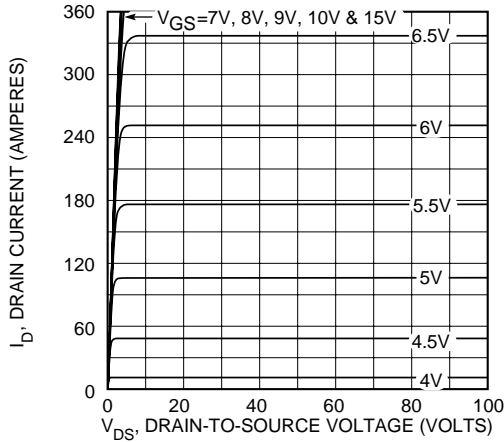
Symbol	Characteristic	MIN	TYP	MAX	UNIT
R <sub>θJC</sub>	Junction to Case			0.18	°C/W
R <sub>θJA</sub>	Junction to Ambient			40	
V <sub>Isolation</sub>	RMS Voltage (50-60 Hz Sinusoidal Waveform From Terminals to Mounting Base for 1 Min.)	2500			Volts
Torque	Maximum Torque for Device Mounting Screws and Electrical Terminations.			13	lb•in

- ① Repetitive Rating: Pulse width limited by maximum junction temperature.
- ② Pulse Test: Pulse width < 380 μs, Duty Cycle < 2%
- ③ See MIL-STD-750 Method 3471
- ④ Starting T<sub>j</sub> = +25°C, L = 235μH, R<sub>G</sub> = 25Ω, Peak I<sub>L</sub> = 175A
- ⑤ I<sub>S</sub> ≤ -I<sub>D</sub> [Cont.], di/dt = 100A/μs, V<sub>DD</sub> ≤ V<sub>DSS</sub>, T<sub>j</sub> ≤ 150°C, R<sub>G</sub> = 2.0Ω, V<sub>R</sub> = 200V

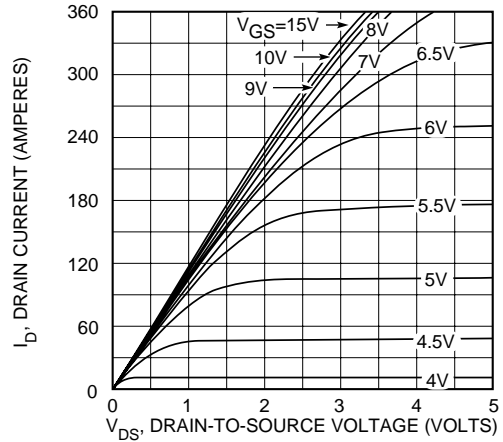
APT Reserves the right to change, without notice, the specifications and information contained herein.



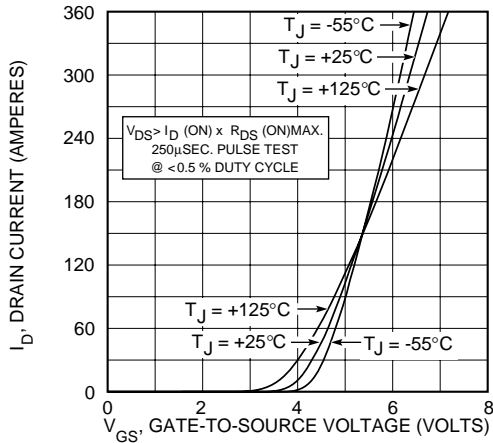
**APT20M11JVFR**



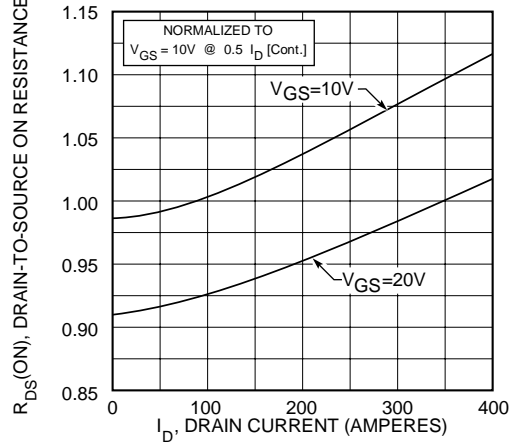
**FIGURE 2, TYPICAL OUTPUT CHARACTERISTICS**



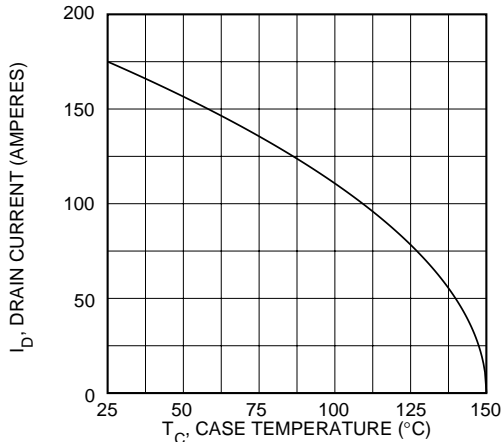
**FIGURE 3, TYPICAL OUTPUT CHARACTERISTICS**



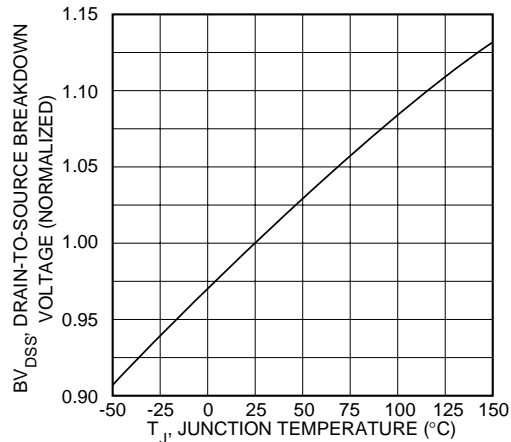
**FIGURE 4, TYPICAL TRANSFER CHARACTERISTICS**



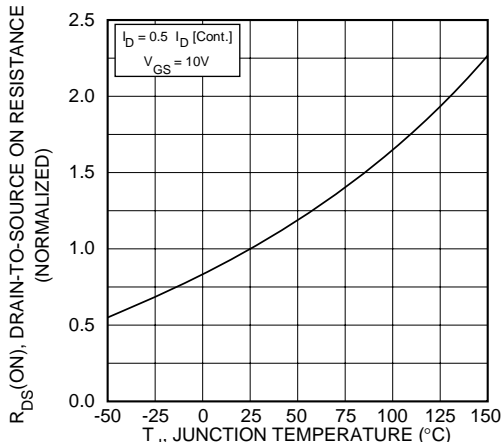
**FIGURE 5,  $R_{DS(ON)}$  vs DRAIN CURRENT**



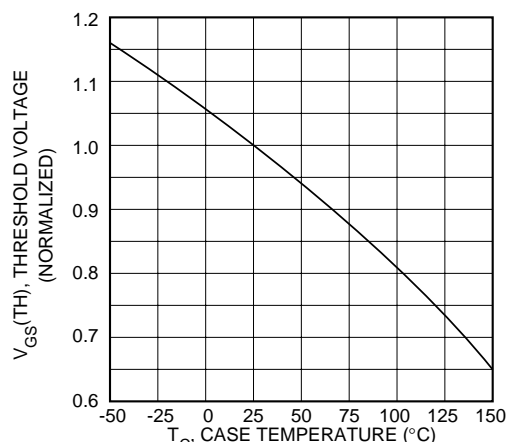
**FIGURE 6, MAXIMUM DRAIN CURRENT vs CASE TEMPERATURE**



**FIGURE 7, BREAKDOWN VOLTAGE vs TEMPERATURE**



**FIGURE 8, ON-RESISTANCE vs. TEMPERATURE**



**FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE**

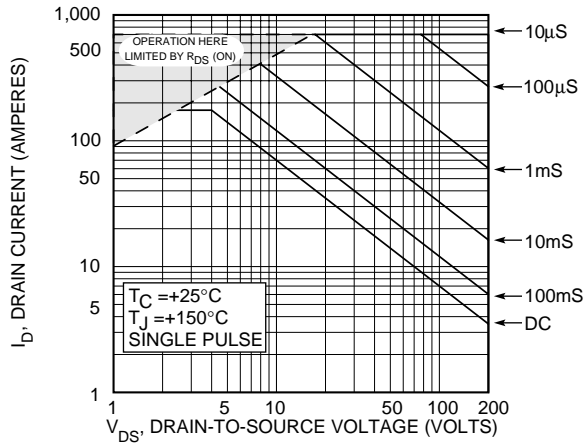


FIGURE 10, MAXIMUM SAFE OPERATING AREA

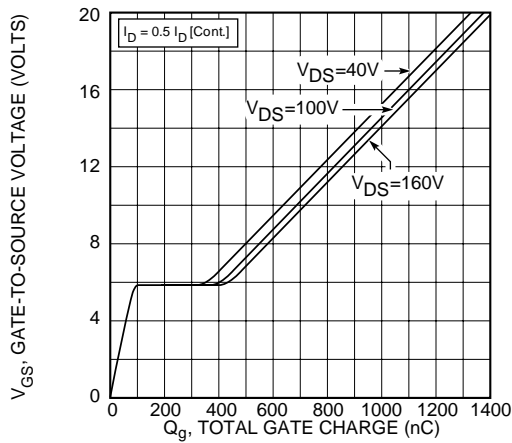


FIGURE 12, GATE CHARGES vs GATE-TO-SOURCE VOLTAGE

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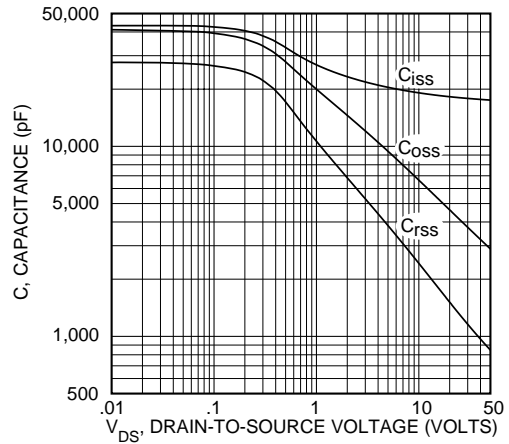


FIGURE 11, TYPICAL CAPACITANCE vs DRAIN-TO-SOURCE VOLTAGE

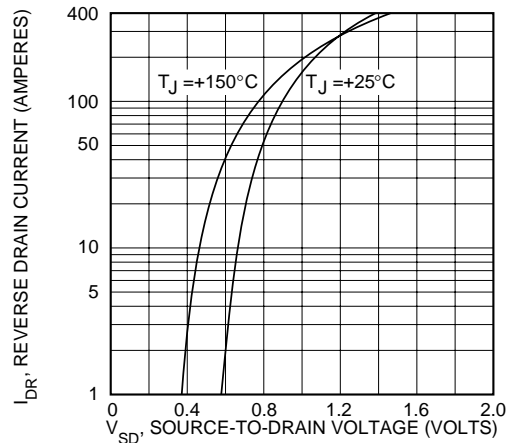
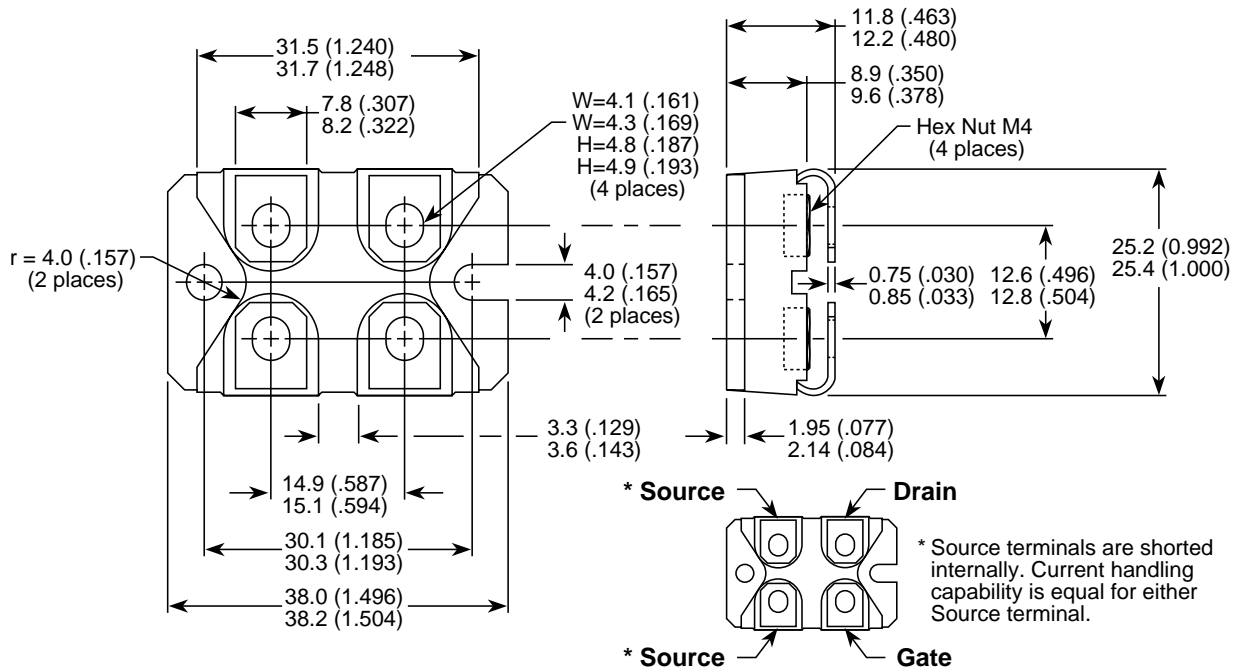


FIGURE 13, TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE

SOT-227 (ISOTOP®) Package Outline



Dimensions in Millimeters and (Inches)

$V_{Isolation}$ , RMS Voltage (50-60 Hz Sinusoidal Waveform from Terminals to Mounting Base for 1 Minute) = 2500 Volts Minimum

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