



UML25F

NPN SILICON RF POWER TRANSISTOR

DESCRIPTION:

The ASI UML25F is Designed for

FEATURES:

-
-
- **Omnigold™** Metalization System

MAXIMUM RATINGS

I_C	1.0 A
V_{CB0}	65 V
V_{CEO}	35 V
V_{CES}	65 V
V_{EBO}	4.0 V
P_{DISS}	13 W @ $T_C = 25^\circ C$
T_J	$-65^\circ C$ to $+200^\circ C$
T_{STG}	$-65^\circ C$ to $+150^\circ C$
θ_{JC}	$2.5^\circ C/W$

PACKAGE STYLE .500 6L FLG

DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A	.150 / 3.43	.160 / 4.06
B	.045 / 1.14	
C	.210 / 5.33	.220 / 5.59
D	.835 / 21.21	.865 / 21.97
E	.200 / 5.08	.210 / 5.33
F	.490 / 12.45	.510 / 12.95
G	.003 / 0.08	.007 / 0.18
H	.125 / 3.18	
I	.725 / 18.42	
J	.970 / 24.64	.980 / 24.89
K	.090 / 2.29	.105 / 2.67
L	.150 / 3.81	.170 / 4.32
M	.285 / 7.24	
N	.120 / 3.05	.135 / 3.43

ORDER CODE: ASI10695

CHARACTERISTICS $T_C = 25^\circ C$

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CB0}	$I_C = 200$ mA	65			V
BV_{CES}	$I_C = 200$ mA	65			V
BV_{CEO}	$I_C = 200$ mA	35			V
BV_{EBO}	$I_E = 10$ mA	4.0			V
I_{CB0}	$V_{CB} = 30$ V			1.0	mA
h_{FE}	$V_{CE} = 5.0$ V $I_C = 200$ mA	5.0		---	---
C_{ob}	$V_{CB} = 30$ V $f = 1.0$ MHz			30	pF
P_G	$V_{CC} = 28$ V $P_{OUT} = 25$ W $f = 400$ MHz	10			dB
η_D			60		%