

3.0A Low Dropout Voltage Regulator

B1085

Adjustable

Description

The Bay Linear B1085 is Monolithic low power 3.0A Adjustable and fixed NPN voltage regulator that are easy to use with minimum external components. All internal circuitry is designed to operate down to 1V input to output differential and the dropout voltage is fully specified as a function of load current. Dropout is guaranteed at a maximum of 1.5V at a maximum output current. Current limit is trimmed, minimizing the stress on both the regulator and power source circuitry under overload conditions. It is suitable for applications requiring a well-regulated positive output voltage with low input-output differential voltage.

The B1085 Outstanding features include full power usage up to 3.0Amp of load current internal current limiting and thermal shutdown. A 10 μF output capacitor is required on these new devices; how ever, this is usually included in most regulator design.

The B1085 is offered in a 3-pin TO-220, TO-263 & TO-252 packages compatible with other 3 terminal regulators. For 5A Low dropout Regulator refer to the BL1084 data sheet.

Features

- Adjustable Output Down to 1.2V
- Output Current of 3.0A
- Low Dropout Voltage 1.0V Typ.
- 0.015% Line Regulation
- 0.01% Load Regulation
- Current & Thermal Limiting
- Standard 3-Terminal Low Cost TO-220, D², D Packages
- Similar to industry Standard LT1085

Applications

- Constant Current Regulators
- SMPS Post Regulator
- High Efficiency Linear Regulator
- High Efficiency Linear Power Supplies
- Battery Charger
- Adjustable Power Supplies

Pin Connection







Ordering Information

| Devices | Package | Temp. | | |
|---------|---------|---------------|--|--|
| B1085T | TO-220 | 0 °C to 70 °C | | |
| B1085S | TO-263 | 0 °C to 70 °C | | |
| B1085D | TO-252 | 0 °C to 70 °C | | |
| | W (237) | -ac.com | | |
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Absolute Maximum Rating

| Parameter | Symbol | Value | Unit |
|-------------------------------------|------------------|--------------------|------|
| Maximum Input Voltage | $ m V_{IN}$ | 30 | V |
| Power Dissipation | P_{O} | Internally Limited | W |
| Thermal Resistance Junction to Case | $\theta_{ m JC}$ | 3 | °C/W |
| Thermal Resistance Junction to | $	heta_{ m JA}$ | 50 | |
| Ambient | | | |
| Operating Junction | T_{J} | | °C |
| Temperature Range | | | |
| Control Section | | 0 to 125 | |
| Power Transistor | | 0 to 150 | |
| Storage Temperature Range | T_{STG} | -65 to 150 | |
| Lead Temperature (Soldering 10 | T_{LEAD} | 300 | |
| Sec.) | | | |

Electrical Characteristics

 $(V_{IN} = 4.75 V \text{ to } 5.25 V; I_O = 10 \text{mA to } 3.0 \text{Amp, unless otherwise specified})$

| Parameter | Symbol | Conditions | MIN | TYP | MAX | UNIT |
|------------------------|-----------------------|--|-------|-------|-------|------|
| Reference Voltage | V_{O} | $I_{O} = 10 \text{mA}, T = 25 \text{ °C}, V_{in} - V_{out} = 3 \text{ V}$ | 1.238 | 1.250 | 1.262 | V |
| | | 10mA≤I _{OUT} ≤I _{FULL LOAD} | | | | |
| | | $1.5V \le (V_{in}-V_{out}) \le 25V \text{ (Note3)}$ | 1.225 | | 1.270 | |
| Line Regulation (1) | REG (line) | $I_{LOAD} = 10 \text{mA}, 1.5 \text{V} \le (V_{IN} - V_{OUT}) \le 15 \text{V}$ | | 0.015 | 0.20 | % |
| | | T= 25 °C | | 0.035 | 0.20 | |
| | | $15V \le (V_{in} - V_{out}) \le 30V$ | | 0.05 | 0.50 | |
| Load Regulation (1) | REG _(LOAD) | $(V_{in}-V_{out})=3V$ | | | | |
| | | 10mA≤I _{OUT} ≤I _{FULL LOAD} | | 0.1 | 0.30 | |
| | | T= 25 °C | | 0.2 | 0.40 | |
| Dropout Voltage | V_{D} | T= 25 °C | | 1.3 | 1.5 | V |
| | | Over Temperature | | | | |
| Current Limit | I_S | $(V_{in}-V_{out})=5V$ | | 3.2 | 4 | A |
| | | $(V_{in}-V_{out})=25V$ | | 0.2 | 0.5 | |
| Minimum Load Current | $I_{MIN LOAD}$ | $(V_{in}-V_{out})=25V$ | | 10 | 10 | mA |
| Temperature Regulation | T_{A} | T=25 °C, 30ms pulse | | 0.004 | 0.02 | %/W |
| Long Term Stability | - | T= 25 °C, 1000Hrs | | 0.3 | 1 | |
| Temperature Stability | T_{S} | | | 0.5 | | % |
| Adjust pin Current | - | T= 25 °C | | 55 | | μΑ |
| | | Over Temp. | | | 120 | · |
| Ripple Rejection | R_A | F=120Hz, C _{ADJ} =25μF, C _{OUT} =25μF Tantalum | 60 | 75 | | dB |
| | | $I_{OUT}=I_{FULL\ LOAD}$, $(V_{in}-V_{out})=3V$ (Note 5) | | | | |
| Thermal Resistance | - | TO-220 Junction to Tab | | 3.0 | 3.0 | °C/W |
| | | Junction to Ambient | | 60 | 60 | |
| | | DD Package Junction to Tab | | 3.0 | 3.0 | |
| | | Junction to Ambient | | 60 | 60 | |

Note: Output Switch tests are performed under pulsed conditions to minimize power dissipation

| Advance Information- These data sheets contain descriptions of products that are in development. The specifications are based on the engineering calculations, |
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| computer simulations and/ or initial prototype evaluation. Preliminary Information- These data sheets contain minimum and maximum specifications that are based on the initial device characterizations. These limits are subject to change upon the completion of the full characterization over the specified temperature and supply voltage ranges. |
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