Product Profile

FUJITSU

MB87M2141   *SmartMPEG*™

January 2003
Edition 1.00

MPEG-2 Decoder for Set-Top-Boxes

FME/MM/PP/0103

INTRODUCTION:

The *SmartMPEG*™ is an integrated set-top-box decoder. As a major component of a digital video broadcast (DVB) set-top-box or IDTV this device uses Fujitsu's industry-leading Cx81 CMOS technology (0.18µm).
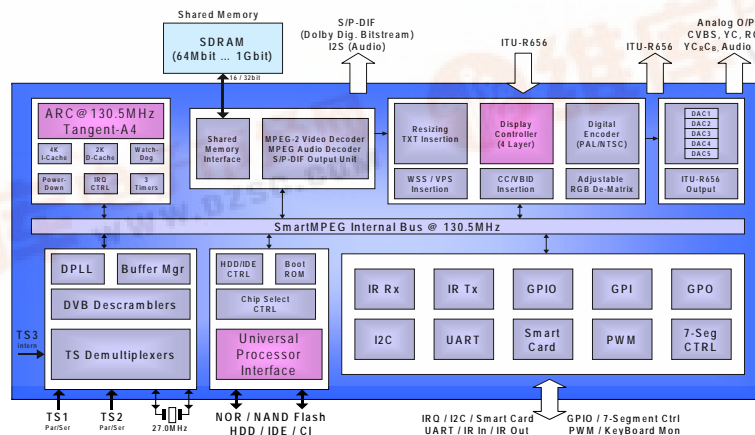
It incorporates an ARC Tangent A4 RISC core (@130MHz), two transport stream demultiplexers with integrated DVB descramblers, a PAL/NTSC digital video encoder and a display controller, which overlays up to four layers of OSD. This set-top-box decoder consists of a shared memory interface for CPU and for MPEG decoding. The universal processor interface allows connection to FLASH, hard disk drives and other asynchronous devices. The minimum SDRAM required is one 64Mbit device using 16bit data bus.

The *SmartMPEG*™ is the 3rd generation of Fujitsu's MPEG decoder, the successor to the MB87L2250. With the MB87L2250, Fujitsu offered a set-top-box chip, which enabled very low end product cost. Its successor, the *SmartMPEG*™ incorporates all the features which are required for standard set-top-boxes. This high level of integration makes it ideally suited for todays interactive set-top-box applications.

Furthermore the Fujitsu Application Programming Interface (FAPI) ensures a short time-to-market by making the software development easier. The FAPI is the programming interface for Fujitsu DVB components as well as easing migration to future devices.

FEATURES

- MPEG2 video ISO/IEC 13818-2 (MP@ML...SP@ML)
- MPEG audio layer 1/2
- 32-bit RISC CPU (ARC Tangent A4 @130MHz)
- 4K I-cache / 2K D-cache
- Three timers / watchdog / power-down mode
- Shared memory interface (SDRAM, 16/32 bit data), 64Mbit...1Gbit
- Universal processor interface (IDE, NAND/NOR FLASH & Common Interface)
- Two transport stream decoders (decoding/recording) including DVB descrambler
- Flexible MPEG video resizing (factor 1/16 to 2)
- Display controller with up to four true colour graphic or CLUT layers
- Teletext / WSS / CC / VBID insertion
- PAL/NTSC digital encoder
- RGB De-matrix (RGB or YCrCb output)
- Control of brightness, contrast and colour saturation of RGB and YCrCb output
- 5 video DAC's @ 10bit
- ITU-R 656 video input/output (shared with TS2 input)
- S/P DIF output for PCM/AC3/MPEG
- UART / Smart Card IF / I2C / GPIO
- 7-segment LED controller for 4 digits
- Infra Red receiver / transmitter
- PWM Output
- On-chip DPLL, requiring only 27.0MHz crystal
- Bootable from NOR Flash or I2C
- FPT-208P-M06 (LQFP-Package)
- Ambient Temperature Range (Std Pkg): 0°C to +70°C
- Advanced Technology: Fujitsu CMOS Cx81 (0.18µm)
- 1.8 volt device with 3.3 volt I/O
- Power consumption: typ. 800mW

Preliminary

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Worldwide Headquarters

Japan

Tel: +81 3 5322 3353
Fax: +81 3 5322 3386

Fujitsu Limited
Marketing Division
Electronic Devices
Shinjuku Dai-Ichi Seimei Bldg.7-1
Nishishinjuku 2-chome, Shinjuku-ku
Tokyo 163-0721
Japan

http://edevice.fujitsu.com/

Asia

Tel: +65 6281 0770
Fax: +65 6281 0220

Fujitsu Microelectronics Asia PTE Limited
#05-08, 151 Lorong Chuan
New Tech Park, Singapore 556741

Tel: +82 2 3484 7100
Fax: +82 2 3484 7111

Fujitsu Microelectronics Korea Ltd.
1702 Kosmo Tower, 1002 Daechi-Dong
Kangnam-Gu, Seoul 135-280, Korea

http://www.fmal.fujitsu.com/
http://www.fmk.fujitsu.com/

USA

Tel: +1 408 922 9000
Fax: +1 408 922 9179

Fujitsu Microelectronics America, Inc.
3545 North First Street
San Jose, CA 95134-1804
USA

Tel: +1 800 866 8608
Fax: +1 408 922 9179

Customer Response Center
Mon-Fri: 7am-5pm (PST)

http://www.fma.fujitsu.com/

Europe

Tel: +49 6103 690-0
Fax: +49 6103 690122

Fujitsu Microelectronics Europe GmbH
Am Siebenstein 6-10
D-63303 Dreieich-Buchschlag
Germany

e-mail   ***multimedia_info@fme.fujitsu.com***

http://www.fme.fujitsu.com/

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