Fan motor driver IC

BA6817F / BA6818FS

The BA6817F and BA6818FS are 24V motor drivers for 2-phase, half-wave fan motors. The ICs have lock detection and automatic restart functions. In compact SOP8 (BA6817F) and SSOP-A16 (BA6818FS) packages, the ICs reduce the number of external components required.

●Applications

2-phase motors such as fan motors

●Features

1) Built-in power transistors.
2) Lock detection and automatic restart functions.
3) Thermal shutdown circuit.
4) Alarm output pin. (BA6817F / BA6818FS)
5) Hall signal output pin. (BA6818FS)

●Absolute maximum ratings (Ta = 25°C)

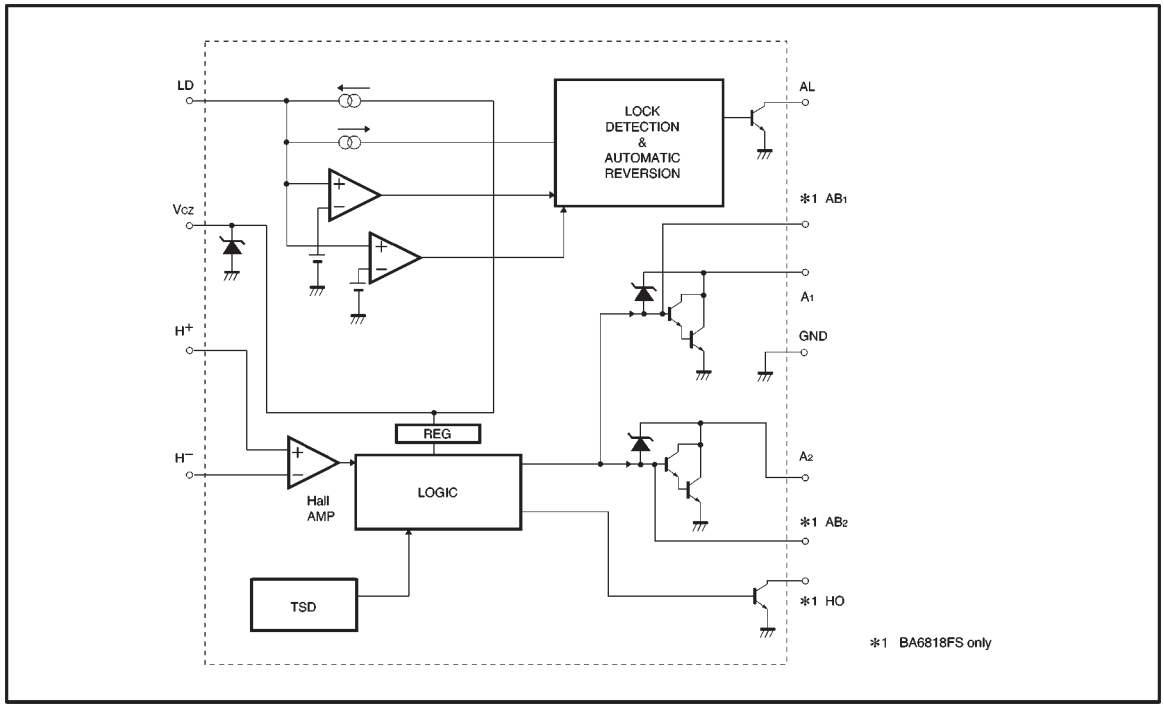
Parameter		Symbol	Limits	Unit
Power supply voltage		V_{CC}	36	V
Power dissipation	BA6818FS	Pd	800*2	mW
	BA6817F		550*3	
Operating temperature		Topr	−30～+85	℃
Storage temperature		Tstg	−55～+150	℃
Output current		I_{OUT}	1.0*4	A
Alarm output pin current		I_{AL}	10	mA
Alarm output pin withstanding voltage		V_{AL}	60	V
Hall signal output pin current *1		I_{HO}	10	mA
Hall signal output pin withstanding voltage *1		V_{HO}	60	V

*1 BA6818FS only
*2 Reduced by 6.4 mW for each increase in Ta of 1℃ over 25℃. When mounted on a glass epoxy board (50.0×50.0×1.6 mm).
*3 Reduced by 4.4 mW for each increase in Ta of 1℃ over 25℃. When mounted on a glass epoxy board (50.0×50.0×1.6 mm).
*4 Should not exceed Pd or ASO values.

●Recommended operating conditions (Ta = 25°C)

Parameter		Symbol	Range	Unit
Operating power supply current	BA6817F	I_{CC}	7～40	mA
	BA6818FS	I_{CC}	8～40	mA

●Block diagram



●Pin descriptions

BA6817F

Pin No.	Pin name	Function
1	A_2	Output 2
2	AL	Alarm output
3	LD	Capacitor connection pin for lock detection and automatic restart
4	V_{CZ}	Power supply
5	H^+	Hall input (+)
6	H^-	Hall input (-)
7	A_1	Output 1
8	GND	GROUND

BA6818FS

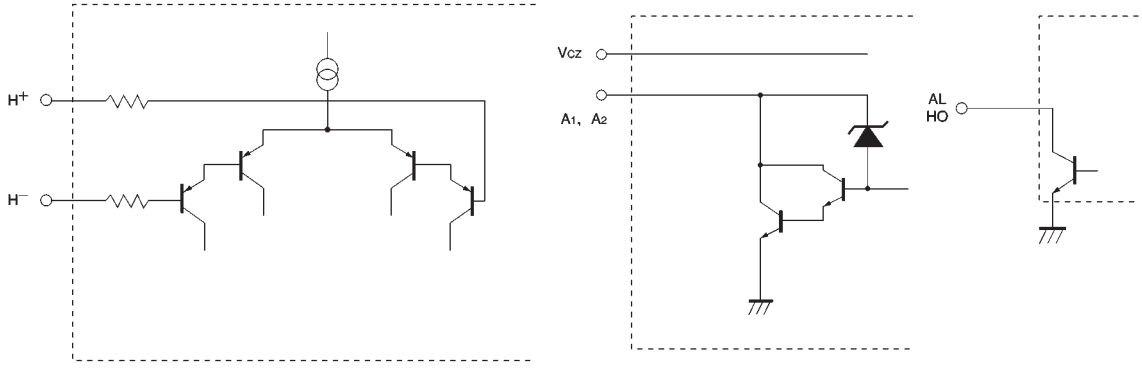
Pin No.	Pin name	Function
1	GND	GROUND
2	N.C.	—
3	AB_2	Output 2 transistor base
4	A_2	Output 2
5	HO	Hall signal output
6	AL	Alarm output
7	LD	Capacitor connection pin for lock detection and automatic restart
8	N.C.	—
9	V_{CZ}	Power supply
10	H^+	Hall input (+)
11	H^-	Hall input (-)
12	N.C.	—
13	A_1	Output 1
14	AB_1	Output 1 transistor base
15	N.C.	—
16	N.C.	—

●Hall input / output truth table

H^+	H^-	A_1	A_2	HO
H	L	HIGH (output transistor OFF)	LOW (output transistor ON)	LOW (output transistor ON)
L	H	LOW (output transistor ON)	HIGH (output transistor OFF)	HIGH (output transistor OFF)

Note: LD = 0 V

●Input / output circuits



●Electrical characteristics (unless otherwise noted, Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Internal voltage	V_{CZ}	6.6	7.6	8.6	V	When output is OFF
Lock detection capacitor charge current	I_{LDC}	1.50	2.64	3.90	μA	V_{LD}=1.8V
Lock detection capacitor discharge current	I_{LDD}	0.22	0.48	0.96	μA	V_{LD}=1.8V
Lock detection capacitor charge/discharge ratio	r_{CD}	2.9	5.5	8.1	—	$r_{CD}=I_{LDC}/I_{LDD}$
Lock detection capacitor clamp voltage	V_{LDCL}	2.1	2.45	2.80	V	—
Lock detection capacitor comparator voltage	V_{LDCP}	0.9	1.05	1.2	V	—
Output low level voltage	V_{OL}	—	0.8	1.25	V	I_O=200mA
Output leakage current	I_{OL}	—	0	100	μA	V_O=45V
Output zener voltage	V_{OZ}	51	55	59	V	Clamp current = 10 mA
Alarm output pin low level voltage	V_{ALL}	—	0.12	0.4	V	I_O=5mA
Alarm output pin leakage current	I_{ALL}	—	0	50	μA	V_{AL}=60V
Hall signal output pin voltage *	V_{HOL}	—	0.12	0.4	V	I_O=5mA
Hall signal output pin leakage current *	I_{HOL}	—	0	50	μA	V_{HO}=60V
Hall input pin offset voltage	V_{OFF}	−10	0	10	mV	V_{BH}=3V

* BA6818FS only

◎Not designed for radiation resistance.

●Circuit operation

The BA6817F and BA6818FS have motor lock detection and automatic restart circuits. The timing of lock detection and automatic restart is determined by the external capacitor connected to the LD pin. The charge time of the external capacitor is given by :

$$t_{on}\ \text{(Charge time)} = \frac{C \bullet (V_{LDCL} - V_{LDCP})}{I_{LDC}}$$

$$t_{off}\ \text{(Discharge time)} = \frac{C \bullet (V_{LDCL} - V_{LDCP})}{I_{LDD}}$$

(Typical value)

where

V_{LDCL} is the LD-pin clamp voltage (2.45V),
V_{LDCP} is the LD-pin comparator voltage (1.05V),
I_{LDC} is the LD-pin charge current (2.64µA),
I_{LDD} is the LD-pin discharge current (0.48µA),
C is the capacitance of the LD-pin external capacitor.
For C=0.47µF, for example, the charge (output ON) and discharge (output OFF) times are 0.24s and 1.37s, respectively.

The timing chart for a motor lock occasion is shown in Fig. 1.

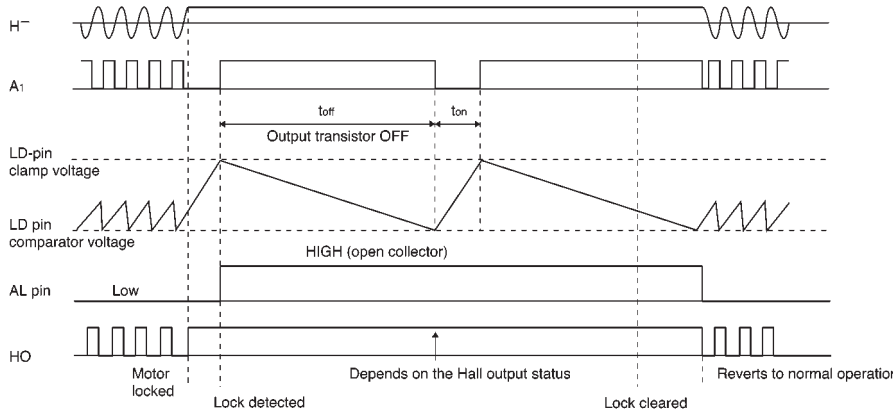


Fig.1 Timing chart

●Application example

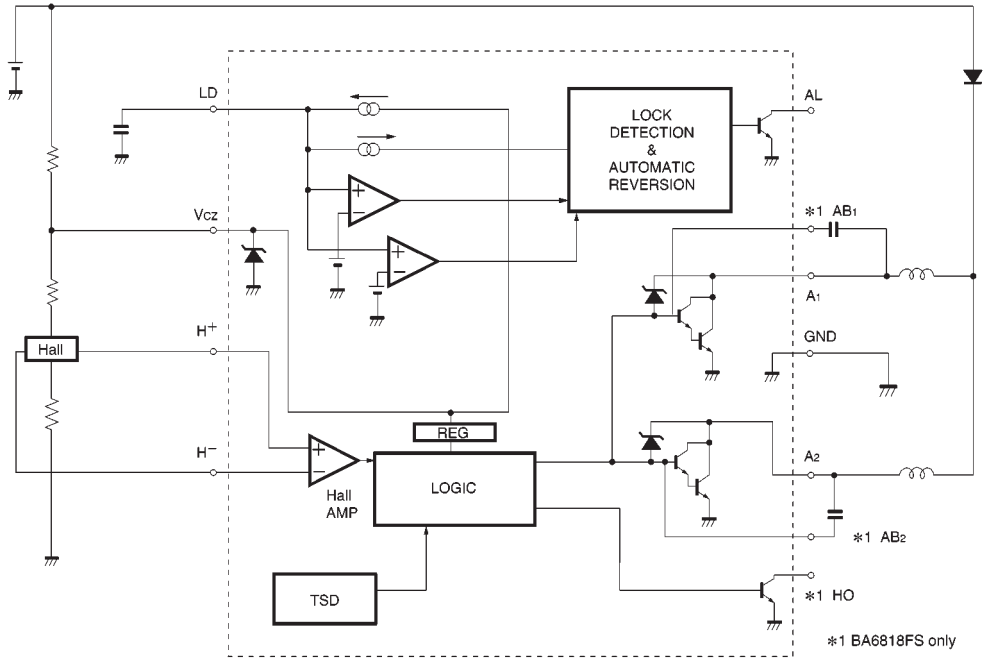


Fig.2

●Operation notes

(1) Thermal shutdown circuit

The IC has a built-in thermal shutdown circuit. The is a temperature difference of 25°C (typical) between the temperatures at which the circuit is activated and deactivated.

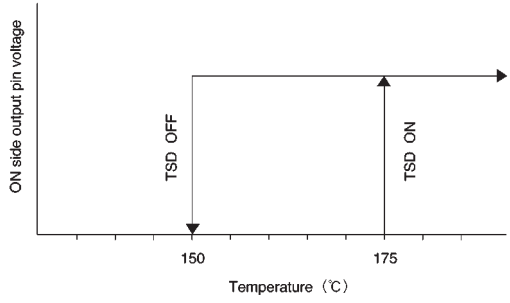


Fig.3 Temperature setting of the thermal shutdown circuit

The circuit is activated at the temperature of about 175°C (typical), so that both output transistors are turned OFF. Normal operation resumes when the circuit is deactivated.

(2) Power dissipation

Power consumed in the IC can be calculated from the following equation :

$P_C = P_C1 + P_C2 + P_C3$

P_C1 is power consumed by the circuit current.

$P_C1 = V_{CZ} \times I_{CC}$

P_C2 is the output stage current consumption.

$P_C2 = V_{OL} \times I_O$

V_{OL} is the LOW level output voltage of output pins 1 and 2, and I_O is the sink current of pins 1 and 2.

P_C3 is power consumed by the AL and HO pins.

(The HO-pin is only for BA6818FS)

where

$P_C3 = V_{ALL} \times I_{AL} + V_{HOL} \times I_{HO} / 2$

V_{ALL} is the AL-pin LOW level voltage,

I_{AL} is the AL-pin sink current,

V_{HOL} is the HO-pin LOW level voltage,

I_{HO} is the HO-pin sink current.

Make sure that your application does not exceed the allowable power dissipation of the IC.

(3) Hall amplifier input voltage

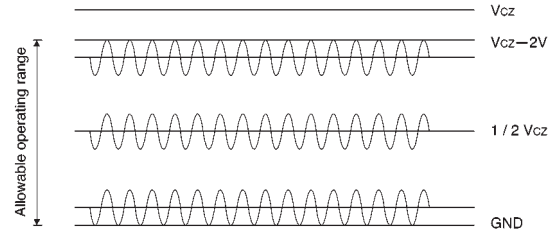


Fig.4 Hall amplifier input bias voltage

The R1 and R2 resistances must be set so as to maintain the Hall amplifier input bias voltage within the range of 0V to ($V_{CZ} - 2V$) including the signal amplitude.

The Hall device may be affected by power supply noise due to the PCB conductor pattern. If you have this problem, insert a capacitor C1 as shown in Fig. 5.

If the conductor lines from the Hall device output terminals to the Hall inputs of the IC are particularly long, noise can be picked up and fed into the inputs. If you have this problem, insert a capacitor C2 as shown in Fig. 5. Note that the Hall inputs have no hysteresis in this IC.

Hall current is given by : $\dfrac{V_{CZ}}{R1 + R2 + RH}$

where RH is the Hall device impedance.

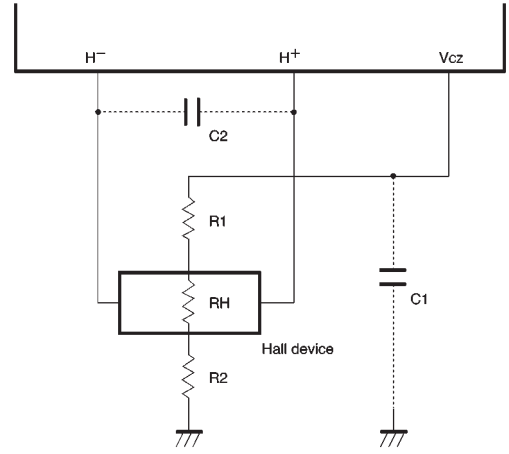
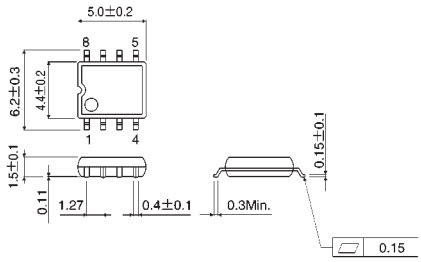


Fig.5

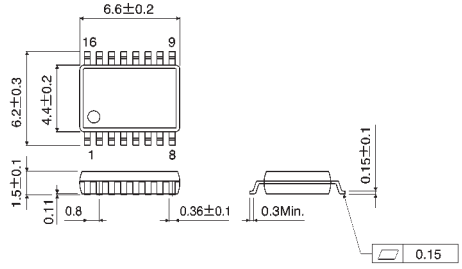
●External dimensions (Units: mm)

BA6817F



SOP8

BA6818FS



SSOP-A16