Preferred Device

Switching Diode

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	VR	75	Vdc
Peak Forward Current	TS OF. CV	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

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THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) $T_A = 25^{\circ}C$	PD	200	mW
Derate above 25°C		1.57	mW/°C
Thermal Resistance Junction to Ambient	R_{\thetaJA}	635	°C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to 150	°C

2 1 SOD-323

CASE 477 STYLE 1

MARKING DIAGRAM

1. FR-4 Minimum Pad.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Reverse Voltage Leakage Current $(V_R = 75 \text{ Vdc})$ $(V_R = 75 \text{ Vdc}, T_J = 150^{\circ}\text{C})$ $(V_R = 25 \text{ Vdc}, T_J = 150^{\circ}\text{C})$	I _R	- -	1.0 50 30	μAdc
Reverse Breakdown Voltage (I _{BR} = 100 μAdc)	V _(BR)	75	1	Vdc
Forward Voltage $(I_F = 1.0 \text{ mAdc})$ $(I_F = 10 \text{ mAdc})$ $(I_F = 50 \text{ mAdc})$ $(I_F = 150 \text{ mAdc})$	VF	2014	715 855 1000 1250	mV
Diode Capacitance $(V_R = 0, f = 1.0 \text{ MHz})$	CD	-	2.0	pF
Forward Recovery Voltage (I _F = 10 mAdc, t _r = 20 ns)	V _{FR}	-	1.75	Vdc
Reverse Recovery Time ($I_F = I_R = 10 \text{ mAdc}, R_L = 50 \Omega$)	t _{rr}	10	6.0	ns
Stored Charge ($I_F = 10 \text{ mAdc to } V_R = 5.0 \text{ Vdc}$, $R_L = 500 \Omega$)	Qs	A405	45	рС

A6 M

Π

A6 = Specific Device Code M = Date Code

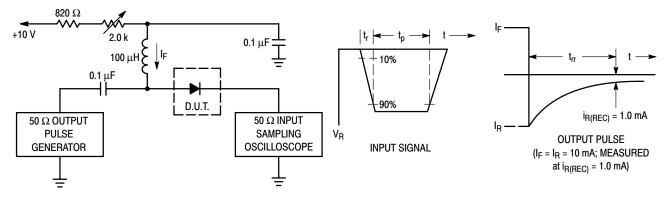
ORDERING INFORMATION

Device	Package Shipping	
BAS16HT1	SOD-323	3000/Tape & Reel

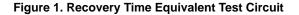
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

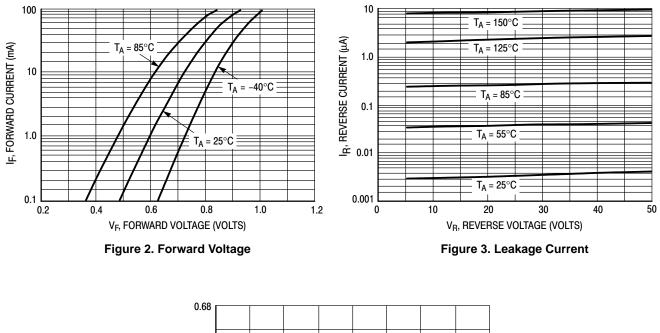
Preferred devices are recommended choices for future use and best overall value.





Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA. 2. Input pulse is adjusted so I_{R(peak)} is equal to 10 mA. 3. t_p » t_{rr}





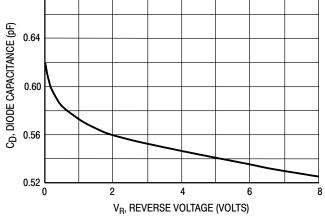
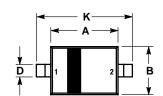


Figure 4. Capacitance

PACKAGE DIMENSIONS



ISSUE D



– L NOTE 5

J NOTE 3

Y Ī.μ

- NOTES:
 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETERS.
 LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.
 DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
 DIMENSION L IS MEASURED FROM END OF RADIUS.

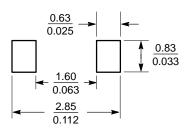
	MILLIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	1.60	1.80	0.063	0.071
В	1.15	1.35	0.045	0.053
С	0.80	1.00	0.031	0.039
D	0.25	0.40	0.010	0.016
Е	0.15 REF		0.006	REF
н	0.00	0.10	0.000	0.004
J	0.089	0.177	0.0035	0.0070
ĸ	2.30	2.70	0.091	0.106
L	0.075		0.003	





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*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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