

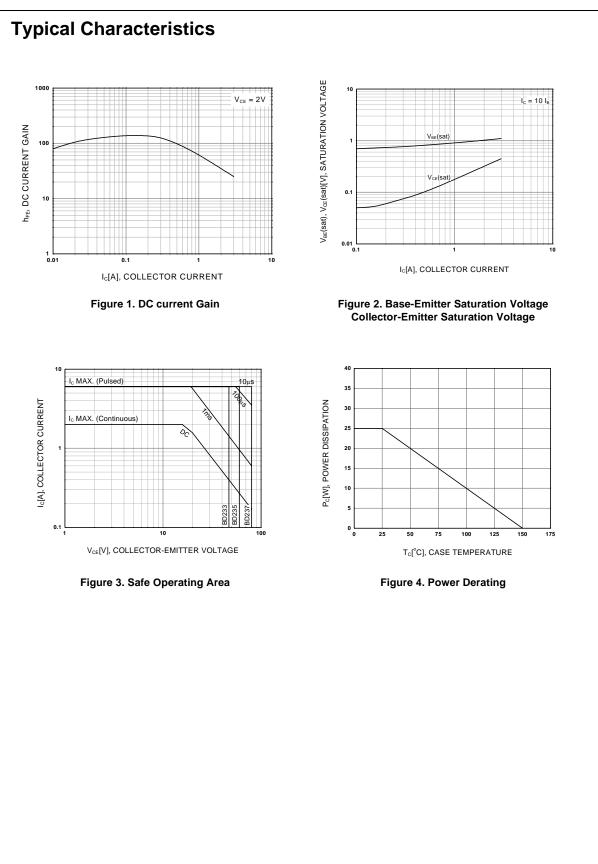


# **NPN Epitaxial Silicon Transistor**

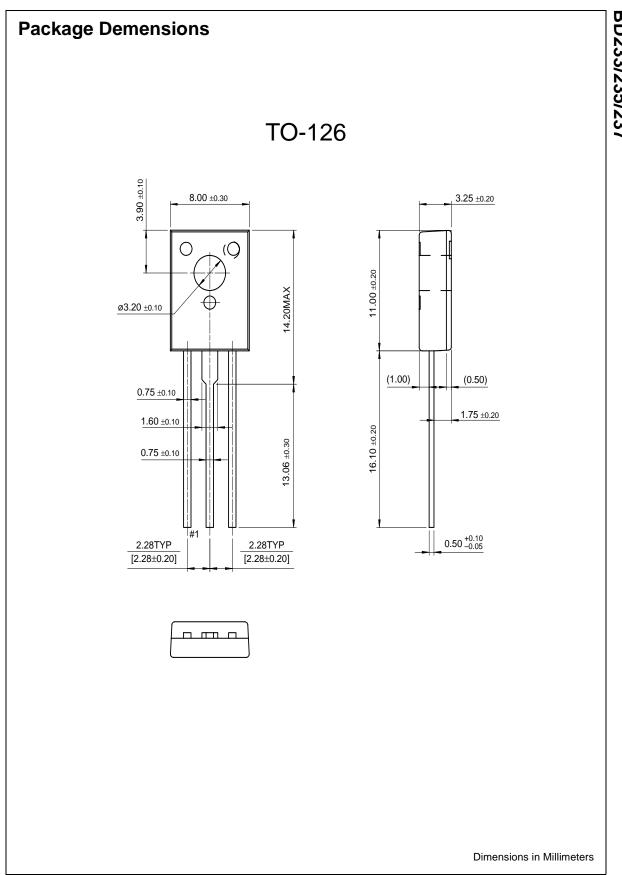
Absolute Maximum Rati	<b>NGS</b> T <sub>C</sub> =25°C unless otherwise noted
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Symbol	Para	meter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	: BD233	45	V
	EB7 :156.0	: BD235	60	V
	WWW.UL	: BD237	100	V
V <sub>CEO</sub>	Collector-Emitter Voltage	: BD233	45	V
		: BD235	60	V
		: BD237	80	V
V <sub>CER</sub>	Collector-Emitter Voltage	: BD233	45	V
	: BD235	60	V	
		: BD237	100	V
V <sub>EBO</sub>	Emitter-Base Voltage	- 15	5	V
I <sub>C</sub>	Collector Current (DC)	19. 74	2	A
I <sub>CP</sub>	*Collector Current (Pulse)		6	A
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C	;)	25	W
Т <sub>Ј</sub>	Junction Temperature	0.00	150	°C
T <sub>STG</sub>	Storage Temperature		- 65 ~ 150	°C

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V <sub>CEO</sub> (sus)	* Collector-Emitter Sustaining Voltage : BD233 : BD235 : BD237	I <sub>C</sub> = 100mA, I <sub>B</sub> = 0	45 60 80	WW	DZS	v v v
I <sub>CBO</sub>	Collector Cut-off Current : BD233 : BD235 : BD237	$V_{CB} = 45V, I_E = 0$ $V_{CB} = 60V, I_E = 0$ $V_{CB} = 100V, I_E = 0$			100 100 100	μΑ μΑ μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$			1	mA
h <sub>FE</sub>	* DC Current Gain	$V_{CE} = 2V, I_{C} = 150mA$ $V_{CE} = 2V, I_{C} = 1A$	40 25			
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	I <sub>C</sub> = 1A, I <sub>B</sub> = 0.1A			0.6	V
V <sub>BE</sub> (on)	* Base-Emitter ON Voltage	$V_{CE} = 2V, I_{C} = 1A$			1.3	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 10V, I_{C} = 250mA$	3			MHz



# BD233/235/237



BD233/235/237

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