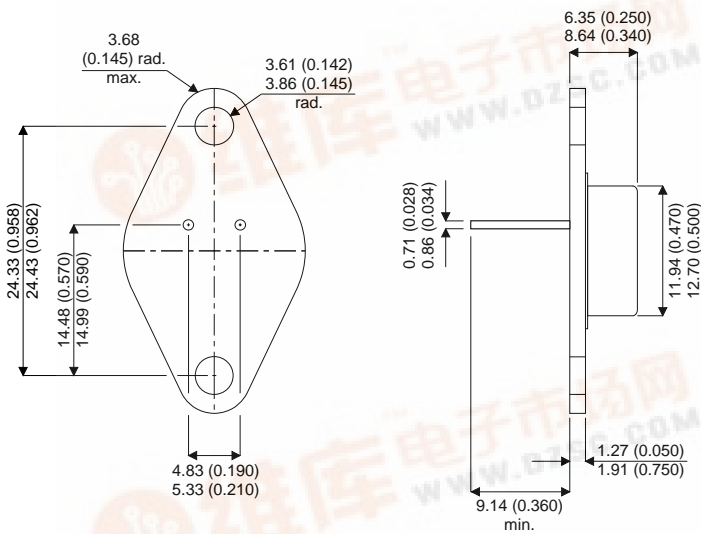


BDX14A

MECHANICAL DATA

Dimensions in mm



TO66 Package.

Pin 1 – Base Pin 2 – Emitter Case - Collector

**PNP
SILICON TRANSISTOR,
EPITAXIAL BASE**

FEATURES:

- LF Large Signal Power Amplification
- Medium Current Switching

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

V_{CBO}	Collector – Base Voltage (Open Emitter)	- 90V
V_{CEO}	Collector – Emitter Voltage (Open Base)	- 55V
V_{CER}	Collector – Emitter Voltage $R_{BE} = 100\Omega$	- 60V
V_{CEX}	Collector – Base Voltage $V_{BE} = +1.5V$	- 90V
V_{EBO}	Emitter – Base Voltage	-7V
I_C	Collector Current	-4V
I_B	Base Current	-2V
P_{tot}	Power Dissipation	29W
T_J	Maximum Junction Temperature	200°C
T_{STG}	Storage Temperature	-65 to 200°C
$R_{th-(j-c)}$	Junction to Case.	6°C / W



ELECTRICAL CHARACTERISTICS ($T_{\text{case}} = 25^{\circ}\text{C}$ unless otherwise stated)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{CEX} Collector Emitter Cut Off Current	$V_{\text{CE}} = -90\text{V}$ $V_{\text{BE}} = +1.5\text{V}$			-1	mA
	$V_{\text{CE}} = -30\text{V}$ $V_{\text{BE}} = +1.5\text{V}$ $T_{\text{case}} = 150^{\circ}\text{C}$			-5	
$V_{\text{CEO(SUS)}}^*$ Collector Emitter Breakdown Voltage	$I_{\text{C}} = -100\text{mA}$ $I_{\text{B}} = 0$	-55			V
$V_{\text{CER(SUS)}}^*$ Collector Emitter Breakdown Voltage	$I_{\text{C}} = -100\text{mA}$ $R_{\text{BE}} = 100\Omega$	-60			
$V_{(\text{BR})\text{EBO}}^*$ Emitter Base Breakdown Voltage	$I_{\text{E}} = -1\text{A}$ $I_{\text{C}} = 0$	-7			V
$h_{21\text{E}}^*$ Static Forward Current Transfer Ratio	$V_{\text{CE}} = -4\text{V}$ $I_{\text{C}} = -0.5\text{A}$	25		250	—
$V_{\text{CE(sat)}}^*$ Collector Emitter Saturation Voltage	$I_{\text{C}} = -0.5\text{A}$ $I_{\text{B}} = -0.05\text{A}$			-1	V
V_{BE}^* Base Emitter Voltage	$V_{\text{CE}} = -4\text{V}$ $I_{\text{C}} = -0.5\text{A}$			-1.7	V
f_{T} Transition Frequency	$V_{\text{CB}} = -10\text{V}$ $I_{\text{C}} = -0.2\text{A}$ $f = 1\text{MHz}$	4			MHz

* Pulse test $t_{\text{p}} = 300\mu\text{s}$, $\delta < 2\%$