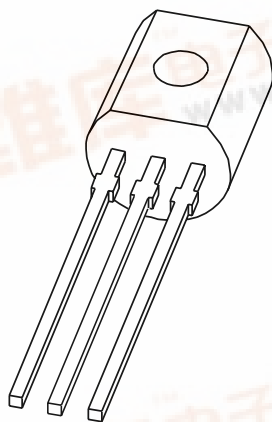


DISCRETE SEMICONDUCTORS

DATA SHEET



BF483; BF485; BF487 NPN high-voltage transistors

Product specification
Supersedes data of 1996 Dec 09

1999 Apr 12

NPN high-voltage transistors

BF483; BF485; BF487

FEATURES

- Low feedback capacitance.

APPLICATIONS

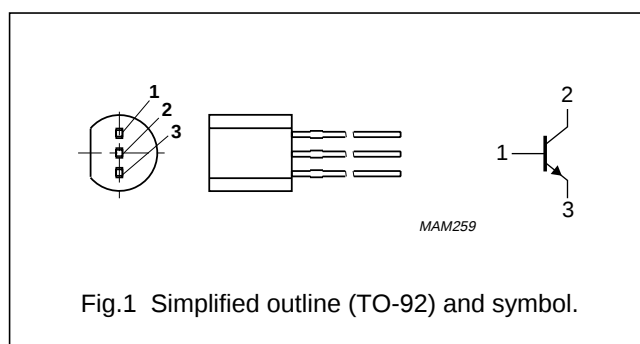
- Intended for use in video output stages in black-and-white and in colour television receivers.

DESCRIPTION

NPN transistors in a TO-92 plastic package.
PNP complement: BF488

PINNING

PIN	DESCRIPTION
1	base
2	collector
3	emitter



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter			
	BF483		—	300	V
	BF485		—	350	V
	BF487		—	400	V
V_{CEO}	collector-emitter voltage	open base			
	BF483		—	250	V
	BF485		—	300	V
	BF487		—	350	V
V_{EBO}	emitter-base voltage	open collector	—	5	V
I_C	collector current (DC)		—	100	mA
I_{CM}	peak collector current		—	200	mA
I_{BM}	peak base current		—	100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$; note 1	—	830	mW
T_{stg}	storage temperature		−65	+150	°C
T_j	junction temperature		—	150	°C
T_{amb}	operating ambient temperature		−65	+150	°C

Note

1. Transistor mounted on a printed-circuit board.

NPN high-voltage transistors

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	150	K/W

Note

1. Transistor mounted on a printed-circuit board.

CHARACTERISTICS

$T_j = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0; V_{CB} = 300\text{ V}$	–	20	nA
		$I_E = 0; V_{CB} = 250\text{ V}; T_j = 150\text{ °C}$	–	20	μA
I_{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = 5\text{ V}$	–	100	nA
h_{FE}	DC current gain	$I_C = 25\text{ mA}; V_{CE} = 20\text{ V}$	50	–	
		$I_C = 40\text{ mA}; V_{CE} = 20\text{ V}$	20	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 30\text{ mA}; I_B = 5\text{ mA}$	–	600	mV
C_{re}	feedback capacitance	$I_C = i_c = 0; V_{CE} = 30\text{ V}; f = 1\text{ MHz}$	–	1.4	pF
f_T	transition frequency	$I_C = -10\text{ mA}; V_{CE} = 10\text{ V}; f = 100\text{ MHz}$	70	110	MHz

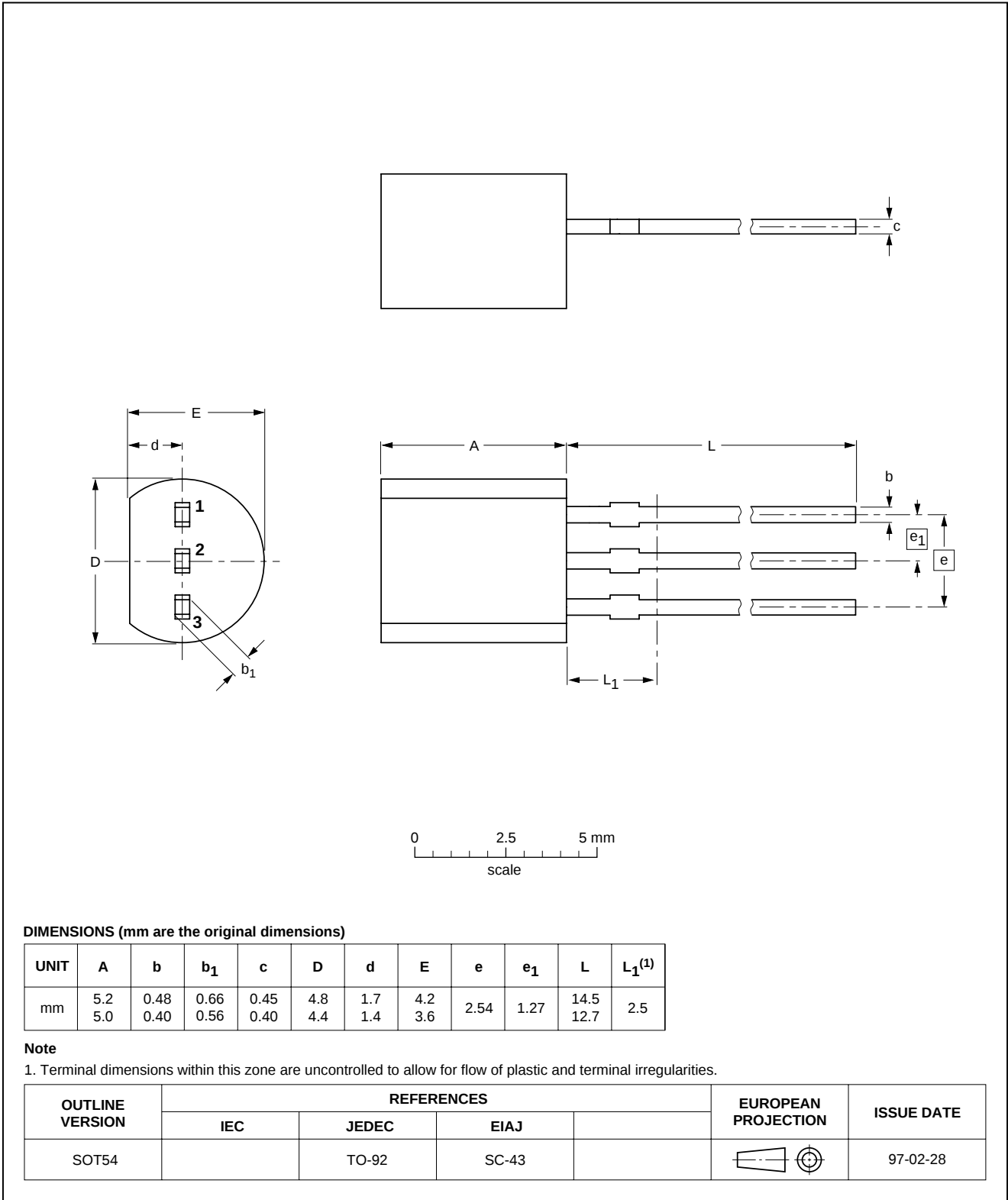
NPN high-voltage transistors

BF483; BF485; BF487

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



NPN high-voltage transistors

BF483; BF485; BF487

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

NPN high-voltage transistors

BF483; BF485; BF487

NOTES

NPN high-voltage transistors

BF483; BF485; BF487

NOTES

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