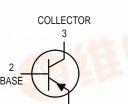
High Voltage Transistor PNP Silicon



EMITTER

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	VCEO	-350	Vdc	
Collector-Base Voltage	VCBO	-350	Vdc	
Emitter-Base Voltage	VEBO	-6.0	Vdc	
Collector Current — Continuous	IC	-500	mAdc	
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0	Watts mW/°C	
Total Device Dissipation @ T _C = 25°C Derate above 25°C	D - PD	1.5 12	Watts mW/°C	
Operating and Storage Junction Temperature Range	T _J , T _{Stg}	-55 to +150	°C	

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	200	°C/W
Thermal Resistance, Junction to Case	R ₀ JC	83.3	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (1) (IC = -1.0 mAdc, IB = 0)	V(BR)CEO	-350	- 17.19	Vdc
Collector-Base Breakdown Voltage (I _C = -100 μAdc, I _E = 0)	V(BR)CBO	-350	75C.C1	Vdc
Emitter–Base Breakdown Voltage (I _E = –100 μAdc, I _C = 0)	V(BR)EBO	-6.0	_	Vdc
Collector Cutoff Current (V _{CE} = -250 Vdc)		_	-10	nAdc
Emitter Cutoff Current $(V_{EB} = -6.0 \text{ Vdc}, I_{C} = 0)$		_	0.1	μAdc
Collector Cutoff Current (VCB = -250 Vdc, IE = 0, TA = 25°C) (VCB = -250 Vdc, IE = 0, TA = 100°C)	ICBO	_ _	-0.005 -1.0	μAdc

^{1.} Pulse Test: Pulse Width \leq 300 μ s; Duty Cycle \leq 2.0%.





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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS	•			
DC Current Gain (I _C = -1.0 mAdc, V _{CE} = -10 Vdc) (I _C = -10 mAdc, V _{CE} = -10 Vdc)	hFE	25 40		_
Collector – Emitter Saturation Voltage (I _C = -20 mAdc, I _B = -2.0 mAdc)	VCE(sat)	_	-2.0	Vdc
Base-Emitter On Voltage (I _C = -20 mA, I _B = -2.0 mA)	VBE(sat)	_	-2.0	Vdc
DYNAMIC CHARACTERISTICS	•			
Current-Gain — Bandwidth Product (I _C = -10 mAdc, V _{CE} = -20 Vdc, f = 20 MHz)	fΤ	50	_	MHz
Common–Emitter Feedback Capacitance (V _{CB} = -100 Vdc, I _E = 0, f = 1.0 MHz)	C _{re}	_	1.6	pF

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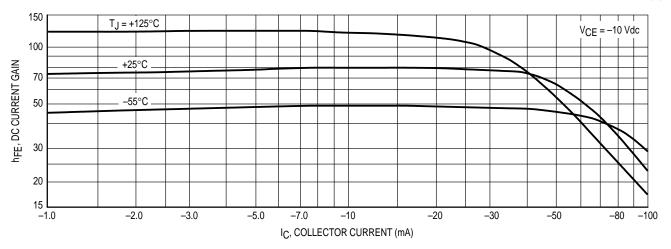


Figure 1. DC Current Gain

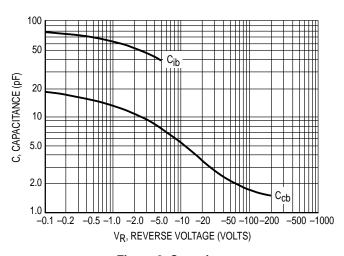


Figure 2. Capacitances

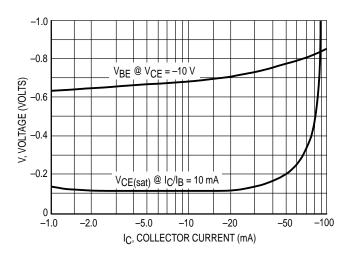


Figure 4. "On" Voltages

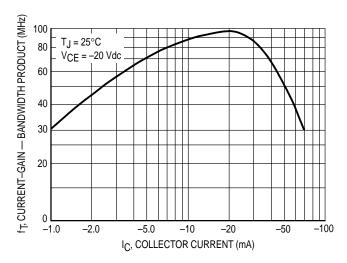


Figure 3. Current-Gain — Bandwidth Product

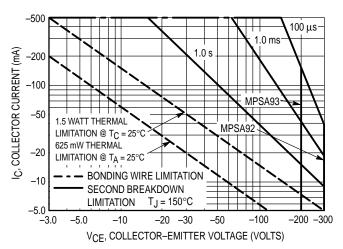
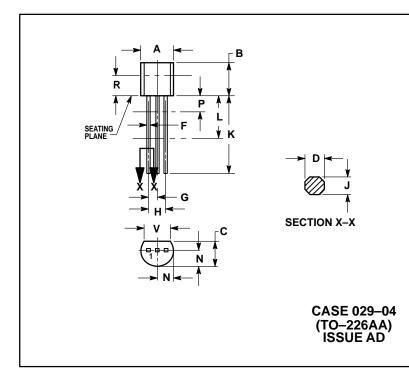


Figure 5. Active Region — Safe Operating Area

PACKAGE DIMENSIONS



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
 Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH.
- 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN P AND L.
 DIMENSION D AND J APPLY BETWEEN L AND K
 MINIMUM. LEAD DIMENSION IS UNCONTROLLED
 IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
С	0.125	0.165	3.18	4.19	
D	0.016	0.022	0.41	0.55	
F	0.016	0.019	0.41	0.48	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.115		2.93		
٧	0.135		3.43		

STYLE 1: PIN 1. EMITTER

BASE
 COLLECTOR

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