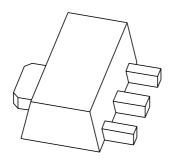
DISCRETE SEMICONDUCTORS

DATA SHEET



BFQ591NPN 7 GHz wideband transistor

Product specification Supersedes data of 2002 Jan 07 2002 Feb 04





NPN 7 GHz wideband transistor

BFQ591

FEATURES

- High power gain
- Low noise figure
- High transition frequency
- Gold metallization ensures excellent reliability.

APPLICATIONS

Intended for applications in the GHz range such as MATV or CATV amplifiers and RF communications subscribers equipment.

DESCRIPTION

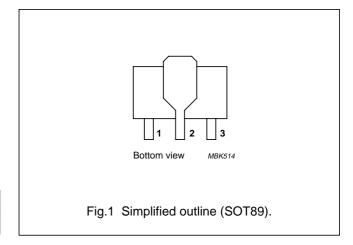
NPN wideband transistor in a SOT89 plastic package.

MARKING

TYPE NUMBER	MARKING CODE
BFQ591	ВСр

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	_	20	V
V _{CEO}	collector-emitter voltage	open base	_	_	15	V
I _C	collector current (DC)		_	_	200	mA
P _{tot}	total power dissipation	T _s ≤ 90 °C; note 1	_	_	2.25	W
h _{FE}	DC current gain	I _C = 70 mA; V _{CE} = 8 V	60	90	250	
C _{re}	feedback capacitance	I _C = 0; V _{CB} = 12 V; f = 1 MHz	_	0.8	_	pF
f _T	transition frequency	I _C = 70 mA; V _{CE} = 12 V; f = 1 GHz	_	7	-	GHz
G _{UM}	maximum unilateral power gain	I _C = 70 mA; V _{CE} = 12 V; f = 900 MHz; T _{amb} = 25 °C	_	11	_	dB
S ₂₁ ²	insertion power gain	I _C = 70 mA; V _{CE} = 12 V; f = 900 MHz; T _{amb} = 25 °C	_	10	-	dB

Note

1. T_s is the temperature at the soldering point of the collector pin.

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LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	20	V
V _{CEO}	collector-emitter voltage	open base	_	15	V
V _{EBO}	emitter-base voltage	open collector	_	3	V
I _C	collector current (DC)		_	200	mA
P _{tot}	total power dissipation	T _s ≤ 90 °C; note 1	_	2.25	W
T _{stg}	storage temperature		-65	+150	°C
T _i	junction temperature		_	175	°C

Note

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	$T_s \le 90$ °C; note 1	38	K/W

Note

1. T_s is the temperature at the soldering point of the collector pin.

^{1.} T_s is the temperature at the soldering point of the collector pin.

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CHARACTERISTICS

 T_i = 25 °C; unless otherwise specified.

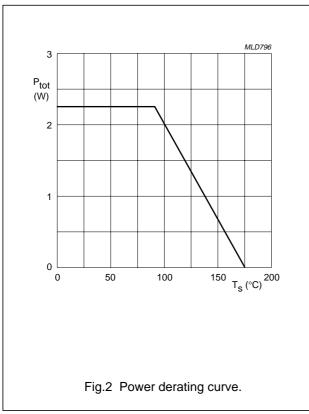
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 0.1 mA; I _E = 0	_	_	20	V
V _{(BR)CES}	collector-emitter breakdown voltage	$I_C = 0.1 \text{ mA}; I_B = 0$	_	_	15	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 0.1 mA; I _C = 0	-	_	3	V
I _{CBO}	collector-base leakage current	I _E = 0; V _{CB} = 10	_	_	100	nA
h _{FE}	DC current gain $I_C = 70 \text{ mA}$; $V_{CE} = 8 \text{ V}$		60	90	250	
C _{re}	feedback capacitance	I _C = 0; V _{CB} = 12 V; f = 1 MHz	_	0.8	_	pF
f _T	transition frequency	I _C = 70 mA; V _{CE} = 12 V; f = 1 GHz	_	7	_	GHz
G _{UM}	maximum unilateral power gain; note 1	$I_C = 70 \text{ mA}; V_{CE} = 12 \text{ V};$ $T_{amb} = 25 \text{ °C}$				
		f = 900 MHz	-	11	_	dB
		f = 2 GHz	-	5.5	_	dB
S ₂₁ ²	insertion power gain	$I_C = 70 \text{ mA}; V_{CE} = 12 \text{ V};$ f = 1 GHz; $T_{amb} = 25 \text{ °C}$	_	10	_	dB
Vo	output voltage	note 2	_	700	_	mV

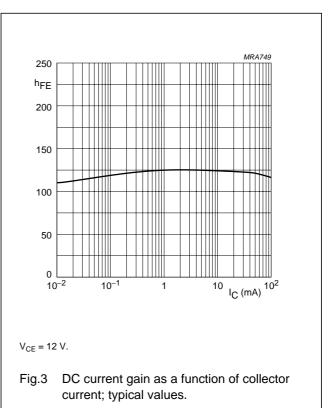
Notes

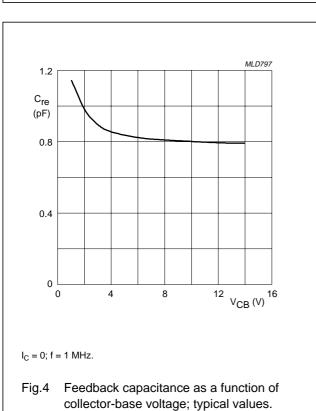
- 1. G_{UM} is the maximum unilateral power gain, assuming s_{12} is zero and $G_{UM} = 10 \log \frac{\left|s_{21}\right|^2}{(1-\left|s_{11}\right|^2)(1-\left|s_{22}\right|^2)} dB$.
- 2. d_{im} = 60 dB (DIN45004B); $V_p = V_o$; $V_q = V_o$ -6 dB; f_p = 795.25 MHz; f_q = 803.25 MHz; f_r = 803.25 MHz; measured at $f_{(p+q+r)}$ = 793.25 MHz.

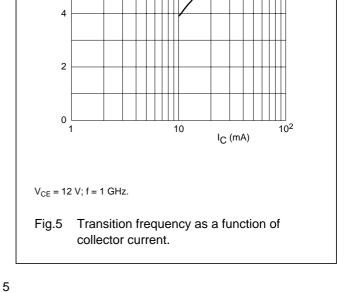
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8 f_T

6

(GHz)

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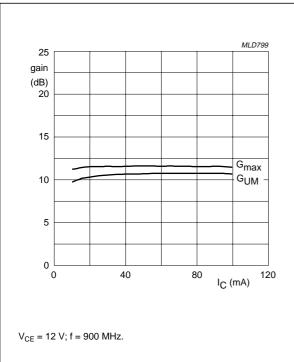
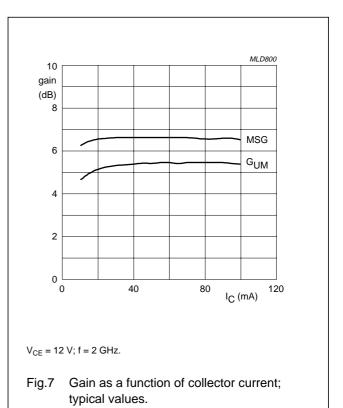
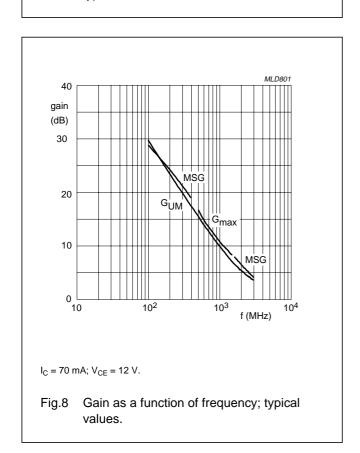


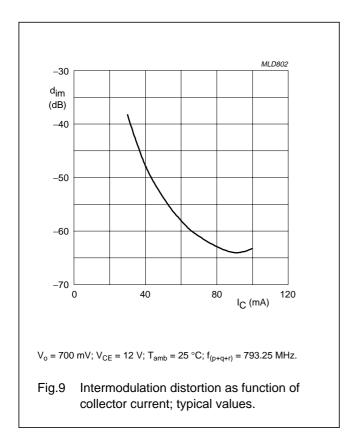
Fig.6 Gain as a function of collector current; typical values.





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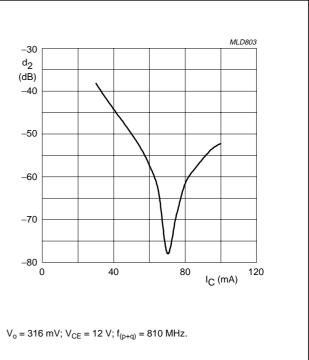


Fig.10 Second order intermodulation distortion as function of collector current; typical values.

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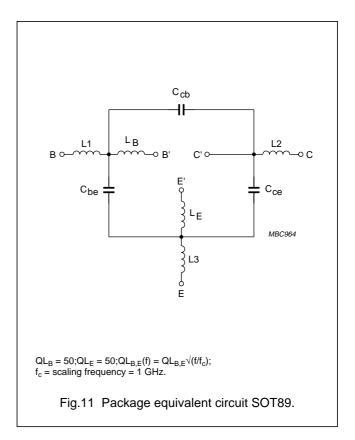
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SPICE parameters for the BFQ591 die.

SEQUENCE No.	PARAMETER	VALUE	UNIT
1	IS	1.341	fA
2	BF	123.5	_
3	NF	.988	_
4	VAF	75.85	V
5	IKF	9.656	mA
6	ISE	232.2	fA
7	NE	2.134	_
8	BR	10.22	_
9	NR	1.016	_
10	VAR	1.992	V
11	IKR	294.1	mA
12	ISC	211.0	аА
13	NC	997.2	_
14	RB	5.00	Ω
15	IRB	1.000	μΑ
16	RBM	5.00	Ω
17	RE	1.275	Ω
18	RC	920.6	Ω
19 ⁽¹⁾	XTB	0.000	_
20 ⁽¹⁾	EG	1.110	eV
21(1)	XTI	3.000	_
22	CJE	3.821	pF
23	VJE	600.0	mV
24	MJE	348.5	_
25	TF	13.60	ps
26	XTF	71.73	_
27	VTF	10.28	V
28	ITF	1.929	mA
29	PTF	0.000	deg
30	CJC	1.409	fF
31	VJC	219.4	mV
32	MJC	166.5	-
33	XCJ	2.340	-
34	TR	543.7	ps
35 ⁽¹⁾	CJS	0.000	F
36 ⁽¹⁾	VJS	750.0	mV
37 ⁽¹⁾	MJS	0.000	-
38	FC	733.2	_



List of components (see Fig.11)

DESIGNATION	VALUE	UNIT
C _{be}	16	fF
C _{cb}	150	fF
C _{ce}	150	fF
L1	1	nH
L2	0.01	nH
L3	1	nH
L _B	1.2	nH
L _E	1.2	nH

Note

1. These parameters have not been extracted, the default values are shown.

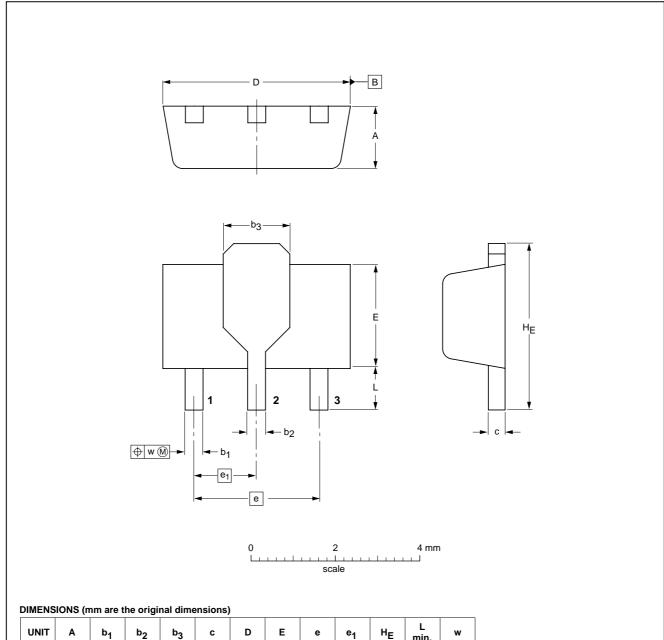
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PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 3 leads

SOT89



UNIT	A	b ₁	b ₂	b ₃	С	D	E	e	e ₁	HE	L min.	w
mm	1.6 1.4	0.48 0.35	0.53 0.40	1.8 1.4	0.44 0.37	4.6 4.4	2.6 2.4	3.0	1.5	4.25 3.75	0.8	0.13

OUTLINE		REFER	ENCES	EUROPEAN ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE	
SOT89		TO-243	SC-62		97-02-28 99-09-13	

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DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
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