

DISCRETE SEMICONDUCTORS

DATA SHEET

BLV11 VHF power transistor

Product specification

August 1986

VHF power transistor

BLV11

DESCRIPTION

N-P-N silicon planar epitaxial transistor intended for use in class-A, B and C operated mobile, h.f. and v.h.f. transmitters with a nominal supply voltage of 13,5 V. The transistor is resistance stabilized and is guaranteed to withstand severe load mismatch conditions with a supply over-voltage to 16,5 V.

It has a 3/8" flange envelope with a ceramic cap. All leads are isolated from the flange.

QUICK REFERENCE DATA

R.F. performance up to $T_h = 25\text{ }^\circ\text{C}$ in an unneutralized common-emitter class-B circuit

MODE OF OPERATION	V_{CE} V	f MHz	P_L W	G_p dB	η %	\bar{z}_i Ω	\bar{Y}_L mS
c.w.	13,5	175	15	> 8,0	> 60	2,3 + j2,2	130 - j4,4
c.w.	12,5	175	15	typ. 7,5	typ. 67	-	-

PIN CONFIGURATION

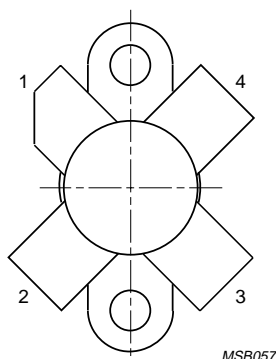


Fig.1 Simplified outline, SOT123.

PINNING

PIN	DESCRIPTION
1	collector
2	emitter
3	base
4	emitter

PRODUCT SAFETY This device incorporates beryllium oxide, the dust of which is toxic. The device is entirely safe provided that the BeO disc is not damaged.

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RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Collector-emitter voltage ($V_{BE} = 0$)

peak value

V_{CESM} max. 36 V

Collector-emitter voltage (open base)

V_{CEO} max. 18 V

Emitter-base voltage (open collector)

V_{EBO} max. 4 V

Collector current (average)

$I_{C(AV)}$ max. 3 A

Collector current (peak value); $f > 1$ MHz

I_{CM} max. 8 A

R.F. power dissipation ($f > 1$ MHz); $T_{mb} = 25$ °C

P_{rf} max. 36 W

Storage temperature

T_{stg} -65 to + 150 °C

Operating junction temperature

T_j max. 200 °C

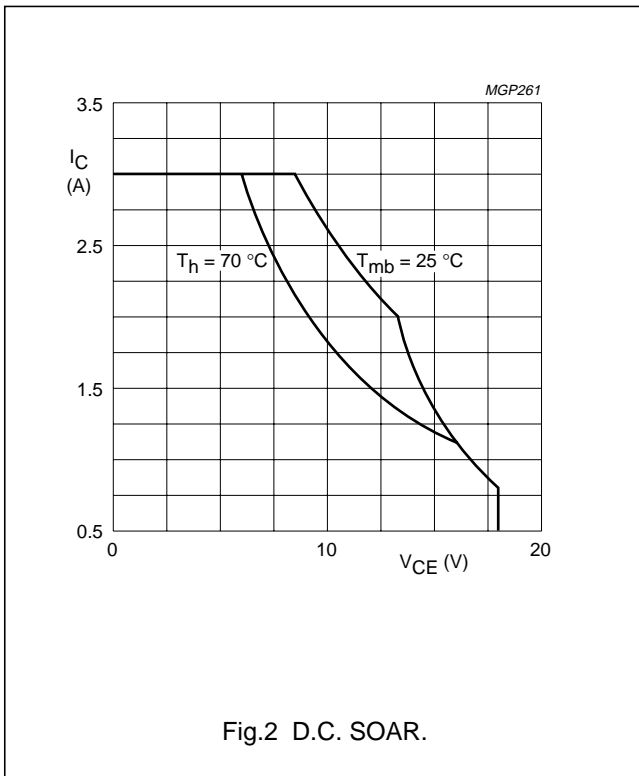
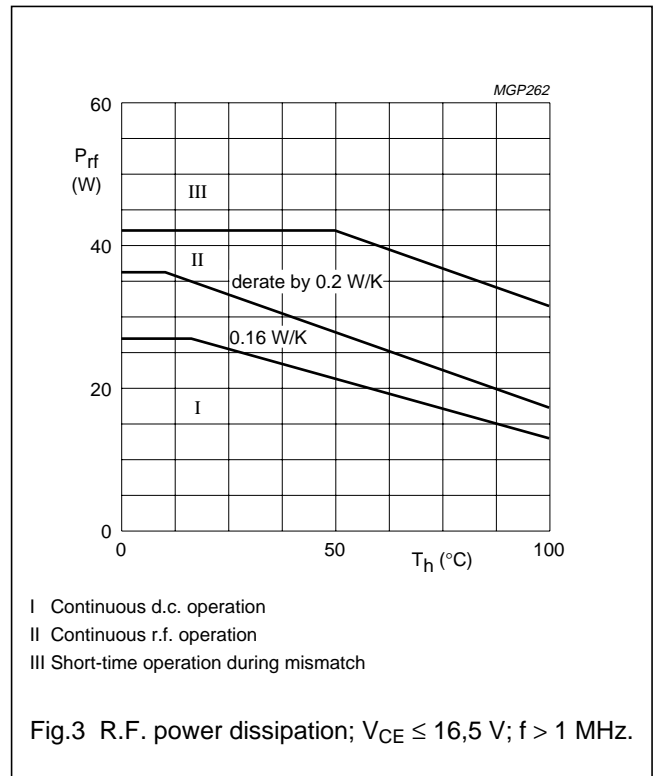


Fig.2 D.C. SOAR.



- I Continuous d.c. operation
- II Continuous r.f. operation
- III Short-time operation during mismatch

Fig.3 R.F. power dissipation; $V_{CE} \leq 16,5$ V; $f > 1$ MHz.

THERMAL RESISTANCE

(dissipation = 15 W; $T_{mb} = 74,5$ °C, i.e. $T_h = 70$ °C)

From junction to mounting base (d.c. dissipation)

$R_{th\ j-mb(dc)}$ = 6,55 K/W

From junction to mounting base (r.f. dissipation)

$R_{th\ j-mb(rf)}$ = 4,95 K/W

From mounting base to heatsink

$R_{th\ mb-h}$ = 0,3 K/W

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CHARACTERISTICS $T_j = 25\text{ }^\circ\text{C}$

Collector-emitter breakdown voltage

 $V_{BE} = 0; I_C = 10\text{ mA}$ $V_{(BR)CES} > 36\text{ V}$

Collector-emitter breakdown voltage

open base; $I_C = 50\text{ mA}$ $V_{(BR)CEO} > 18\text{ V}$

Emitter-base breakdown voltage

open collector; $I_E = 4\text{ mA}$ $V_{(BR)EBO} > 4\text{ V}$

Collector cut-off current

 $V_{BE} = 0; V_{CE} = 18\text{ V}$ $I_{CES} < 4\text{ mA}$ Second breakdown energy; $L = 25\text{ mH}; f = 50\text{ Hz}$

open base

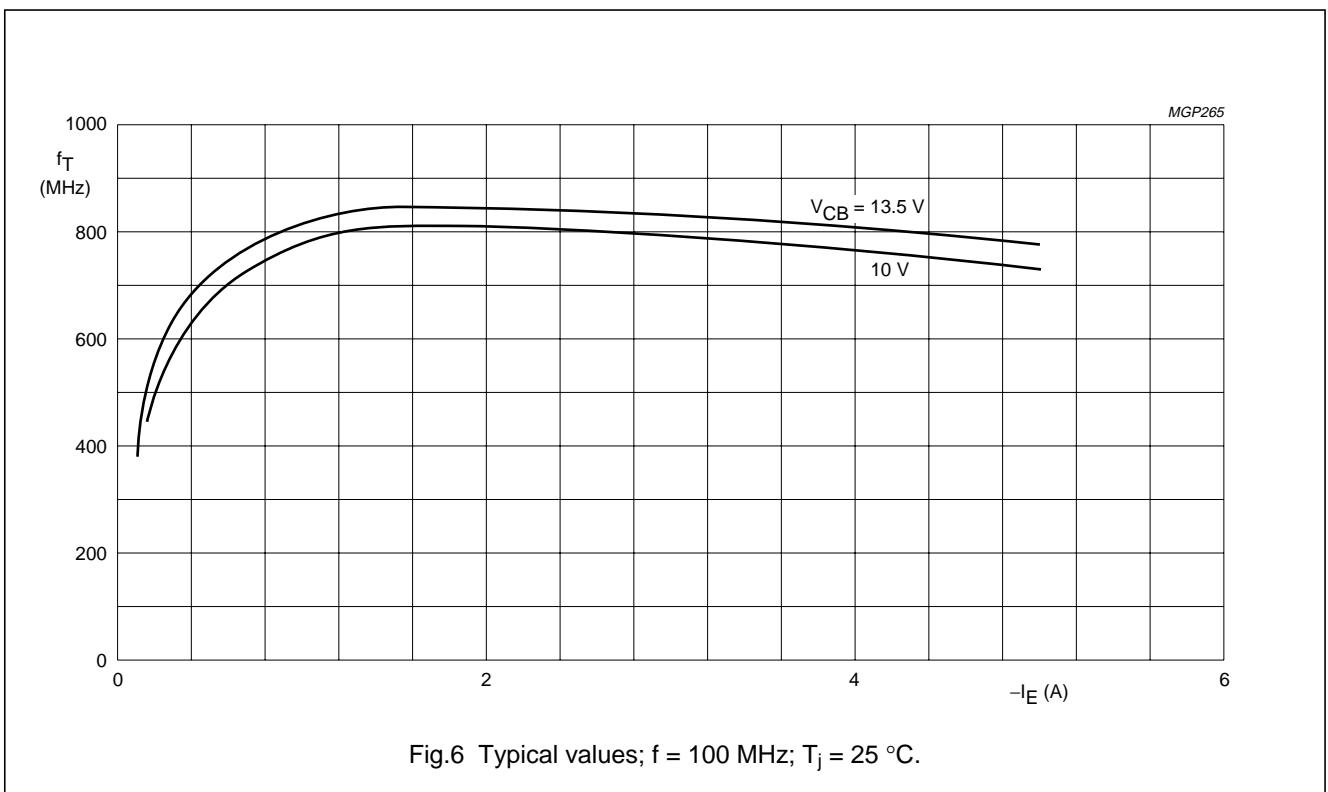
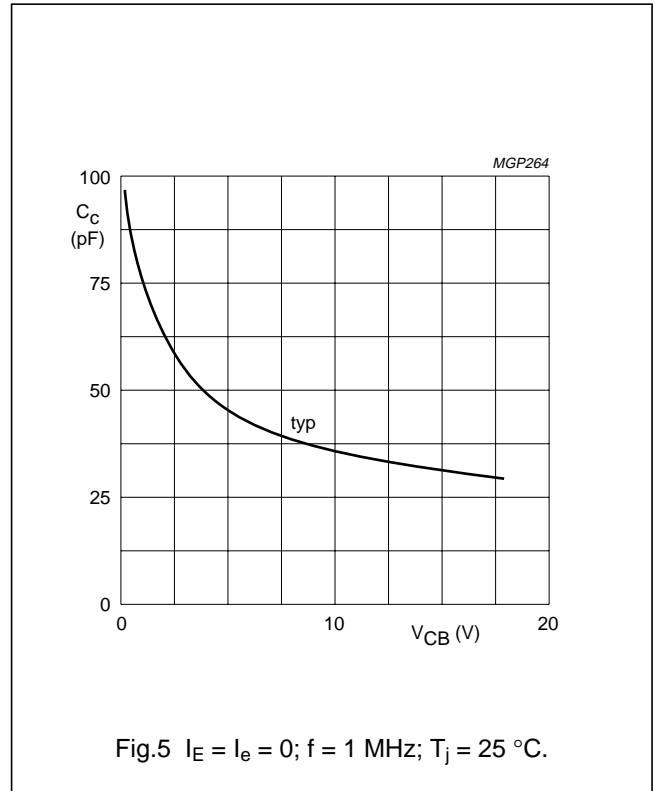
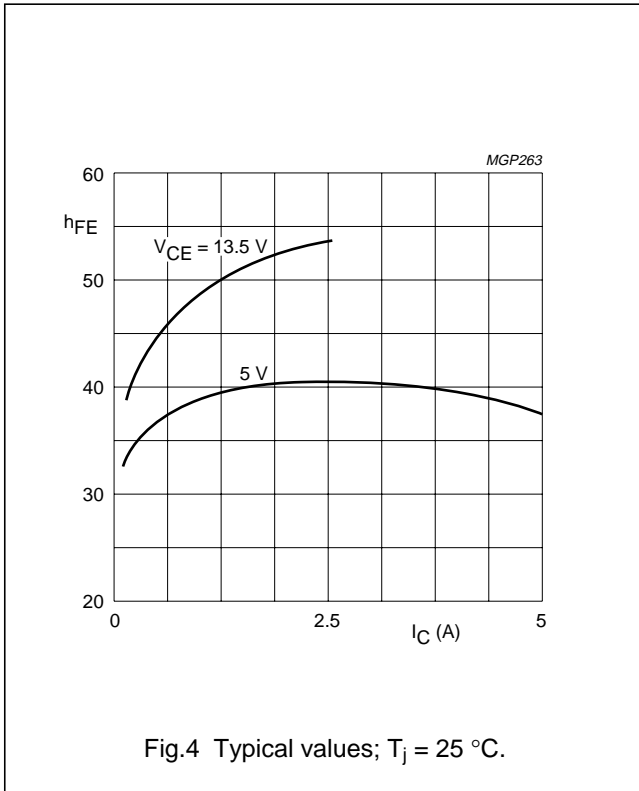
 $E_{SBO} > 2,5\text{ mJ}$ $R_{BE} = 10\ \Omega$ $E_{SBR} > 2,5\text{ mJ}$ D.C. current gain ⁽¹⁾ $I_C = 1,5\text{ A}; V_{CE} = 5\text{ V}$ h_{FE} typ. 40
10 to 100Collector-emitter saturation voltage ⁽¹⁾ $I_C = 4,5\text{ A}; I_B = 0,9\text{ A}$ V_{CEsat} typ. 1,0 VTransition frequency at $f = 100\text{ MHz}$ ⁽¹⁾ $-I_E = 1,5\text{ A}; V_{CB} = 13,5\text{ V}$ f_T typ. 850 MHz $-I_E = 4,5\text{ A}; V_{CB} = 13,5\text{ V}$ f_T typ. 800 MHzCollector capacitance at $f = 1\text{ MHz}$ $I_E = I_e = 0; V_{CB} = 13,5\text{ V}$ C_c typ. 32 pFFeedback capacitance at $f = 1\text{ MHz}$ $I_C = 200\text{ mA}; V_{CE} = 13,5\text{ V}$ C_{re} typ. 23 pF

Collector-flange capacitance

 C_{cf} typ. 2 pF**Note**1. Measured under pulse conditions: $t_p \leq 200\ \mu\text{s}; \delta \leq 0,02$.

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APPLICATION INFORMATION

R. F. performance in c.w. operation (unneutralized common-emitter class-B circuit)

 $T_h = 25\text{ }^\circ\text{C}$

f (MHz)	V_{CE} (V)	P_L (W)	P_S (W)	G_p (dB)	I_C (A)	η (%)	\bar{z}_i (Ω)	\bar{Y}_L (mS)
175	13,5	15	< 2,4	> 8,0	< 1,85	> 60	2,3 + j2,2	130 - j4,4
175	12,5	15	-	typ. 7,5	-	typ. 67	-	-

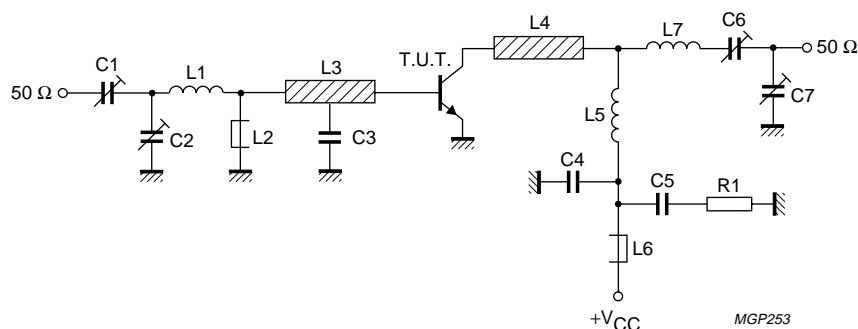


Fig.7 Test circuit; c.w. class-B.

List of components:

C1 = 2,5 to 20 pF film dielectric trimmer (cat. no. 2222 809 07004)

C2 = C6 = 4 to 40 pF film dielectric trimmer (cat. no. 2222 809 07008)

C3 = 47 pF ceramic capacitor (500 V)

C4 = 120 pF ceramic capacitor (500 V)

C5 = 100 nF polyester capacitor

C7 = 5 to 60 pF film dielectric trimmer (cat. no. 2222 809 07011)

L1 = 2 turns Cu wire (1,6 mm); int. dia. 4,5 mm; length 5,7 mm; leads 2 x 5 mm

L2 = L6 = Ferroxcube wide-band h.f. choke, grade 3B (cat. no. 4312 020 36640)

L3 = L4 = strip (12 mm x 6 mm); tap for C3 at 5 mm from transistor

L5 = 3 turns Cu wire (1,6 mm); int. dia. 7,5 mm; length 7,5 mm; leads 2 x 5 mm

L7 = 3 turns Cu wire (1,6 mm); int. dia. 6,5 mm; length 7,4 mm; leads 2 x 5 mm

L3 and L4 are strips on a double Cu-clad printed-circuit board with epoxy fibre-glass dielectric, thickness 1/16".

R1 = 10 Ω carbon resistor

Component layout and printed-circuit board for 175 MHz test circuit see Fig.8.

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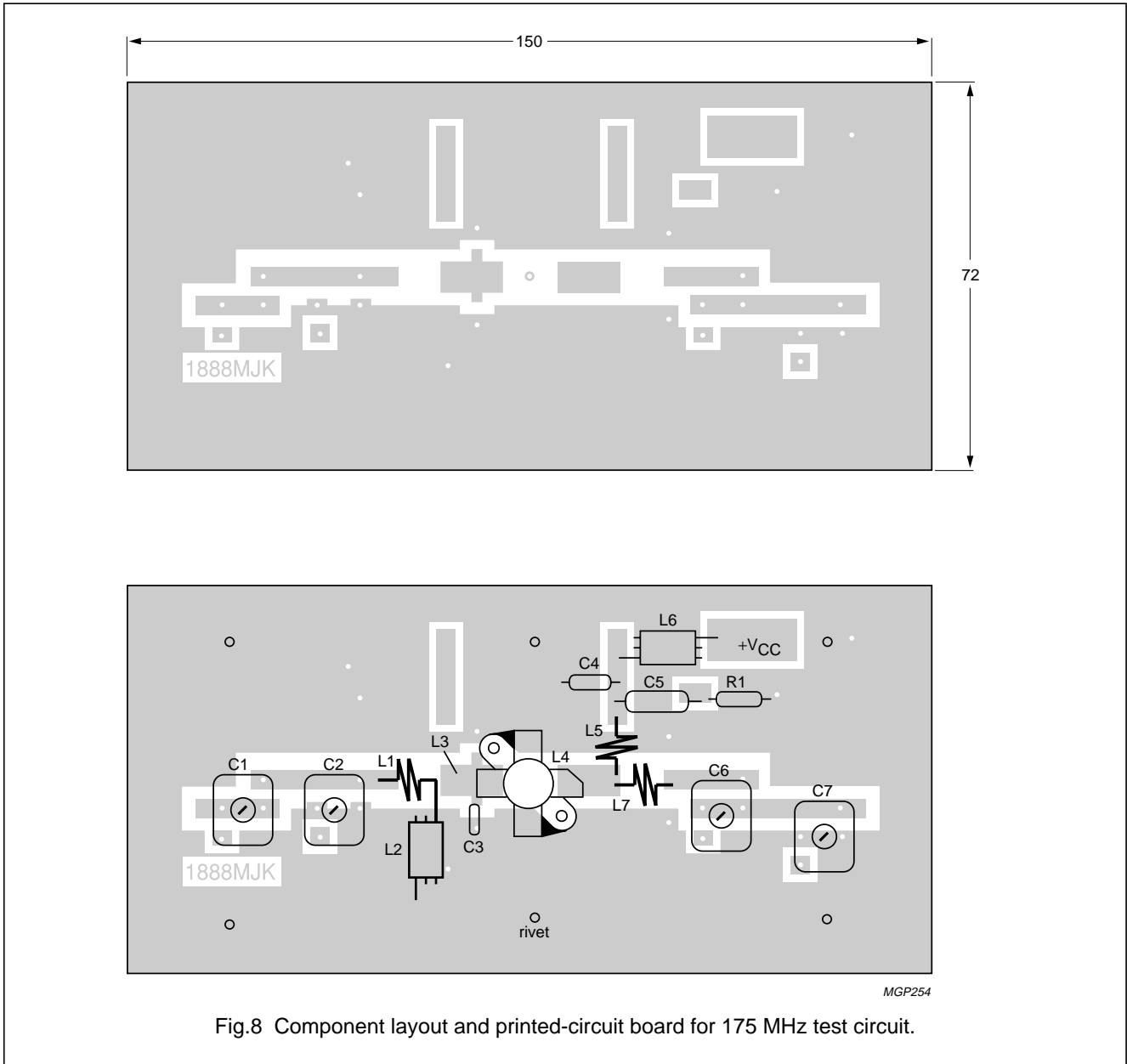
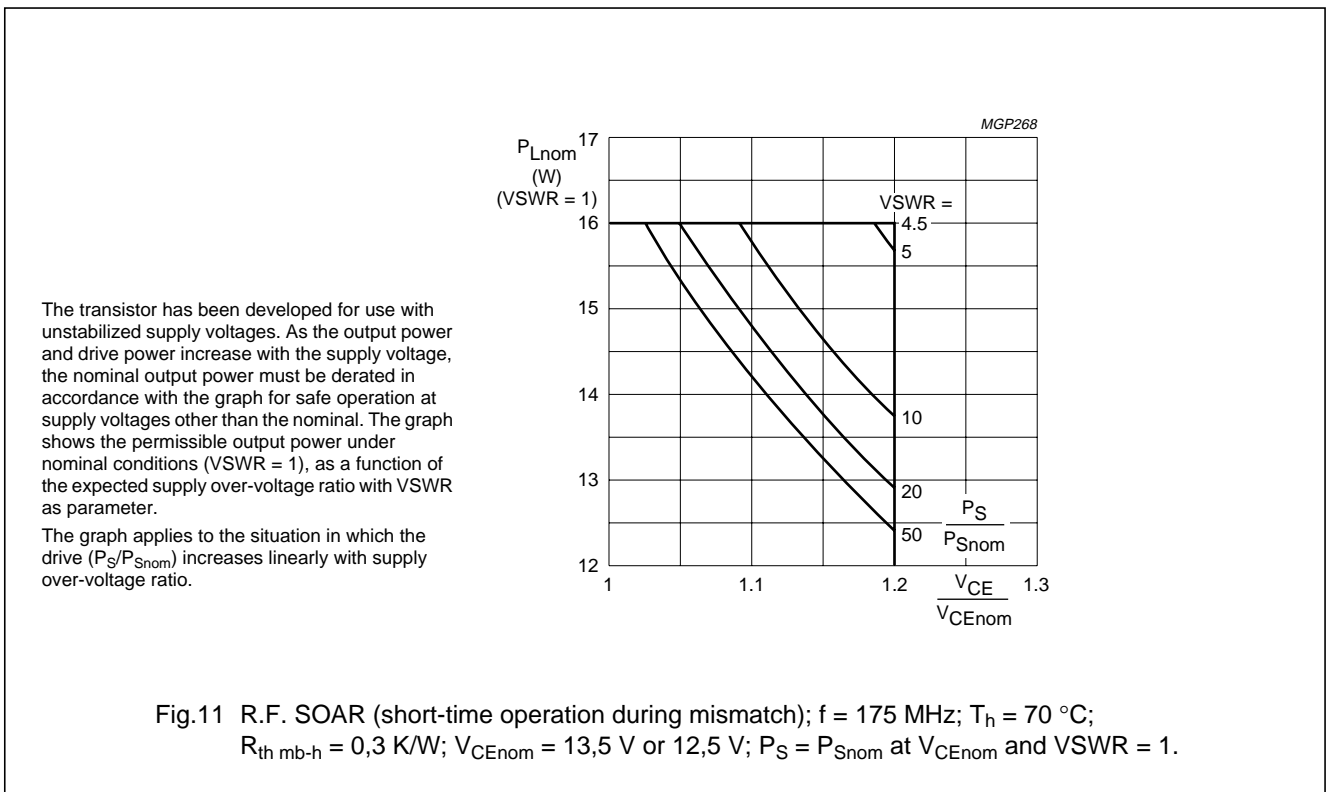
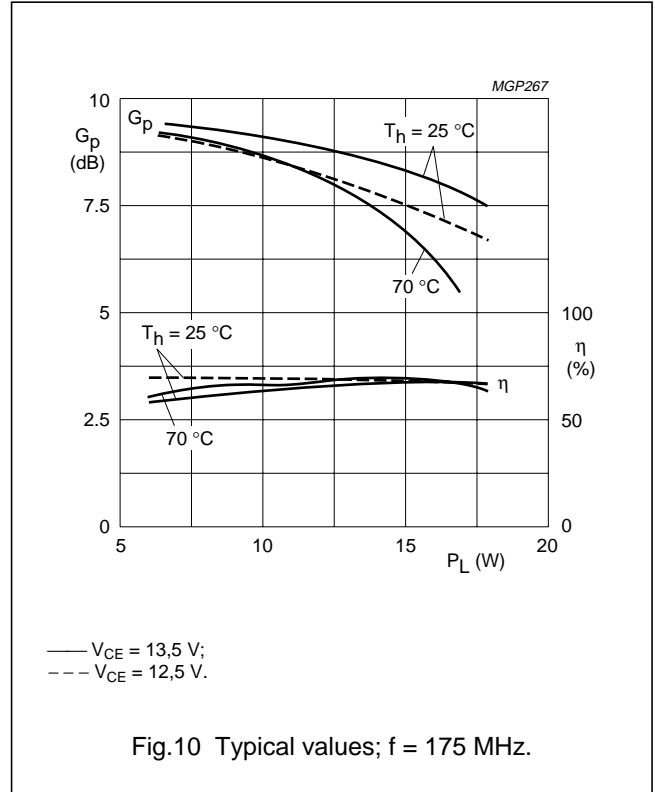
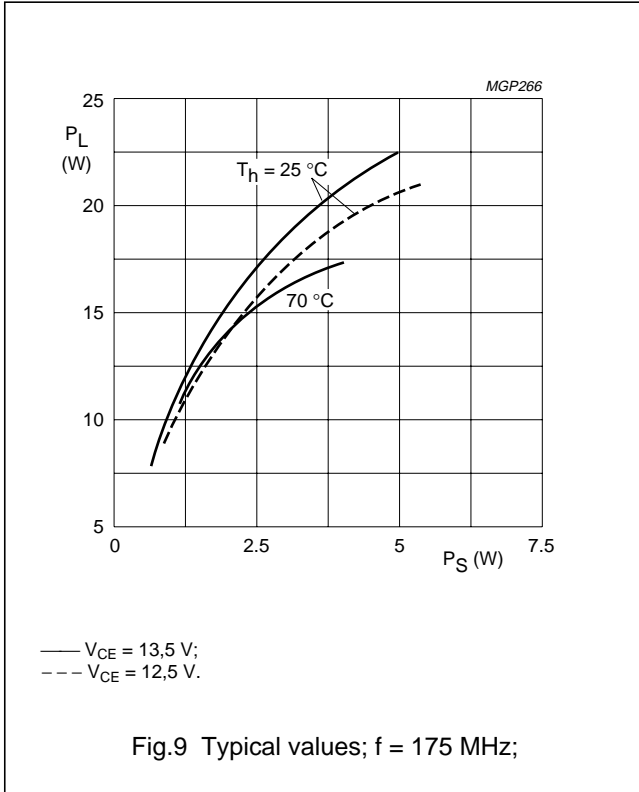


Fig.8 Component layout and printed-circuit board for 175 MHz test circuit.

The circuit and the components are situated on one side of the epoxy fibre-glass board, the other side being fully metallized to serve as earth. Earth connections are made by means of hollow rivets, whilst under the emitter leads Cu straps are used for a direct contact between upper and lower sheets.

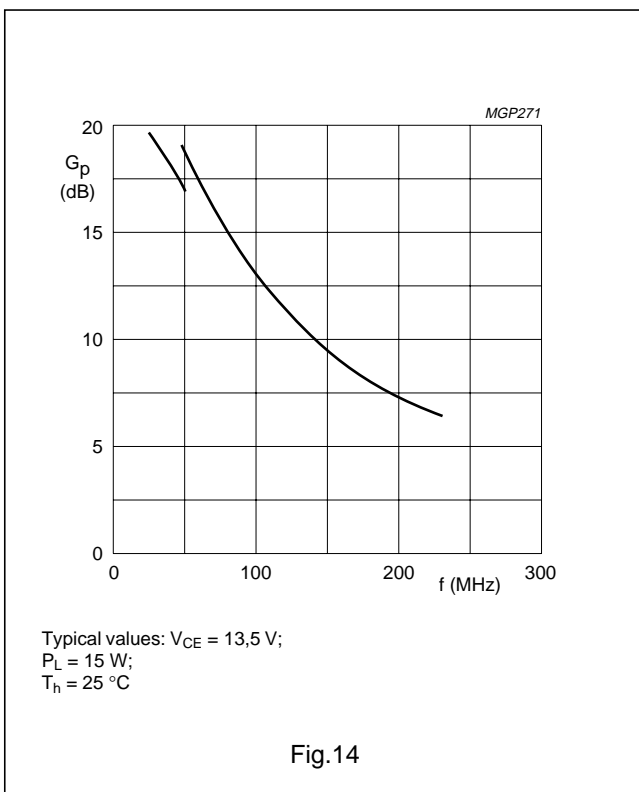
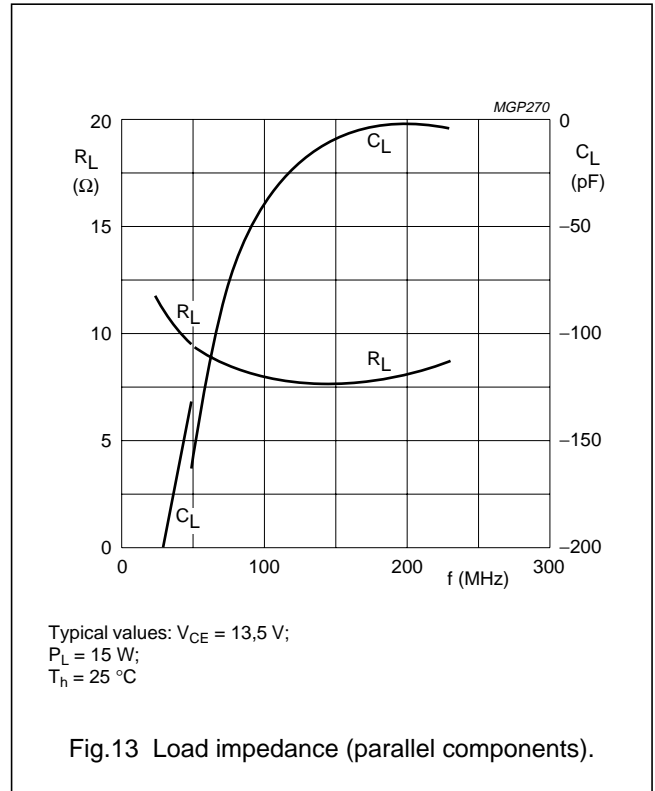
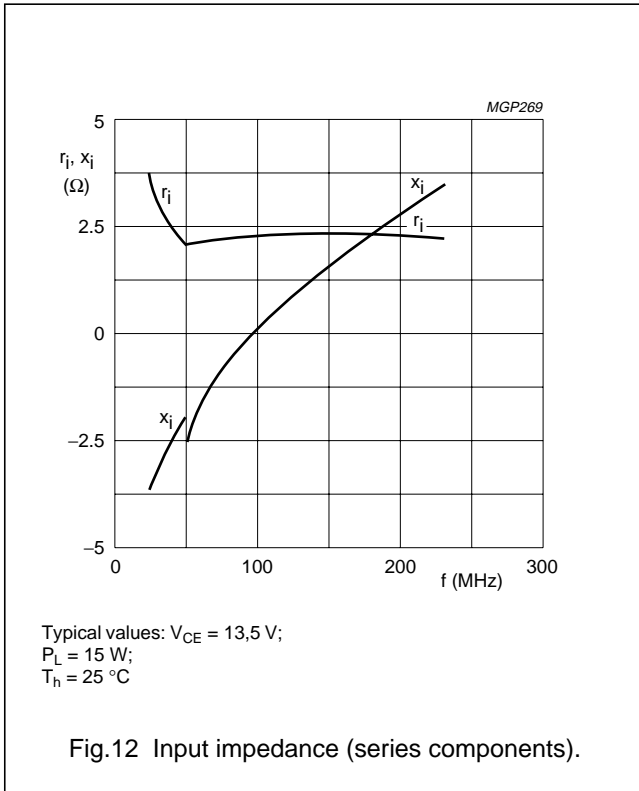
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OPERATING NOTE

Below 50 MHz a base-emitter resistor of $10 \text{ } \Omega$ is recommended to avoid oscillation. This resistor must be effective for r.f. only.

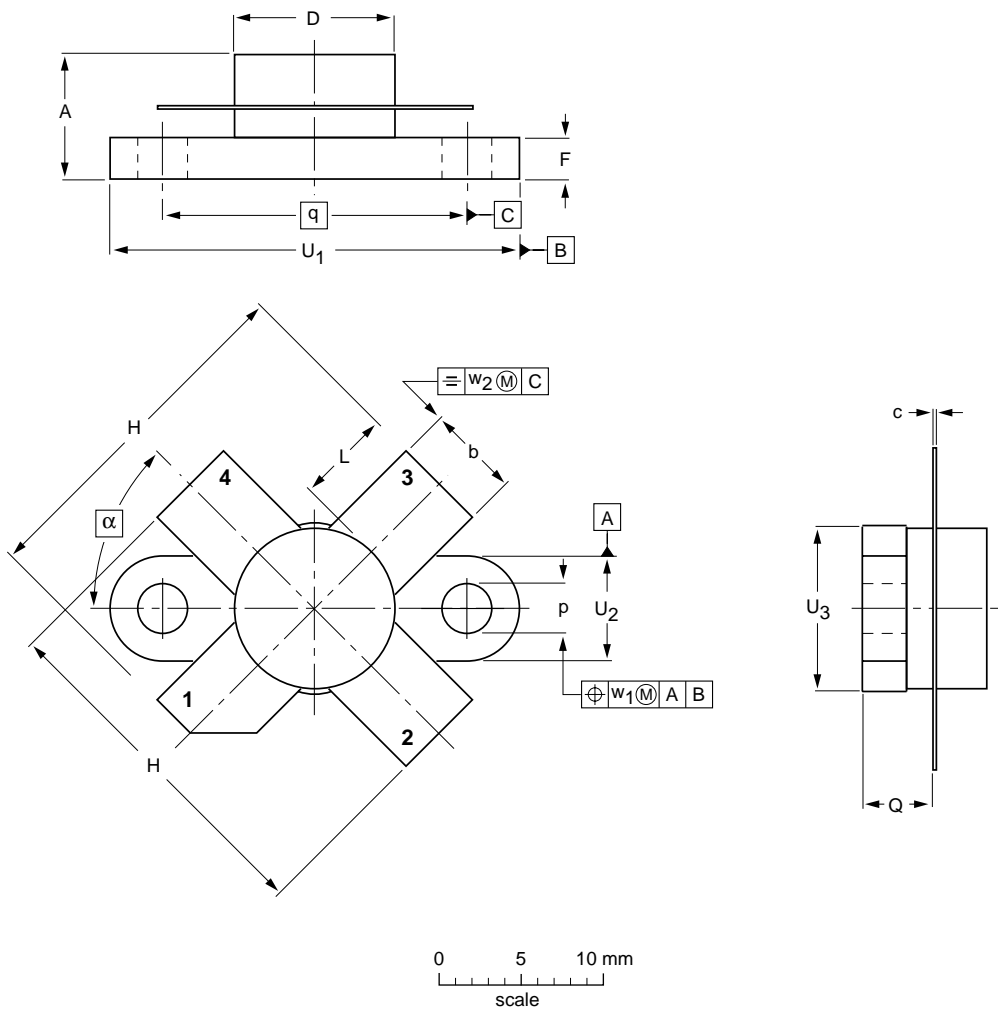
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PACKAGE OUTLINE

Flanged ceramic package; 2 mounting holes; 4 leads

SOT123A



DIMENSIONS (millimetre dimensions are derived from the original inch dimensions)

UNIT	A	b	c	D	D ₁	F	H	L	p	Q	q	U ₁	U ₂	U ₃	w ₁	w ₂	α
mm	7.47 6.37	5.82 5.56	0.18 0.10	9.73 9.47	9.63 9.42	2.72 2.31	20.71 19.93	5.61 5.16	3.33 3.04	4.63 4.11	18.42	25.15 24.38	6.61 6.09	9.78 9.39	0.51	1.02	45°
inches	0.294 0.251	0.229 0.219	0.007 0.004	0.383 0.373	0.397 0.371	0.107 0.091	0.815 0.785	0.221 0.203	0.131 0.120	0.182 0.162	0.725	0.99 0.96	0.26 0.24	0.385 0.370	0.02	0.04	

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
SOT123A					97-06-28

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DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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