DISCRETE SEMICONDUCTORS

DATA SHEET

BLV92 UHF power transistor

Product specification

March 1993





UHF power transistor

BLV92

DESCRIPTION

N-P-N silicon planar epitaxial transistor primarily intended for use in mobile radio transmitters in the 900 MHz communications band.

FEATURES

- multi-base structure and emitter-ballasting resistors for an optimum temperature profile
- internal input matching to achieve an optimum wideband capability and high power gain
- gold metallization ensures excellent reliability.

The transistor has a 6-lead flange envelope with a ceramic cap (SOT-171). All leads are isolated from the flange.

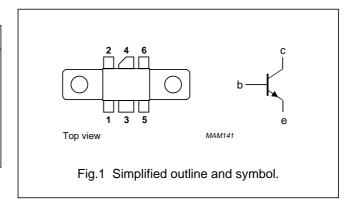
QUICK REFERENCE DATA

R.F. performance at $T_h = 25$ °C in a common-emitter class-B test circuit

MODE OF OPERATION	V _{CE} V	f MHz	P _L W		G _P dB	η c %		
parrow band: a w	12,5	900	4	>	7,5	>	50	_
narrow band; c.w.	9,6	900	3	typ.	7,3	typ.	56	

PINNING - SOT171A

PIN	SYMBOL	DESCRIPTION
1	е	emitter
2	е	emitter
3	b	base
4	С	collector
5	е	emitter
6	е	emitter



WARNING

Product and environmental safety - toxic materials

This product contains beryllium oxide. The product is entirely safe provided that the BeO disc is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

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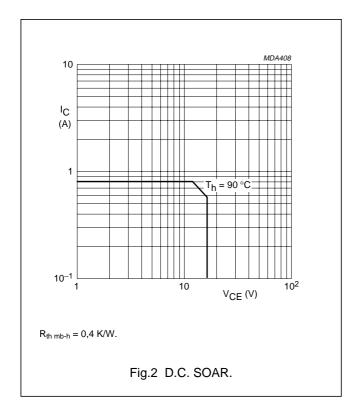
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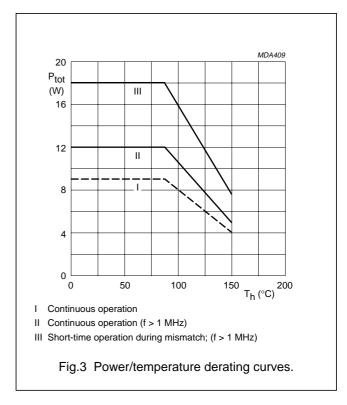
RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Collector-base voltage (open emitter)

peak value	V_{CBOM}	max.	36	V
Collector-emitter voltage (open base)	V_{CEO}	max.	16	V
Emitter-base voltage (open collector)	V_{EBO}	max.	3	V
Collector current				
d.c. or average	I_{C}	max.	0,8	Α
(peak value); f > 1 MHz	I _{CM}	max.	2,4	Α
Total power dissipation				
at T _{mb} = 94 °C	P _{tot(dc)}	max.	9	W
at $T_{mb} = 94 ^{\circ}C$; f > 1 MHz	$P_{tot(rf)}$	max.	12	W
Storage temperature	T_{stg}	-65 to +	150	°С
Operating junction temperature	T _j	max.	200	°С





THERMAL RESISTANCE

Dissipation = 6 W; T_{mb} = 128 °C

From junction to mounting base

(d.c. dissipation)

(r.f. dissipation)

From mounting base to heatsink

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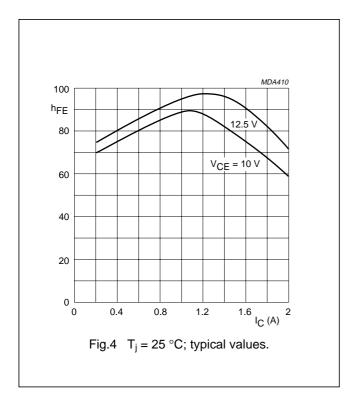
CHARACTERISTICS

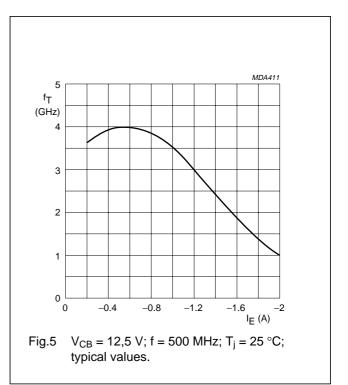
 $T_j = 25$ °C unless otherwise specified

·			
Collector-base breakdown voltage, open emitter; I _C = 10 mA	$V_{(BR)CBO}$	>	36 V
Collector-emitter breakdown voltage, open base; I _C = 20 mA	$V_{(BR)CEO}$	>	16 V
Emitter-base breakdown voltage, open collector; I _E = 1 mA	$V_{(BR)EBO}$	>	3 V
Collector cut-off current, V _{BE} = 0; V _{CE} = 16 V	I _{CES}	<	5 mA
Second breakdown energy, L = 25 mH; f = 50 Hz; R_{BE} = 10 Ω	E_SBR	>	1 mJ
D.C. current gain, $I_C = 0.6 A$; $V_{CE} = 10 V$	h_{FE}	>	25
Transition frequency at f = 500 MHz ⁽¹⁾ , $-I_E = 0.6$ A; $V_{CE} = 12.5$ V	f_T	typ.	4 GHz
Collector capacitance at f = 1 MHz, $I_E = i_e = 0$; $V_{CB} = 12.5 \text{ V}$	C_c	typ.	8 pF
Feed-back capacitance at f = 1 MHz, $I_C = 0$; $V_{CE} = 12.5 \text{ V}$	C_{re}	typ.	5 pF
Collector-flange capacitance	C_{cf}	typ.	2 pF

Note

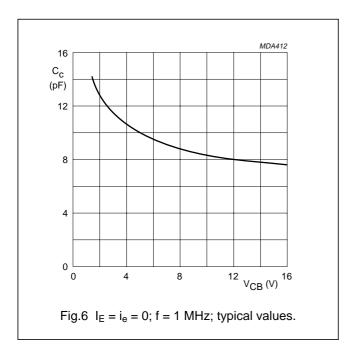
1. Measured under pulse conditions: t_p = 50 μ s; δ < 1%.





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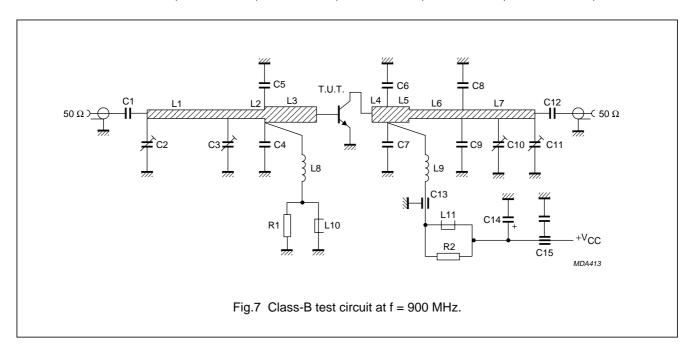
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APPLICATION INFORMATION

R.F. performance in c.w. operation (common-emitter circuit; class-B): f = 900 MHz; $T_h = 25$ °C.

MODE OF OPERATION	V _{CE} V	P _L W		P _S W		G _P dB		I _C A		ης %
	10.5	4	<	0,71	>	7,5	<	0,64	>	50
narrow band; c.w.	12,5	4	typ.	0,57	typ.	8,5	typ.	0,56	typ.	57
	9,6	3	typ.	0,56	typ.	7,3	typ.	0,56	typ.	56



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List of components:

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C1 = C12 = 33 pF multilayer ceramic chip capacitor
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$$C2 = C3 = C10 = C11 = 1,4 \text{ to } 5,5 \text{ pF film dielectric trimmer (cat. no. 2222 809 09001)}$$

C4 = C5 = 3,9 pF multilayer ceramic chip capacitor⁽¹⁾

C13 = 10 pF ceramic feed-through capacitor

C14 = $6.8 \mu F$ (63 V) electrolytic capacitor

C15 = 330 pF ceramic feed-through capacitor

L1 = 50 Ω stripline (29,5 mm \times 2,4 mm)

L2 = 50 Ω stripline (5,5 mm \times 2,4 mm)

L3 = 42,7 Ω stripline (16,8 mm \times 3,0 mm)

L4 = 42,7 Ω stripline (7,5 mm \times 3,0 mm)

L5 = 42,7 Ω stripline (2,0 mm \times 3,0 mm)

L6 = 50 Ω stripline (8,5 mm \times 2,4 mm)

L7 = 50 Ω stripline (28,0 mm \times 2,4 mm)

L8 = 60 nH; 4 turns closely wound enamelled Cu-wire (0,4 mm); int. dia. 3 mm; leads 2 × 5 mm

L9 = 45 nH; 4 turns enamelled Cu-wire (1,0 mm); length 6 mm; int. dia. 4 mm; leads $2 \times 5 \text{ mm}$

L10 = L11 = Ferroxcube wideband h.f. choke, grade 3B (cat. no. 4312 020 36642)

R1 = R2 = 10 $\Omega \pm$ 10%; 0,25 W, metal film resistor

L1 to L7 are striplines on a double Cu-clad printed circuit board with P.T.F.E. fibre-glass dielectric (ε_r = 2,2); thickness $\frac{1}{32}$ inch.

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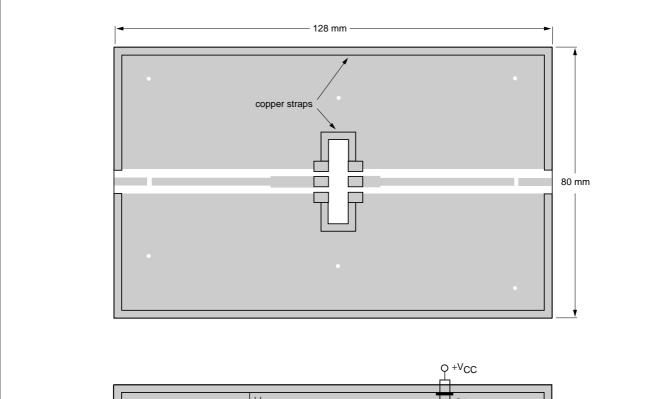
Note

1. American Technical Ceramics capacitors type 100A or capacitor of same quality.

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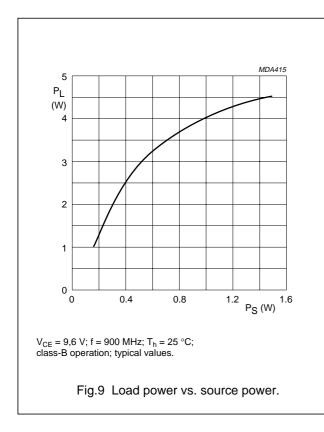
The circuit and the components are on one side of the P.T.F.E. fibre-glass board; the other side is unetched copper serving as ground plane. Earth connections are made by fixing screws and copper straps around the board and under the emitters to provide a direct contact between the copper on the component side and the ground plane.

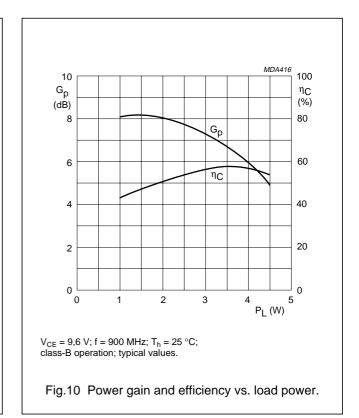
Fig.8 Printed circuit board and component lay-out for 900 MHz class-B test circuit.

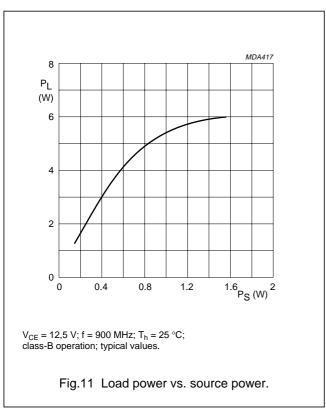
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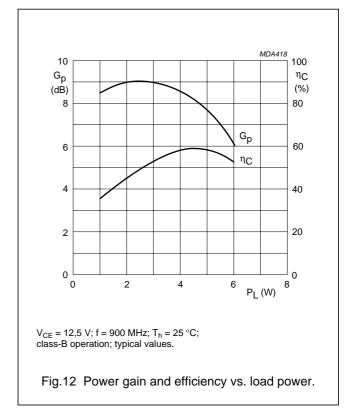
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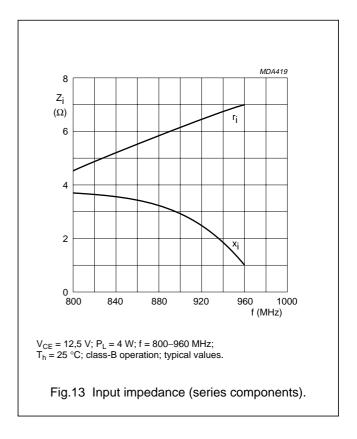
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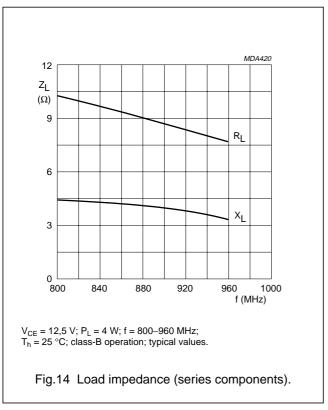
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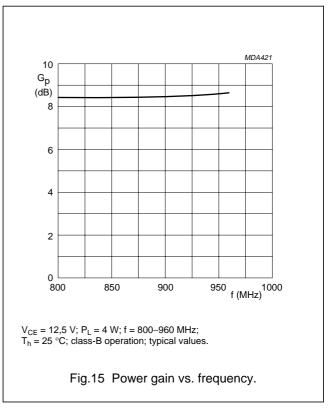
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RUGGEDNESS

The device is capable of withstanding a full load mismatch (VSWR = 50; all phases) at rated load power up to a supply voltage of 15,5 V and at T_h = 25 °C.







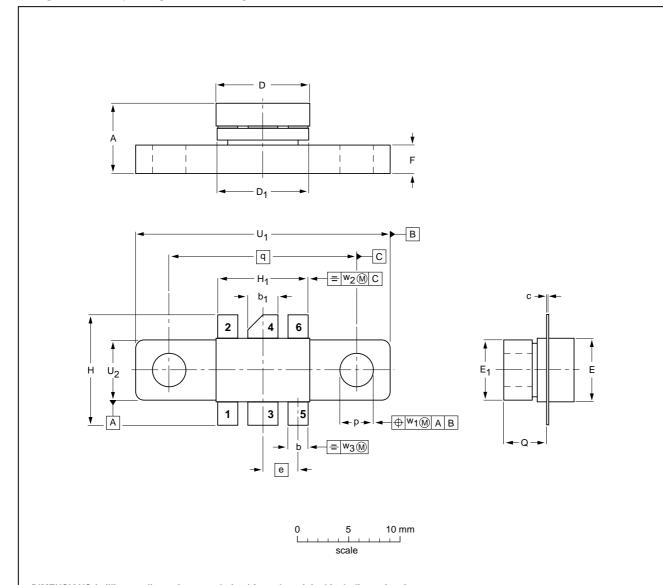
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PACKAGE OUTLINE

Flanged ceramic package; 2 mounting holes; 6 leads

SOT171A



DIMENSIONS (millimetre dimensions are derived from the original inch dimensions)

UN	IT	Α	b	b ₁	С	D	D ₁	E	E ₁	е	F	Н	Н ₁	р	Q	q	U ₁	U ₂	w ₁	w ₂	w ₃
mı	n I	6.81 6.07	2.15 1.85	3.20 2.89	0.16 0.07	9.25 9.04	9.30 8.99	5.95 5.74	6.00 5.70	3.58	3.05 2.54	11.31 10.54		3.43 3.17	4.32 4.11	18.42	24.90 24.63	6.00 5.70	0.51	1.02	0.26
inch							0.366 0.354			0.140	0.120 0.100	0.445 0.415	0.365 0.355	0.135 0.125	0.170 0.162	0.725	0.980 0.970	0.236 0.224	0.02	0.04	0.01

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT171A					97-06-28

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DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

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