

CATTANA FI FOTRONICO CORRODATION

TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

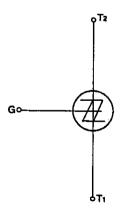
MAXIMUM RATINGS

Symbol	Parameter	Conditions	Rat	T	
			BS08D	BS08E	Unii
<u>іт</u>	DC On Current	Ta = 25*C	175	100	mА
	Repetitive Peak On-Current	1% duty,tw=10µs,Ta = 100°C	1	1	A
	Not Repetitive Peak On-Current	tw=10µs,Ta = 25°C	2	2	A
P	On-State Dissipation	Ta = 25°C	450	150	mW
la	DC Gate Current			5	
Tj	Junction temperature	· · · · · · · · · · · · · · · · · · ·	-55 to +125		mA C
Tetg	Storage temperature		-55 to	- .	

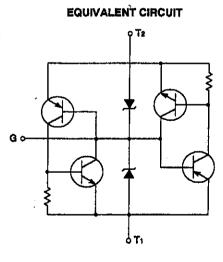
ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test conditions	Limits			11-14
			Min	Тур	Max	Unit
Vs	Switching voltage	Ta = 25°C	7	8	9	V
ls	Switching current	Ta = 25°C		—	200	μΑ
V\$1-V\$2	Switching voltage difference	Ta = 25°C			0.5	V
	Switching current difference	Ta = 25°C		_	100	μA
1H	Holding current	Ta = 25°C			1.5	mA
In .	Off current	VD =5V, Ta = 25°C	- 1	_	1.0	μA
D		Vp =5V, Ta = 85°C	i	<u> </u>	10	
	Switching voltage temperature coefficient	Ta =-55°C to +85°C		±0.01		%/°C
VT	On voltage	IT =175mA, Ta = 25°C			1.4	v
lgt	Gate trigger current	Vp =5V, Ta = 25°C	10		200	μA
Vgd	Gate not trigger voltage	Vp =5V, Ta = 85°C	0.2			v

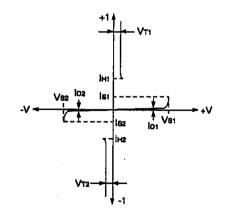
SYMBOL



.

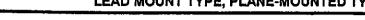


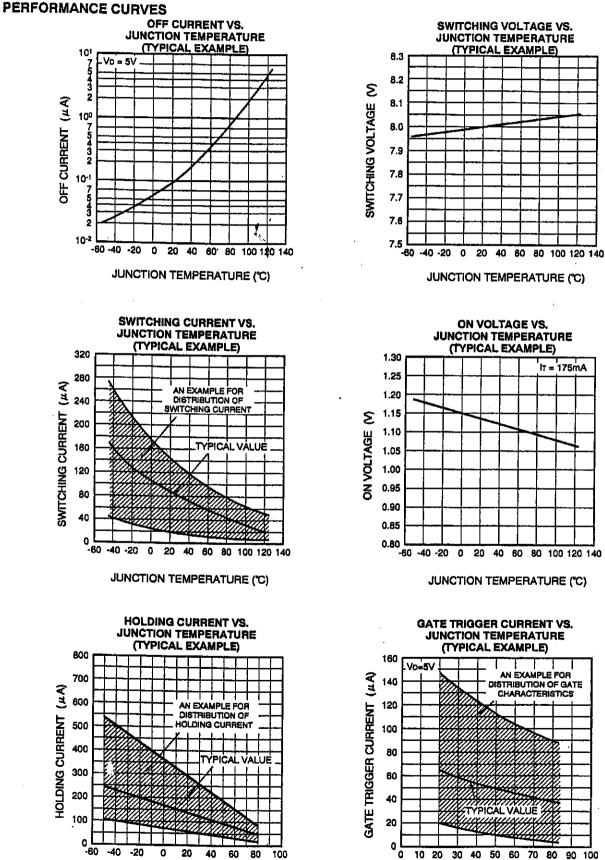
STATIC CHARACTERISTICS



BS08D, BS08E







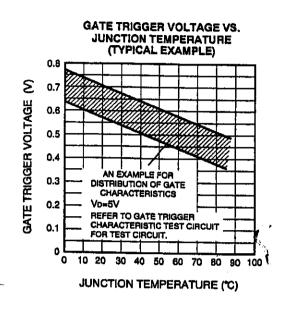
Ις Λ Π Λ ν Λ EI E/ ντρηλ DAD ATION

JUNCTION TEMPERATURE (°C)

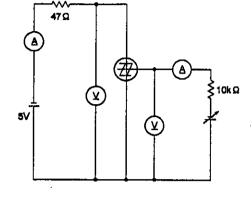
JUNCTION TEMPERATURE (°C)

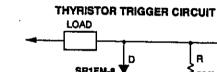
BS08D, BS08E

TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

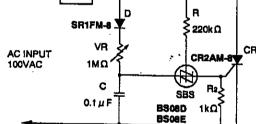


GATE TRIGGER CHARACTERISTIC TEST CIRCUIT

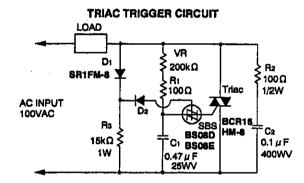




APPLICATION EXAMPLE



The above circuit is a thyristor phase control circuit making use of an \bigcirc . In this circuit, using the SBS gate, the residual charge on C is reset, reducing the hysteresis characteristics. Therefore, over the range of the variable resistor, phase control (in the range 5 to 175°C) is possible, making this circuit widely useful in DC motor control and other control applications,



The above circuit is a triac phase control circuit making use of an SBS. In this circuit, an SBS gate is used to reduce the hysteresis characteristics. Thus, by using the variable resistance, phase control is possible over the wide range of 10 to 160 °C. Therefore, this circuit is widely usable in such applications as lighting control circuits, electric heater control, and other load control applications.

ISAHAYA ELECTRONICS CORPORATION

BS08D,BS08E

TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

Constant-speed control circuit of an universal motor FEATURES

- The feedback amount is automatically controlled by the motor speed for easy constant-speed operation.
- (2) The feedback amount required for each motor type can be adjusted by resistor VR2, thus enabling control of various types of motors.

OPERATING PRINCIPLE

The speed of motor is adjusted by VR1 in the phase-shifting circuit. In the comparison circuit, the reference voltage supplied by the Zener diode and the armature voltage are compared, and C in the phase-shifting circuit is charged by the difference of voltages. The effect of feedback is negligible as the sum of VR1 and C is small during high-speed operation, but, during low-speed operation, when the sum of VR1 and C is large, even at small feedback is effective and constant operation is improved at low-speed operation.(Patened by Mitsubishi Electric)

Gas/Petroleum ignition circuit FEATURES

- (1)When the power supply is in the negative half cycle, spark discharge occurs. Compact and small-capacity resistor R1 and a thyristor can be used as the current does not flow from the power supply of the thyristor.
- (2) High-voltage pulses of more than 14kV are output to cause certain ignition of gas or kerosine.

OPERATING PRINCIPLE

Capacitor C1 is charged through R1 and D1 during the half cycle of positive power supply and C2 is charged through R2 using the reverse voltage applied to D1. In this case, R1, R2, C1 and C2 should be selected to make R1 C1 << R2 C2,SBS is turned on as C2 reaches the switching voltage after C1 is fully charged, and the gate current flows to the thyristor CR. The electric charge charged in C1 is instantly discharged through CR, and after C1 is charged with the reverse polarity, it is discharged again through D2 and the coil, and then C1 is charged again with the original polarity. Thus high voltage is generated (more than 14kV) on the secondary side of the coil by the current flowing through the coil to produce spark discharge at the discharge gap.

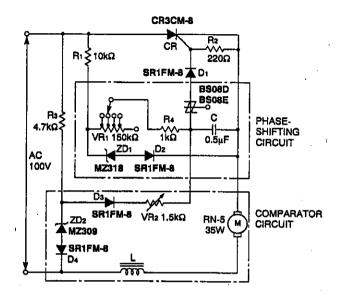
Electric foot warmer (kotatsu) FEATURES

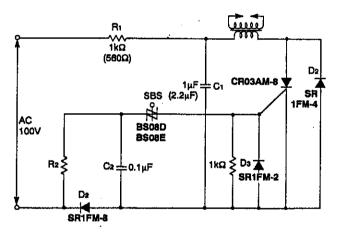
Non-stage and wide-range control of the temperature of kotatsu is possible.

OPERATING PRINCIPLE

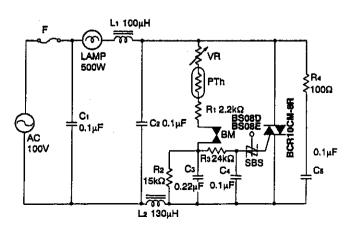
The temperature is controlled by trigger puises generated by VR, PTh, R1, BM, R² and C4 and by the control of the trigger phase of triac BCR. If the temperature exceeds a specified value after the temperature is set by VR, the resistive value of positive-type thermistor PTh increases and the conductive angle of BCR becomes smaller and so the temperature fails. If the value decreases, the conductive angle becomes targer and the temperature rises.

The bimetal switch BM detects sudden temperature rises and turns the BCR to an off state and stops the power supply. R2 is provided to prevent re-triggering of SBS.





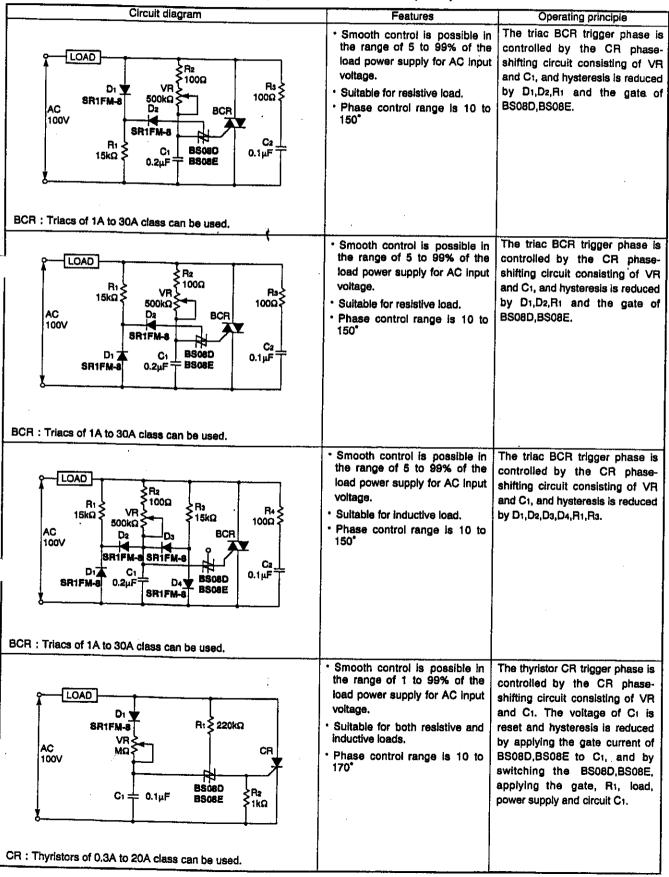




(TRIGGER ELEMENT) BS08D,BS08E

TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

Example of a basic phase control circuit using BS08D,BS08E and BCR (or CR)



ISAHAVA ELECTRONICS CODDODATION

(TRIGGER ELEMENT) BS08D,BS08E

TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

Electric starter for fluorescent lamps FEATURES

Employment of a non-linear saturable capacitor, triac and reverseblocking two-terminal thyristor provides a cheap, compact and light-weight electric starter for fluorescent lamp with short turn-on time.

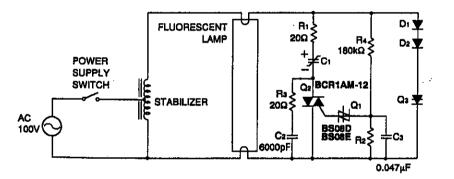
OPERATING PRINCIPLE

If the power supply switch is turned on, the bilateral switching device Q1 is set on at a proper phase 81 of the positive half cycle of the power supply at the early stage of startup, and triac Q2 is triggered. If Q2 is turned on, non-linear saturable capacitor C1 with charge saturation characteristic under a specified charge voltage is charged quickly by the power supply through the stabilizer with polarity as illustrated.

C1 enters quickly into the saturation area and the current flowing to the stabilizer decreases instantly, and the high-voltage pulses of e = L (di/dt) are generated in the stabilizer. The reverse-blocking two-terminal thyristor Q3 is triggered by this pulse, and a pre-

heating current flows to the filament of the fluorescent lamp. The conducting current of Q3 becomes zero at the phase θ_2 of the negative half cycle of power supply, and Q3 is turned off. Then, the near-the-peak voltage of the negative half cycle of the supply voltage is suddenly applied at both ends of the fluorescent lamp, Q1 and Q2 are turned on again and C1 is charged quickly, with the reverse polarity as illustrated. With the same mechanism, high-voltage pulses (about 1.5kV) with the reverse polarity are generated in the stabilizer and the turn-on pulse is applied at both ends of the fluorescent lamp.

Then the same operation continues and the light is turned on if the filament is sufficiently heated. (Approximately 0.5 seconds after the power is supplied.) If the lamp is turned on, the voltage applied at both ends of the lamp decrease, and Q1, Q2 and Q3 are set off. Then preheating and high voltage pulses are stopped.



(CIRCUIT CONSTANTS ARE USED FOR 32W TYPE)

BS08D,BS08E

TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

DESCRIPTION

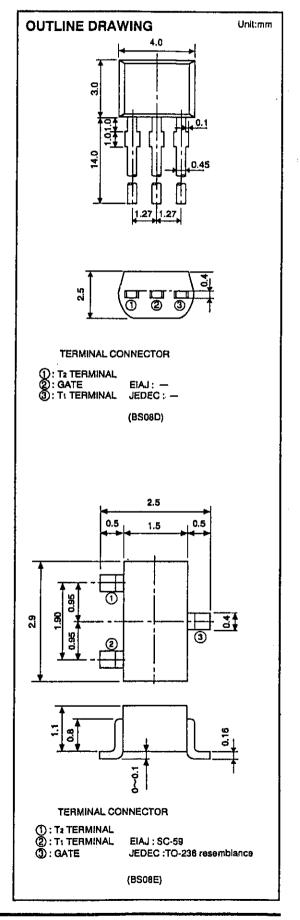
BS08D,BS08E is a silicon planer transistor, bilateral switching integrated circuit. It is suitable for trigger application of thyristor.

FEATURE

- ●Low switching voltage Vs =7 to 9V
- Good switching voltage temperature coefficient 0.01%/ °C
- With gate electrode, it is easy for control and synchronism of switching.

APPLICATION

Trigger circuit of thyristor • triac oscillator, timer.



ISAHAYA ELECTRONICS CORPORATION

* (TRIGGER ELEMENT) BS08D,BS08E

TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

MAXIMUM RATINGS

a. j

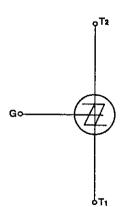
ції 1

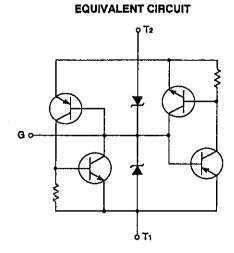
Symbol	Parameter	Conditions	Rat	1	
			BS08D	BS08E	Uni
<u>Iт</u>	DC On Current	Ta = 25°C	175	100	mA
	Repetitive Peak On-Current	1% duty,tw=10µs,Ta = 100°C	1	1.	A
	Not Repetitive Peak On-Current	tw=10µs,Ta = 25°C	2	2	A
Р	On-State Dissipation	Ta = 25*C	450	150	mW
la	DC Gate Current			5	mA
TI TI	Junction temperature		-55 to +125		°C
Tstg	Storage temperature		-55 to +125		1 °C

ELECTRICAL CHARACTERISTICS

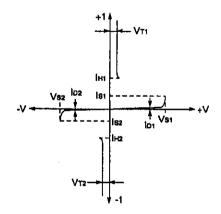
Symbol	Parameter	Test conditions		Limits		
			Min	Түр	Max	Unit
Vs	Switching voltage	Ta = 25°C	7	8	9	V
ls	Switching current	Ta = 25°C			200	μA
VS1-VS2	Switching voltage difference	Ta = 25°C			0.5	V
IS1-IS2	Switching current difference	Ta = 25°C			100	μA
ÌН	Holding current	Ta = 25°C			1.5	mA
lp	Off current	Vp =5V, Ta = 25*C			1.0	μA
0		Vp ≕5V, Ta = 85°C			10	
-	Switching voltage temperature coefficient	Ta =-55°C to +85°C		±0.01	_	%/°C
V۲	On voltage	Iτ =175mA, Ta = 25°C			1.4	V
lgt	Gate trigger current	Vb =5V, Ta = 25°C	10		200	μA
Vgd	Gate not trigger voltage	Vp =5V, Ta = 85°C	0.2			V

SYMBOL

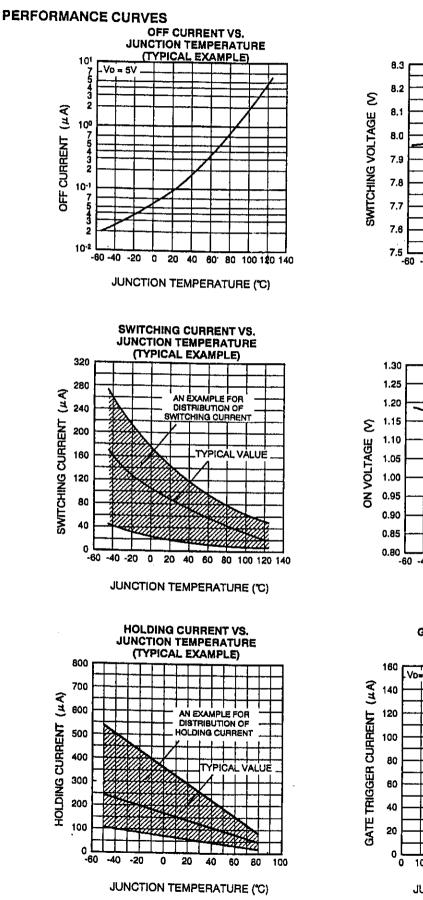


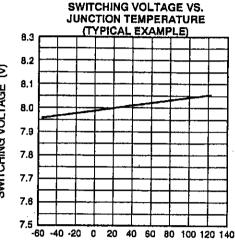


STATIC CHARACTERISTICS



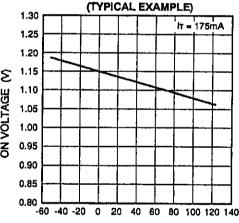
TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)





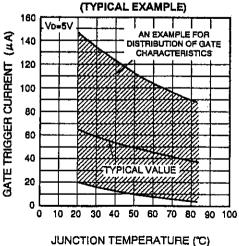
JUNCTION TEMPERATURE (°C)





JUNCTION TEMPERATURE (°C)

GATE TRIGGER CURRENT VS. JUNCTION TEMPERATURE

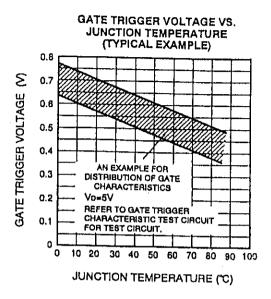


ΓΑΠΑΝΑ ΕΙΕΛΤΡΟΝΙΛΟ ΛΟΡΟΟΑΤΙΟΝ

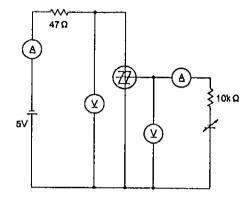
(IHIGGER ELEMENT)

BS08D,BS08E

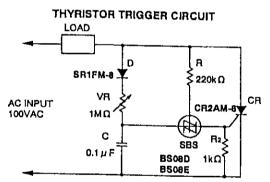
TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)



GATE TRIGGER CHARACTERISTIC TEST CIRCUIT

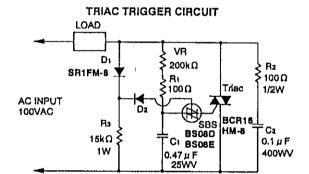


APPLICATION EXAMPLE



The above circuit is a thyristor phase control circuit making use of

BS¹. In this circuit, using the SBS gate, the residual charge on C is reset, reducing the hysteresis characteristics. Therefore, over the range of the variable resistor, phase control (in the range 5 to 175°C) is possible, making this circuit widely useful in DC motor control and other control applications.



The above circuit is a triac phase control circuit making use of an SBS. In this circuit, an SBS gate is used to reduce the hysteresis characteristics. Thus, by using the variable resistance, phase control is possible over the wide range of 10 to 160 °C. Therefore, this circuit is widely usable in such applications as lighting control circuits, electric heater control, and other load control applications.

ISAHAYA ELECTRONICS CORPORATION

TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

Constant-speed control circuit of an universal motor FEATURES

- (1) The feedback amount is automatically controlled by the motor speed for easy constant-speed operation.
- (2) The feedback amount required for each motor type can be adjusted by resistor VR2, thus enabling control of various types of motors.

OPERATING PRINCIPLE

The speed of motor is adjusted by VR1 in the phase-shifting circuit. In the comparison circuit, the reference voltage supplied by the Zener diode and the armature voltage are compared, and C in the phase-shifting circuit is charged by the difference of voltages. The effect of feedback is negligible as the sum of VR1 and C is small during high-speed operation, but, during low-speed operation, when the sum of VR1 and C is large, even arsmall feedback is effective and constant operation is improved at low-speed operation.(Patened by Mitsubishi Electric)

Gas/Petroleum ignition circuit FEATURES

- (1)When the power supply is in the negative half cycle, spark discharge occurs. Compact and small-capacity resistor R1 and a thyristor can be used as the current does not flow from the power supply of the thyristor.
- (2) High-voltage pulses of more than 14kV are output to cause certain ignition of gas or kerosine.

OPERATING PRINCIPLE

Capacitor C1 is charged through R1 and D1 during the half cycle of positive power supply and C2 is charged through R2 using the reverse voltage applied to D1. In this case, R1, R2, C1 and C2 should be selected to make R1 C1 << R2 C2,SBS is turned on as C2 reaches the switching voltage after C1 is fully charged, and the gate current flows to the thyristor CR. The electric charge charged in C1 is instantly discharged through CR, and after C1 is charged with the reverse polarity, it is discharged again through D2 and the coil, and then C1 is charged again with the original polarity. Thus high voltage is generated (more than 14kV) on the secondary side of the coll by the current flowing through the coil to produce spark discharge at the discharge gap.

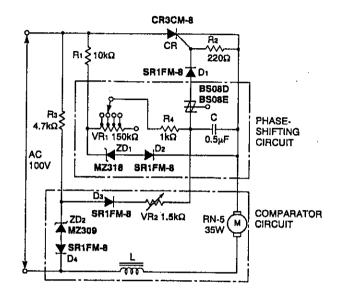
Electric foot warmer (kotatsu) FEATURES

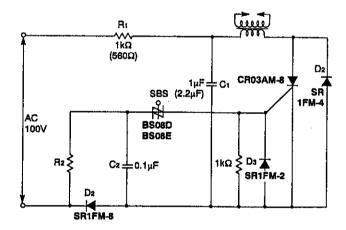
Non-stage and wide-range control of the temperature of kotatsu is possible.

OPERATING PRINCIPLE

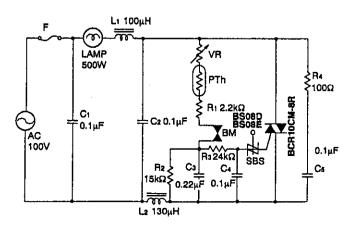
The temperature is controlled by trigger pulses generated by VR, PTh, R1, BM, R3 and C4 and by the control of the trigger phase of triac BCR. If the temperature exceeds a specified value after the temperature is set by VR, the resistive value of positive-type thermistor PTh increases and the conductive angle of BCR becomes smaller and so the temperature fails. If the value decreases, the conductive angle becomes larger and the temperature rises.

The bimetal switch BM detects sudden temperature rises and turns the BCR to an off state and stops the power supply. R2 is provided to prevent re-triggering of SBS.



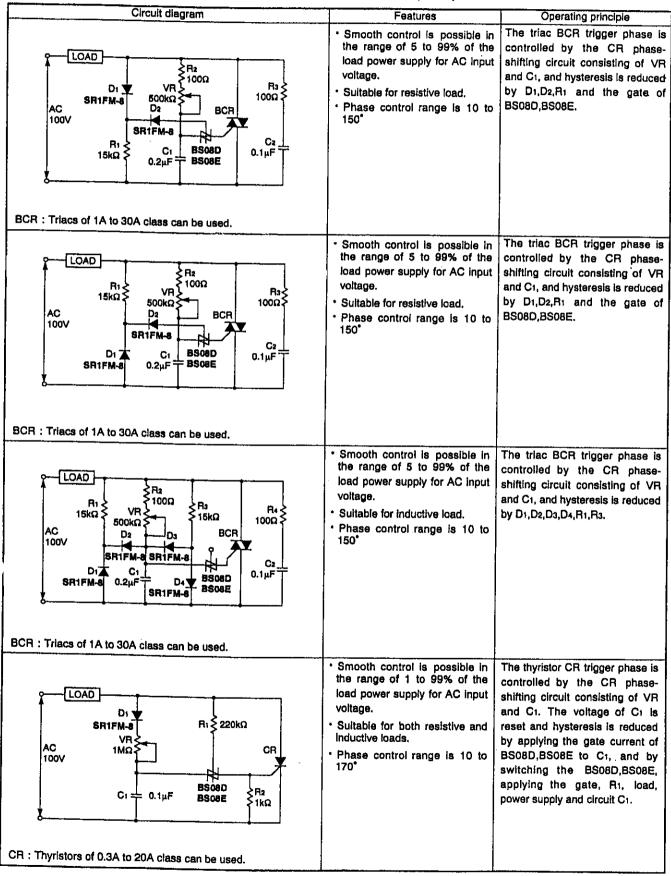






TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

Example of a basic phase control circuit using BS08D,BS08E and BCR (or CR)



BS08D,BS08E

TRIGGER APPLICATION LEAD MOUNT TYPE, PLANE-MOUNTED TYPE (SC-59 OUTLINE)

Electric starter for fluorescent lamps FEATURES

Employment of a non-linear saturable capacitor, triac and reverseblocking two-terminal thyristor provides a cheap, compact and light-weight electric starter for fluorescent lamp with short turn-ontime.

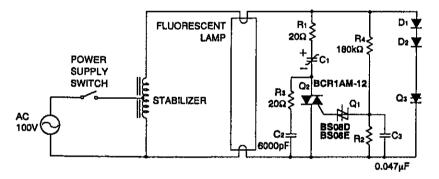
OPERATING PRINCIPLE

If the power supply switch is turned on, the bilateral switching device Q₁ is set on at a proper phase θ₁ of the positive half cycle of the power supply at the early stage of startup, and triac Q₂ is triggered. If Q₂ is turned on, non-linear saturable capacitor C₁ with charge saturation characteristic under a specified, charge voltage is charged quickly by the power supply through the stabilizer with polarity as illustrated.

Ct enters quickly into the saturation area and the current flowing to the stabilizer decreases instantly, and the high-voltage pulses of e = L (di/dt) are generated in the stabilizer. The reverse-blocking two-terminal thyristor Q₃ is triggered by this pulse, and a pre-

heating current flows to the filament of the fluorescent lamp. The conducting current of Q3 becomes zero at the phase θ_2 of the negative half cycle of power supply, and Q3 is turned off. Then, the near-the-peak voltage of the negative half cycle of the supply voltage is suddenly applied at both ends of the fluorescent lamp, Q1 and Q2 are turned on again and C1 is charged quickly, with the reverse polarity as illustrated. With the same mechanism, high-voltage pulses (about 1.5kV) with the reverse polarity are generated in the stabilizer and the turn-on pulse is applied at both ends of the fluorescent lamp.

Then the same operation continues and the light is turned on if the filament is sufficiently heated. (Approximately 0.5 seconds after the power is supplied.) If the lamp is turned on, the voltage applied at both ends of the lamp decrease, and Q1, Q2 and Q3 are set off. Then preheating and high voltage pulses are stopped.



(CIRCUIT CONSTANTS ARE USED FOR 32W TYPE)

ISAHAYA ELECTRONICS CORPORATION

http://www.idc-com.co.jp 6-41, TSUKUBA, ISAHAYA, NAGASAKI, 854-0065, JAPAN

- Keep safety in your circuit designs !

Isahaya Electronics Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.Remember to give consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

•These materials are intended as reference to assist out customers in the selection of the Isahaya semiconductor product best suited to the customer's application, they do not convey any license under any intellectual property rights, or any other rights, belonging to Isahaya Electronics Corporation or a third party.

Isahaya Electronics Corporation assumes no responsibility for any damage, or infringement of any third-party rights, originating in the use of any product data, diagrams, charts or circuit application examples contained in the materials.

All information contained in these materials, including product data, diagrams and charts, represent information on products at the time of publication of these materials, and are subject to change by Isahaya Electronics Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Isahaya Electronics Corporation or authorized Isahaya Semiconductor product distributor for the latest product information before purchasing a product listed herein.

The prior written approval of Isahaya Electronics Corporation is necessary to reprint or reproduce in whole or in part these materials.

If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination. Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.

Please contact Isahaya Electronics Corporation or an authorized Isahaya Semiconductor product distributor for further details on these materials or the products contained therein.