



Objective specification

BU4525AL

Silicon Diffused Power Transistor

GENERAL DESCRIPTION

Enhanced performance, new generation, high-voltage, high-speed switching npn transistor in a plastic full-pack envelope intended for use in horizontal deflection circuits of colour television receivers an p.c monitors. Features exceptional tolerance to base drive and collector current load variations resulting in a very low worst case dissipation.

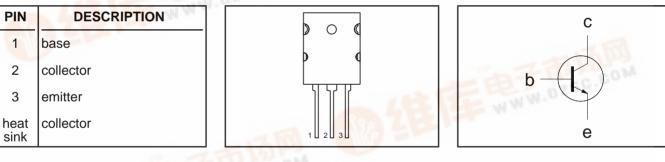
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 V$	-	1500	V
V _{CEO}	Collector-emitter voltage (open base)		-	800	V
I _C	Collector current (DC)		-	12	A
	Collector current peak value		-	30	A
P _{tot}	Total power dissipation	$T_{mb} \leq 25 \ ^{\circ}C$	1-513	125	W
V _{CEsat}	Collector-emitter saturation voltage	$T_{mb} \le 25 \degree C$ $I_{C} = 9.0 \text{ A}; I_{B} = 2.25 \text{ A}$	1.11	3.0	V
I _{Csat}	Collector saturation current	f= 16 kHz	8.0	-	A
Osat		f= 70 kHz	7.5	-	A
t,	Fall time	I _{Csat} = 9.0 A; f = 16 kHz	t.b.f	t.b.f	μs
•	.7. 60	$I_{Csat} = 7.5 \text{ A}; f = 70 \text{ kHz}$	t.b.f	t.b.f	μs

PINNING - SOT430

PIN CONFIGURATION

SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 V$	-	1500	V
V _{CEO}	Collector-emitter voltage (open base)		1 20	800	V
I _C	Collector current (DC)		V	12	A
I _{CM}	Collector current peak value	and the Per	a al	30	A
I _B	Base current (DC)	WW.		8	A
I _{BM}	Base current peak value		-	12	A
-I _{BM}	Reverse base current peak value 1	and the second s	-	7	A
P _{tot}	Total power dissipation	$T_{mb} \leq 25 \degree C$	-	125	W
T _{stg}	Storage temperature		-55	150	°C
T _i	Junction temperature		-	150	°C
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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R _{th j-mb}	Junction to mounting base	-	-	1.0	K/W
R _{th j-a}	Junction to ambient	in free air	45	-	K/W

STATIC CHARACTERISTICS

 $T_{hs} = 25$ °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
ICES	Collector cut-off current ²		-	-	1.0	mA
ICES		V _{BE} = 0 V; V _{CE} = V _{CESMmax} ; T _i = 125 °C	-	-	2.0	mA
I _{EBO}	Emitter cut-off current	$V_{BE} = 7.5 \text{ V}; I_{C} = 0 \text{ A}$	-	-	1.0	mA
I _{EBO} BV _{EBO}	Emitter-base breakdown voltage	$I_{B} = 1 \text{ mA}$	7.5	13.5	-	V
V _{CEOsust}	Collector-emitter sustaining voltage	$I_{B} = 0 \text{ A}; I_{C} = 100 \text{ mA};$	800	-	-	V
		L = 25 mH				
V _{CEsat}	Collector-emitter saturation voltage	I _C = 9.0 A;I _B = 2.25 A;	-	-	3.0	V
V _{BEsat}	Base-emitter saturation voltage	$I_{c} = 9.0 \text{ A}; I_{B} = 2.25 \text{ A};$ $I_{c} = 9.0 \text{ A}; I_{B} = 2.25 \text{ A};$	t.b.f	-	1.1	V
h _{FE}	DC current gain	$I_{c} = 1A; V_{ce} = 5 V$	-	t.b.f	-	
h _{FE}	, , , , , , , , , , , , , , , , , , ,	$I_{\rm C} = 9 \text{ Å}; V_{\rm CE} = 5 \text{ V}$	4.2	5.35	6.5	

DYNAMIC CHARACTERISTICS

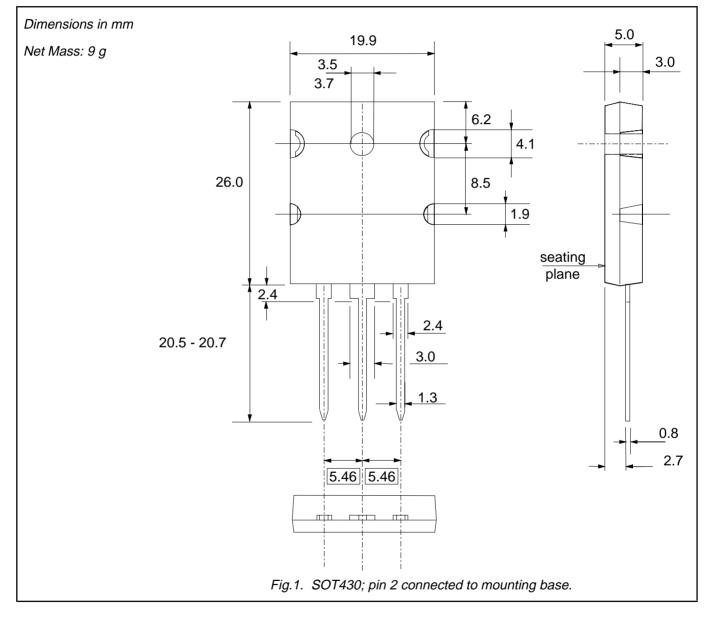
 $T_{hs} = 25$ °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
C _c	Collector capacitance	$I_E = 0 \text{ A}; V_{CB} = 10 \text{ V}; \text{ f} = 1 \text{ MHz}$	145	-	рF
t _s t _f	Switching times (16 kHz line deflection circuit) Turn-off storage time Turn-off fall time	$I_{Csat} = 9.0 \text{ A}; I_{B1} = 1.8 \text{ A};$ $(I_{B2} = -4.5 \text{ A})$	t.b.f t.b.f	t.b.f t.b.f	μs μs
	Switching times (70kHz line deflection circuit)	$I_{Csat} = 7.5 \text{ A}; I_{B1} = 1.5 \text{ A}$ $(I_{B2} = -4.5 \text{ A})$			
t _s t _f	Turn-off storage time Turn-off fall time		t.b.f t.b.f	t.b.f t.b.f	μs μs

Philips Semiconductors

Silicon Diffused Power Transistor

MECHANICAL DATA



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DEFINITIONS

Data sheet status				
Objective specification	Dbjective specification This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
Limiting values				
or more of the limiting val operation of the device at this specification is not im	in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one lues may cause permanent damage to the device. These are stress ratings only and t these or at any other conditions above those given in the Characteristics sections of applied. Exposure to limiting values for extended periods may affect device reliability.			
Application information Where application information is given, it is advisory and does not form part of the specification.				
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