Philips Semiconductors

Objective specification

Silicon Diffused Power Transistor

BUJ202AX

GENERAL DESCRIPTION

High-voltage, high-speed planar-passivated npn power switching transistor in a plastic full-pack envelope intended for use in high frequency electronic lighting ballast applications, converters, inverters, switching regulators, motor control systems, etc.

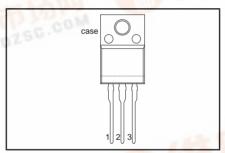
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 \text{ V}$	-	850	V
V _{CBO}	Collector-Base voltage (open emitter)		-	850	V
V _{CEO}	Collector-emitter voltage (open base)		-	450	V
I _C	Collector current (DC)		-	2	Α
	Collector current peak value			3	Α
P _{tot}	Total power dissipation	T _{mb} ≤ 25 °C		18	W
V _{CEsat}	Collector-emitter saturation voltage	$I_{\rm C} = 1.0 \text{ A}; I_{\rm B} = 0.2 \text{ A}$	2.71	1.0	V
t _f	Fall time	IC=1A,I _{B1} =0.2A,I _{B2} =0.2A	88	150	ns

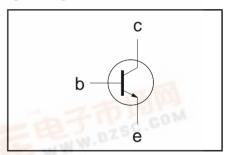
PINNING - SOT186A

PIN	DESCRIPTION		
1	base		
2	collector		
3	emitter		
case	isolated		

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CESM}	Collector to emitter voltage	$V_{BE} = 0 V$	-	850	V
V _{CEO}	Collector to emitter voltage (open base)		-	450	V
V_{CBO}	Collector to base voltage (open emitter)		-	850	V
Ic	Collector current (DC)		-	2	Α
I _{CM}	Collector current peak value		7 - 1	3	Α
I _B	Base current (DC)		T - 1 Y	0.75	Α
I _{BM}	Base current peak value	The state of the s	n Z	1	Α
P _{tot}	Total power dissipation	$T_{mb} \le 25 ^{\circ}C$	W-1	18	W
T _{stq}	Storage temperature		-65	150	°C
T _i	Junction temperature		-	150	°C

THERMAL RESISTANCES

THERMAL RESISTANCES THERMAL RESISTANCES					
SYMBOL	BOL PARAMETER CONDITIONS TYP. MAX.				UNIT
R _{th j-mb}	Junction to mounting base		-	7.2	K/W
R _{th j-a}	Junction to ambient	in free air	55	-	K/W



Silicon Diffused Power Transistor

BUJ202AX

ISOLATION LIMITING VALUE & CHARACTERISTIC

 T_{hs} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{isol}	Repetitive peak voltage from all three terminals to external heatsink	R.H. ≤ 65% ; clean and dustfree	1		1500	V
C _{isol}	Capacitance from T2 to external heatsink	f = 1 MHz	-	12	-	pF

STATIC CHARACTERISTICS

 $T_{mb} = 25$ °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CES}	Collector cut-off current 1	$V_{RE} = 0 \text{ V}; V_{CE} = V_{CESMmax}$	-	-	0.2	mΑ
I _{CES}		$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax}$ $V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax};$	-	-	1.5	mΑ
I _{EBO}	Emitter cut-off current	$T_i = 125 ^{\circ}\text{C}$ $V_{EB} = 5 ^{\circ}\text{V}; I_C = 0 ^{\circ}\text{A}$	_	_	1	mA
V _{CEOsust}	Collector-emitter sustaining voltage	$I_{B} = 0 \text{ A}; I_{C} = 100 \text{ mA};$	450	-	-	V
V _{CEsat}	Collector-emitter saturation voltage	$I_{C} = 25 \text{ mH}$ $I_{C} = 1 \text{ A}; I_{B} = 0.2 \text{ A}$	-	_	1.0	V
V _{BEsat}	Base-emitter saturation voltage	$I_{\rm C} = 1 {\rm A}; I_{\rm B} = 0.2 {\rm A}$	-	-	1.1	V
h _{FE}	DC current gain	$I_{\rm C} = 10 {\rm m \ddot{A}}; {\rm V}_{\rm CE} = 5 {\rm V}$	10	-	35	
		$I_{\rm C} = 100 \text{mÅ}; V_{\rm CE} = 5 \text{V}$	14	-	35	

DYNAMIC CHARACTERISTICS

T_{mb} = 25 °C unless otherwise specified

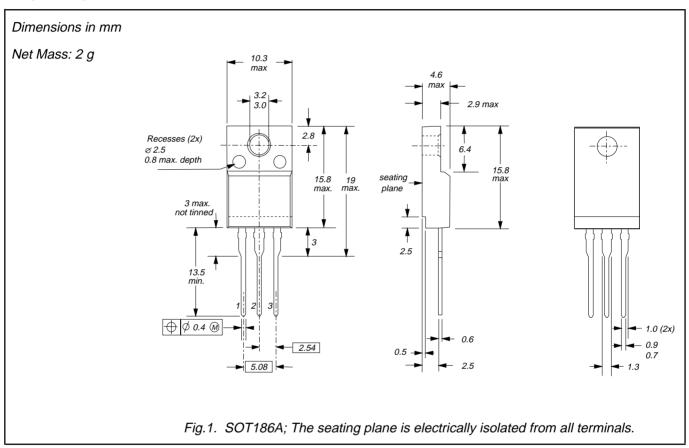
SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
	Switching times (resistive load)	$I_{Con} = 1.0 \text{ A}; I_{Bon} = -I_{Boff} = 0.2 \text{ A};$ $R_1 = 75 \text{ ohms}; V_{BB2} = 4 \text{ V};$			
t _{on}	Turn-on time		-	0.5	μs
t _s	Turn-off storage time		-	3.5	μs
t _f	Turn-off fall time		-	1.4	μs
	Switching times (inductive load)	$I_{Con} = 1.0 \text{ A}; I_{Bon} = 0.2 \text{ A}; L_{B} = 1 \mu\text{H}; $ -V _{BB} = 5 V			
l t _s	Turn-off storage time		-	1.4	μs
l t _f	Turn-off fall time		88	150	ns
	Switching times (inductive load)	$I_{Con} = 1.0 \text{ A}; I_{Bon} = 0.2 \text{ A}; L_{B} = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}; T_{i} = 100 ^{\circ}\text{C}$			
l t _s	Turn-off storage time		-	1.5	μs
t _f	Turn-off fall time		-	200	ns

¹ Measured with half sine-wave voltage (curve tracer).

Silicon Diffused Power Transistor

BUJ202AX

MECHANICAL DATA



- Refer to mounting instructions for F-pack envelopes.
 Epoxy meets UL94 V0 at 1/8".

Silicon Diffused Power Transistor

BUJ202AX

DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
I the Marian and the same				

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

© Philips Electronics N.V. 1998

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, it is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent or other industrial or intellectual property rights.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.