**BUK205-50Y** 

#### DESCRIPTION

Monolithic temperature and overload protected power switch based on MOSFET technology in a 5 pin plastic surface mount envelope, configured as a single high side switch.

#### **APPLICATIONS**

General controller for driving lamps, motors, solenoids, heaters.

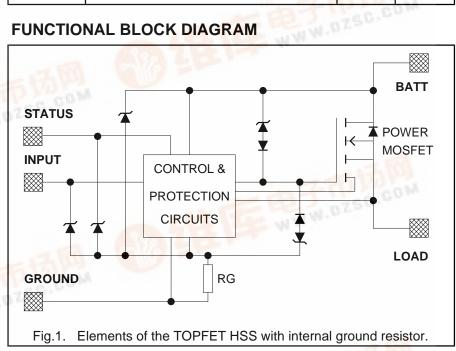
#### **FEATURES**

- Vertical power DMOS switch
- Low on-state resistance
- 5 V logic compatible input
- Overtemperature protection self resets with hysteresis
- Overload protection against short circuit load with output current limiting; latched - reset by input
- High supply voltage load
- protection Supply undervoltage lock out
- Status indication for overload protection activated
- Diagnostic status indication of open circuit load
- Very low quiescent current
- Voltage clamping for turn off of inductive loads
- ESD protection on all pins
- Reverse battery and overvoltage protection

#### QUICK REFERENCE DATA

SYMBOL	PARAMETER	MIN.	UNIT
IL TANK	Nominal load current (ISO)	6	А
SYMBOL	PARAMETER	MAX.	UNIT
V <sub>BG</sub> I <sub>L</sub> T <sub>j</sub> R <sub>ON</sub>	Continuous off-state supply voltage Continuous load current Continuous junction temperature On-state resistance	50 15 150 60	V A °C mΩ

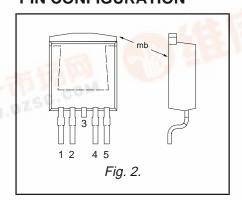
#### **FUNCTIONAL BLOCK DIAGRAM**



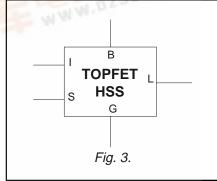
### **PINNING - SOT426**

PIN	DESCRIPTION
1	Ground
2	Input
3	(connected to mb)
4	<b>S</b> tatus
5	Load
mb	<b>B</b> attery

### PIN CONFIGURATION



#### SYMBOL





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#### **LIMITING VALUES**

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
.,	Battery voltages		_		.,
$V_{BG}$	Continuous off-state supply voltage	-	0	50	V
	Reverse battery voltages <sup>1</sup>	External resistors:			
-V <sub>BG</sub>	Repetitive peak supply voltage	$R_1 = R_S \ge 4.7 \text{ k}\Omega, \delta \le 0.1$	-	32	V
-V <sub>BG</sub>	Continuous reverse supply voltage	$R_I = R_S \ge 4.7 \text{ k}\Omega$	1	16	V
IL	Continuous load current	T <sub>mb</sub> ≤115 °C	-	15	А
$P_{D}$	Total power dissipation	T <sub>mb</sub> ≤25 °C	-	83.3	W
T <sub>stg</sub>	Storage temperature	-	-55	175	°C
T <sub>j</sub>	Continuous junction temperature <sup>2</sup>	-	-	150	°C
T <sub>sold</sub>	Lead temperature	during soldering	-	250	°C
	Input and status				
I <sub>I</sub>	Continuous input current	-	-5	5	mA
I <sub>S</sub>	Continuous status current	-	-5	5	mA
I <sub>1</sub>	Repetitive peak input current	δ ≤ 0.1	-20	20	mA
I <sub>S</sub>	Repetitive peak status current	$\delta \le 0.1$	-20	20	mA
	Inductive load clamping				
E <sub>BL</sub>	Non-repetitive clamping energy	$T_{mb} = 150 ^{\circ}\text{C}$ prior to turn-off	-	1.2	J

### **ESD LIMITING VALUE**

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>C</sub>	Electrostatic discharge capacitor voltage	Human body model; C = 250 pF; R = 1.5 kΩ	-	2	kV

### THERMAL CHARACTERISTIC

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Thermal resistance <sup>3</sup>					
R <sub>th j-mb</sub>	Junction to mounting base	-	-	1.2	1.5	K/W

<sup>1</sup> Reverse battery voltage is allowed only with external input and status resistors to limit the currents to a safe value.

**<sup>2</sup>** For normal continuous operation. A higher  $T_j$  is allowed as an overload condition but at the threshold  $T_{j(TO)}$  the over temperature trip operates to protect the switch.

<sup>3</sup> Of the output Power MOS transistor.

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#### STATIC CHARACTERISTICS

T<sub>mb</sub> = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Clamping voltages					
$V_{BG}$	Battery to ground	$I_G = 1 \text{ mA}$	50	55	65	V
$V_{BL}$	Battery to load	$I_L = I_G = 1 \text{ mA}$	50	55	65	V
-V <sub>LG</sub>	Negative load to ground	I <sub>L</sub> = 1 mA	12	17	21	V
	Supply voltage	battery to ground				
$V_{BG}$	Operating range <sup>1</sup>	-	5	-	40	V
	Currents	V <sub>BG</sub> = 13 V				
IL	Nominal load current <sup>2</sup>	$V_{BL} = 0.5 \text{ V}; T_{mb} = 85 ^{\circ}\text{C}$	6	-	-	Α
$I_B$	Quiescent current <sup>3</sup>	$V_{IG} = 0 \text{ V}; V_{LG} = 0 \text{ V}$	-	0.1	2	μΑ
$I_{G}$	Operating current <sup>4</sup>	$V_{IG} = 5 \text{ V}; I_{L} = 0 \text{ A}$	1.5	2.2	4	mA
I <sub>L</sub>	Off-state load current <sup>5</sup>	$V_{BL} = 13 \text{ V}; V_{IG} = 0 \text{ V}$	-	0.1	1	μΑ
	Resistances					
R <sub>on</sub>	On-state resistance <sup>6</sup>	$V_{BG} = 13 \text{ V}; I_L = 7.5 \text{ A}; t_p = 300 \mu\text{s}$	-	45	60	mΩ
R <sub>ON</sub>	On-state resistance	$V_{BG} = 5 \text{ V}; I_{L} = 1.5 \text{ A}; t_{p} = 300 \mu\text{s}$	-	70	90	mΩ
$R_{G}$	Internal ground resistance	$I_G = 10 \text{ mA}$	-	150	-	Ω

#### **INPUT CHARACTERISTICS**

 $T_{mb} = 25 \, ^{\circ}C; \, V_{BG} = 13 \, V$ 

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>1</sub>	Input current	$V_{IG} = 5 \text{ V}$	35	60	100	μΑ
$V_{IG}$	Input clamping voltage	I <sub>I</sub> = 200 μA	6	7.5	8.5	V
$V_{IG(ON)}$	Input turn-on threshold voltage		-	2.1	2.7	V
$V_{IG(OFF)}$	Input turn-off threshold voltage		1.5	2	-	V

 $<sup>{\</sup>bf 1} \ {\hbox{On-state resistance is increased if the supply voltage is less than 9 V.} \ \ {\hbox{Refer to figure 8}}.$ 

<sup>2</sup> Defined as in ISO 10483-1.

<sup>3</sup> This is the continuous current drawn from the battery when the input is low and includes leakage current to the load.

<sup>4</sup> This is the continuous current drawn from the battery with no load connected, but with the input high.

<sup>5</sup> The measured current is in the load pin only.

<sup>6</sup> The supply and input voltage for the R<sub>ON</sub> tests are continuous. The specified pulse duration t<sub>p</sub> refers only to the applied load current.

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#### PROTECTION FUNCTIONS AND STATUS INDICATIONS

Truth table for normal, open-circuit load and overload conditions and abnormal supply voltages.

	FUNCTIONS	Т	RUTH TAB	LE	THRESHOLD			
SYMBOL	CONDITION	INPUT	STATUS	OUTPUT	MIN.	TYP.	MAX.	UNIT
	Normal on-state	1	1	1				
	Normal off-state	0	1	0				
I <sub>L(OC)</sub>	Open circuit load <sup>1</sup>	1	0	1	100	350	600	mA
	Open circuit load	0	1	0				
T <sub>j(TO)</sub>	Over temperature <sup>2</sup>	1	0	0	150	175	-	°C
	Over temperature <sup>3</sup>	0	0	0				
V <sub>BL(TO)</sub>	Short circuit load⁴	1	0	0	9	10.5	12	V
	Short circuit load	0	1	0				
V <sub>BG(TO)</sub>	Low supply voltage <sup>5</sup>	Х	1	0	3	4	5	V
$V_{BG(LP)}$	High supply voltage <sup>6</sup>	Х	1	0	40	45	50	V

For input '0' equals low, '1' equals high, 'X' equals don't care. For status '0' equals low, '1' equals open or high. For output switch '0' equals off, '1' equals on.

#### STATUS CHARACTERISTICS

The status output is an open drain transistor, and requires an external pull-up circuit to indicate a logic high.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{SG}$	Status clamping voltage	$I_{S} = 100 \mu\text{A};  V_{IG} = 0  \text{V}$	6	7	8	V
$V_{SG}$	Status low voltage	$I_S = 50 \mu A; V_{BG} = 13 V; V_{IG} = 5 V$	-	0.7	0.8	V
Is	Status leakage current	$V_{SG} = 5 V$	-	0.1	1	μΑ
Is	Status saturation current <sup>7</sup>	$V_{SS} = 5 \text{ V}; R_S = 0 \Omega; V_{BG} = 13 \text{ V}$	-	5	-	mΑ
	Application information					
R <sub>s</sub>	External pull-up resistor <sup>8</sup>	V <sub>SS</sub> = 5 V	-	100	-	kΩ

<sup>1</sup> In the on-state, the switch detects whether the load current is less than the quoted open load threshold current. This is for status indication only. Typical hysteresis equals 140 mA. The thresholds are specified for supply voltage within the normal working range.

<sup>2</sup> After cooling below the reset temperature the switch will resume normal operation. The reset temperature is lower than the trip temperature by typically 10 °C.

<sup>3</sup> If the overtemperature protection has operated, status remains low to indicate the overtemperature condition even if the input is taken low, providing the device has not cooled below the reset temperature.

<sup>4</sup> After short circuit protection has operated, the input voltage must be toggled low for the switch to resume normal operation.

<sup>5</sup> Undervoltage sensor causes the device to switch off. Typical hysteresis equals 0.7 V.

<sup>6</sup> Overvoltage sensor causes the device to switch off to protect the load. Typical hysteresis equals 1.3 V.

<sup>7</sup> In a fault condition with the pull-up resistor short circuited while the status transistor is conducting.

<sup>8</sup> The pull-up resistor also protects the status pin during reverse battery conditions.

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#### DYNAMIC CHARACTERISTICS

 $T_{mb} = 25 \, ^{\circ}C; \, V_{BG} = 13 \, V$ 

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
-V <sub>LG</sub>	Inductive load turn-off Negative load voltage <sup>1</sup>	$V_{IG} = 0 \text{ V}; I_L = 7.5 \text{ A}; t_p = 300 \mu\text{s}$	15	20	25	V
t <sub>d sc</sub>	Short circuit load protection <sup>2</sup> Response time Load current prior to turn-off	$V_{IG} = 5 \text{ V}; \text{ R}_{L} \le 10 \text{ m}\Omega$ $t < t_{d \text{ sc}}$	-	90 42	-	μs A
I <sub>L(lim)</sub>	Overload protection <sup>3</sup> Load current limiting	$V_{BL} = 9 \text{ V}; t_p = 300 \mu\text{s}$	28	40	52	А

#### **SWITCHING CHARACTERISTICS**

 $T_{mb}$  = 25 °C,  $V_{BG}$  = 13 V, for resistive load  $R_L$  = 13  $\Omega$ .

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	During turn-on	to $V_{IG} = 5 \text{ V}$				
$\mathbf{t}_{\sf d\ on}$	Delay time	to 10% V <sub>L</sub>	-	16	-	μs
$\mathrm{dV/dt}_{\mathrm{on}}$	Rate of rise of load voltage		-	1	2.5	V/μs
t on	Total switching time	to 90% V <sub>L</sub>	-	40	-	μs
	During turn-off	to $V_{IG} = 0 V$				
$t_{d off}$	Delay time	to 90% V <sub>L</sub>	-	30	-	μs
$dV/dt_{off}$	Rate of fall of load voltage		-	1.2	2.5	V/μs
t off	Total switching time	to 10% V <sub>L</sub>	-	50	-	μs

## **CAPACITANCES**

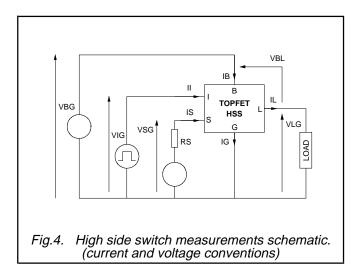
 $T_{mb} = 25$  °C; f = 1 MHz;  $V_{IG} = 0$  V

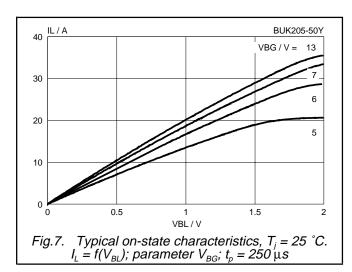
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
C <sub>ig</sub>	Input capacitance	V <sub>BG</sub> = 13 V		15	20	pF
C <sub>bl</sub>	Output capacitance	$V_{BL} = V_{BG} = 13 \text{ V}$	-	415	580	pF
C <sub>sg</sub>	Status capacitance	$V_{SG} = 5 \text{ V}$	-	11	15	pF

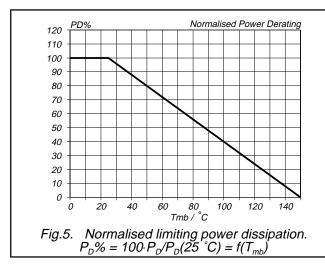
<sup>1</sup> For a high side switch, the load pin voltage goes negative with respect to ground during the turn-off of an inductive load. This negative voltage is clamped by the device.

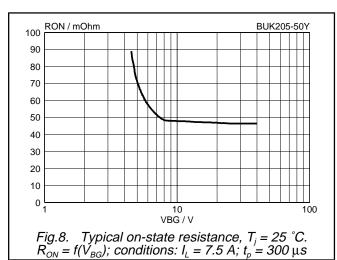
<sup>2</sup> The load current is self-limited during the response time for short circuit load protection. Response time is measured from when input goes high.

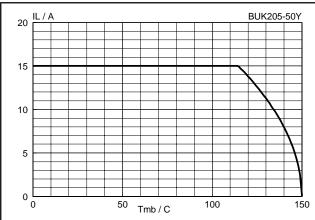
<sup>3</sup> If the load resistance is low, but not a complete short circuit, such that the on-state voltage remains less than V<sub>BL(TO)</sub>, the device remains in current limiting until the overtemperature protection operates.











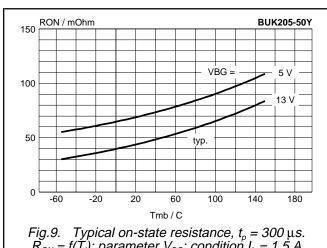
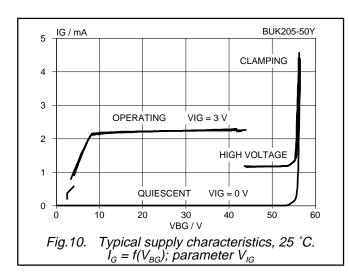
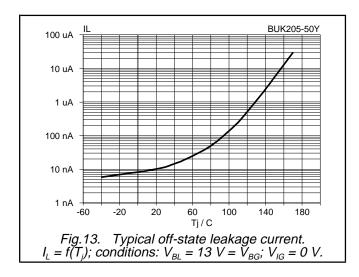
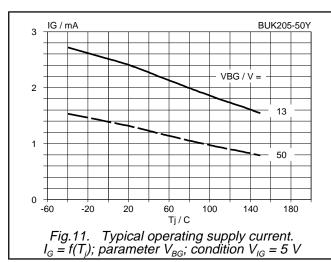


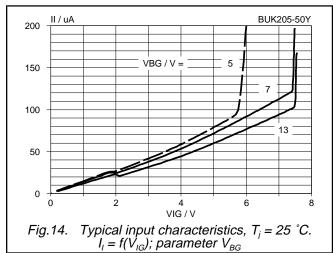
Fig.6. Limiting continuous on-state load current.  $I_L = f(T_{mb})$ ; conditions:  $V_{IG} = 5 \text{ V}$ ,  $V_{BG} = 13 \text{ V}$ 

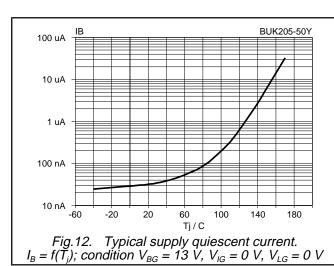
Fig.9. Typical on-state resistance,  $t_p = 300 \, \mu s$ .  $R_{ON} = f(T_j)$ ; parameter  $V_{BG}$ ; condition  $I_L = 1.5 \, A$ 

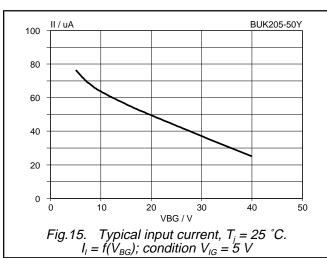


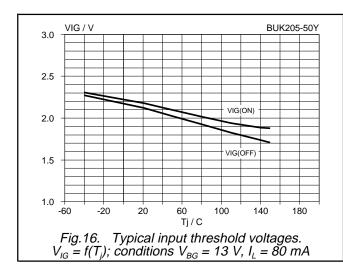


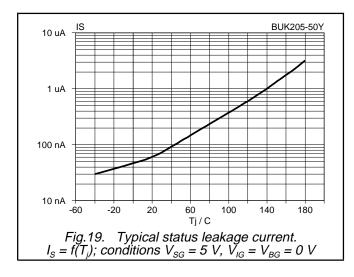


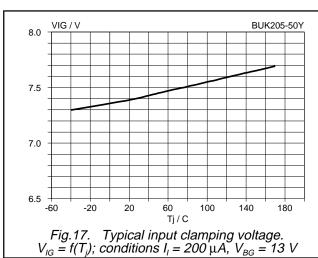


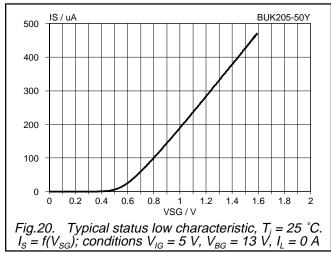


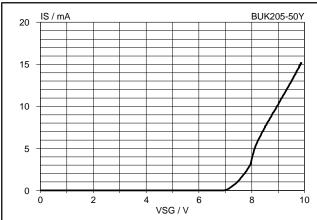


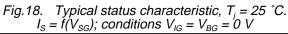












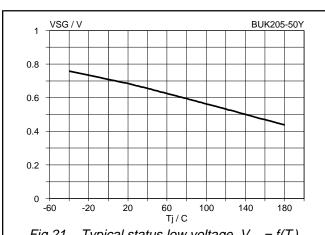


Fig.21. Typical status low voltage,  $V_{SG} = f(T_j)$ . conditions  $I_S = 50 \,\mu A$ ,  $V_{IG} = 5 \, V$ ,  $V_{BG} = 13 \, V$ ,  $I_L = 0 \, A$ 

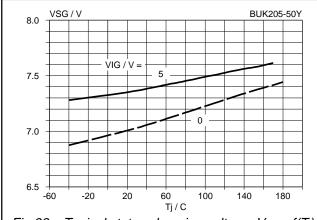


Fig.22. Typical status clamping voltage,  $V_{SG} = f(T_j)$ . parameter  $V_{IG}$ ; conditions  $I_S = 100 \, \mu A$ ,  $V_{BG} = 13 \, V$ 

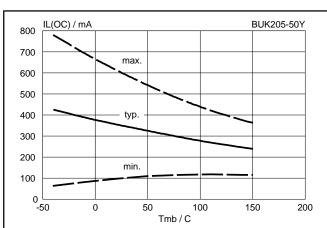


Fig.23. Low load current detection threshold.  $I_{L(OC)} = f(T_j)$ ; conditions  $V_{IG} = 5 \ V$ ;  $V_{BG} = 13 \ V$ 

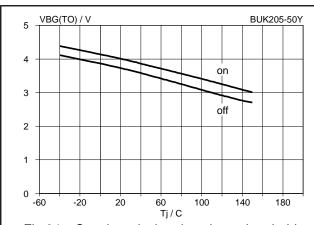


Fig.24. Supply typical undervoltage thresholds.  $V_{BG(TO)} = f(T_j)$ ; conditions  $V_{IG} = 3 \ V$ ;  $I_L = 80 \ mA$ 

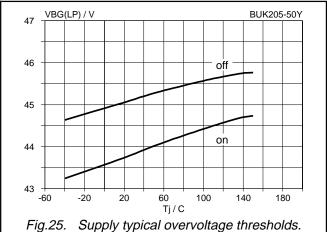


Fig.25. Supply typical overvoltage thresholds.  $V_{BG(LP)} = f(T_i)$ ; conditions  $V_{IG} = 5 \text{ V}$ ;  $I_L = 80 \text{ mA}$ 

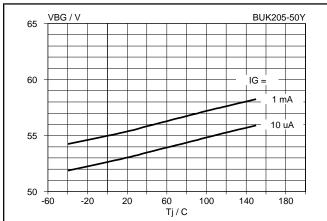


Fig.26. Typical battery to ground clamping voltage.  $V_{BG} = f(T_j)$ ; parameter  $I_G$ 

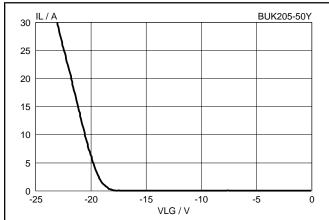
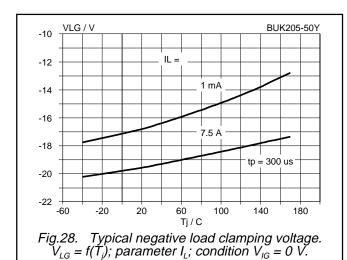
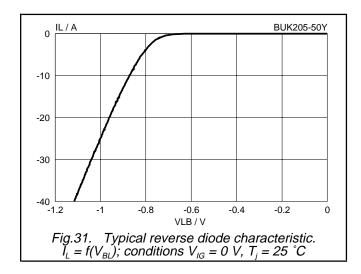
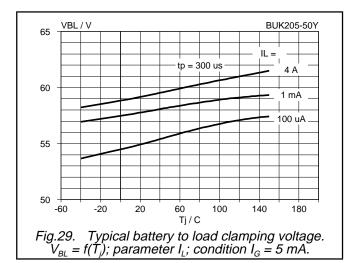
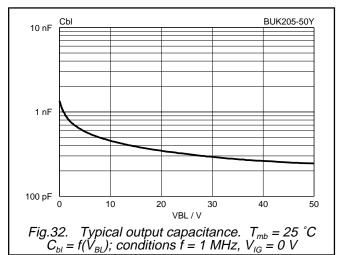


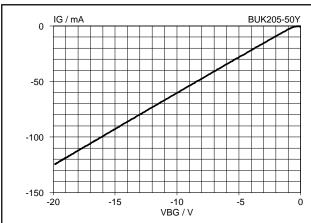
Fig.27. Typical negative load clamping characteristic.  $I_L = f(V_{LG})$ ; conditions  $V_{IG} = 0$  V,  $t_p = 300 \,\mu\text{s}$ , 25 °C











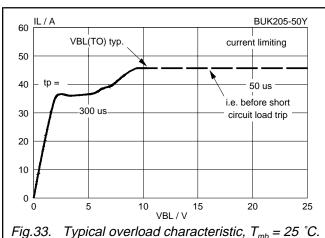
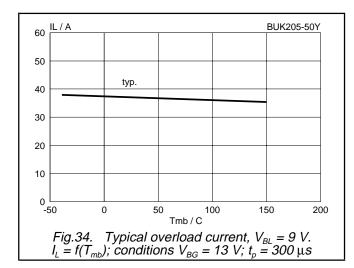
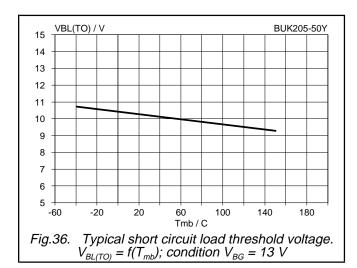
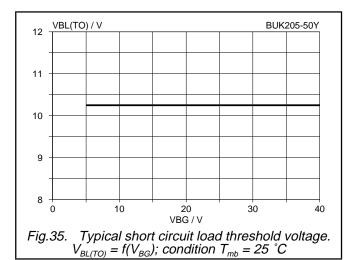


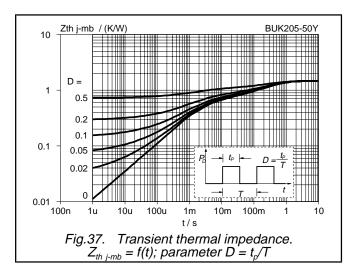
Fig.30. Typical reverse battery characteristic.  $I_G = f(V_{BG})$ ; conditions  $I_L = 0$  A,  $T_j = 25$  °C

Fig.33. Typical overload characteristic,  $T_{mb} = 25$  °C.  $I_L = f(V_{BL})$ ; condition  $V_{BG} = 13$  V; parameter  $t_p$ 



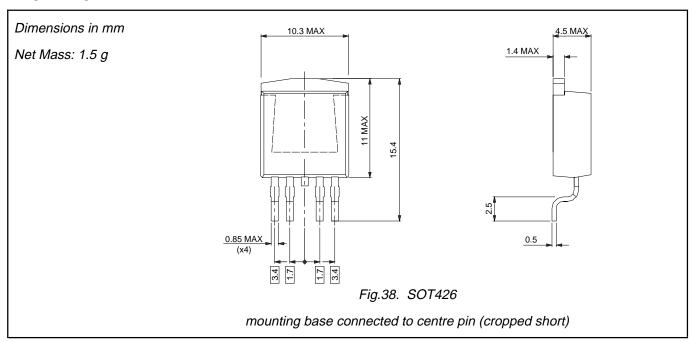




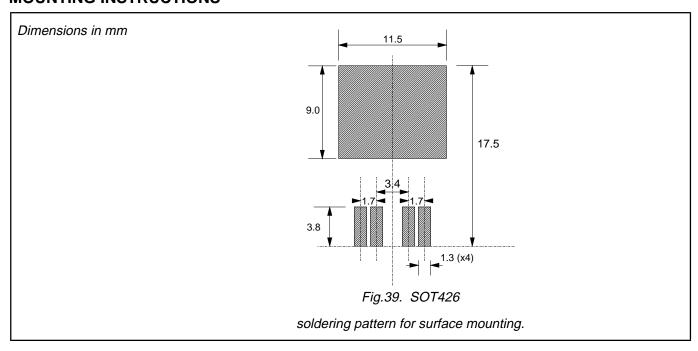


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## **MECHANICAL DATA**



### **MOUNTING INSTRUCTIONS**



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#### **DEFINITIONS**

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
1	

#### Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

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