SWITCHMODE Series NPN Silicon Power Transistor

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- . . . designed for high speed, high current, high power applications.
- Very fast switching times:
 T_F max. = 0.4 μs at I_C = 8 A

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO(sus)}	200	Vdc
Collector-Base Voltage	VCBO	250	Vdc
Emitter-Base Voltage	VEBO	7	Vdc
Collector–Emitter Voltage (V _{BE} = −2.5 V)	VCEX	250	Vdc
Collector–Emitter Voltage (R _{BE} = 100 Ω)	VCER	240	Vdc
Collector–Current — Continuous — Peak (pw ≤ 10 ms)	I _C	15 20	Adc Apk
Base-Current continuous	d _B CO	3	Adc
Total Power Dissipation @ T _C = 25°C	PD	120	Watts
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to 200	°C

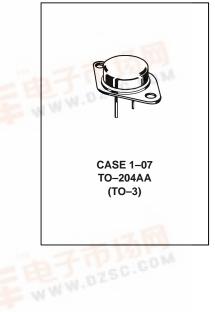
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	θЈС	1.46	°C/W

Figure 1. Power Derating

BUX41

15 AMPERES
NPN SILICON
POWER
METAL TRANSISTOR
200 VOLTS
120 WATTS





BUX41

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

	Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS ¹	I	•	-	-	•
Collector–Emitter Sustaini (I _C = 200 mA, I _B = 0, L		VCEO(sus)	200		Vdc
Collector Cutoff Current at (VCE = 250 V, VBE = - (VCE = 250 V, VBE = -	1.5 V)	ICEX		1.0 5.0	mAdc
Collector–Emitter Cutoff C (V _{CE} = 160 V)	Current	ICEO		1.0	mAdc
Emitter–Base Reverse Vo	ltage	V _{EBO}	7		V
Emitter–Cutoff Current (V _{EB} = 5 V)		I _{EBO}		1.0	mAdc
SECOND BREAKDOWN			•	•	
Second Breakdown Collect (VCE = 30 V, t = 1 s) (VCE = 135 V, t = 1 s)	ctor Current with base forward biased	I _{S/b}	4.0 0.15		Adc
ON CHARACTERISTICS ¹		'			
DC Current Gain (I _C = 5 A, V _{CE} = 4 V) (I _C = 8 A. V _{CE} = 4 V)		hFE	15 8	45	
Collector–Emitter Saturati ($I_C = 5 \text{ A}$, $I_B = 0.5 \text{ A}$) ($I_C = 8 \text{ A}$, $I_B = 1 \text{ A}$)	on Voltage	VCE(sat)		1.2 1.6	Vdc
Base–Emitter Saturation \ (I _C = 8 A, I _B = 1 A)	/oltage	VBE(sat)		2.0	Vdc
DYNAMIC CHARACTERIS	TICS	<u> </u>			
Current Gain — Bandwidt (V _{CE} = 15 V, I _C = 1 A, f		fT	8.0		MHz
SWITCHING CHARACTER	ISTICS (Resistive Load)		-		
Turn-on Time		ton		0.6	μs
Storage Time	$(I_C = 8 \text{ A}, I_{B1} = I_{B2} = 1 \text{ A},$ $V_{CC} = 150 \text{ V}, R_C = 18.75 \Omega)$	t _s		1.5	1
Fall Time	100 100 1, 1.0 10 0 12.,	t _f		0.4	1

¹ Pulse Test: Pulse Width $\leq 300 \,\mu\text{s}$, Duty Cycle $\leq 2\%$.

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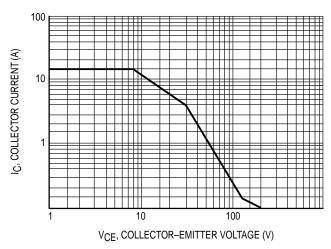


Figure 2. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 2 is based on $I_C = 25^{\circ}C$. I_{CRV} is

The data of Figure 2 is based on $T_C = 25^{\circ}C$, $T_{J(pk)}$ is variable depending on power level. Second breakdown limitations do not derate the same as thermal limitations.

At high case temperatures, thermal limitations will reduce the power that can handled to values less than the limitations imposed by second breakdown.

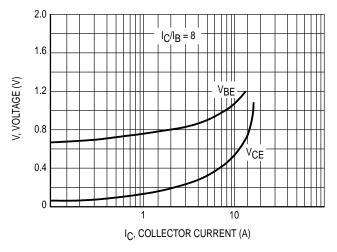


Figure 3. "On" Voltages

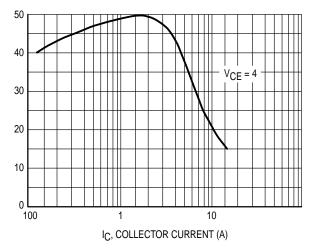


Figure 4. DC Current Gain

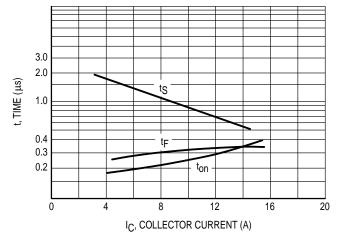
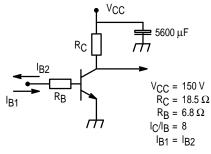


Figure 5. Resistive Switching Performance



R_C – R_B: Non inductive resistances

Figure 6. Switching Times Test Circuit

BUX41

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