### DISCRETE SEMICONDUCTORS









### BY715 to BY724

### FEATURES

- · Glass passivated
- High maximum operating temperature
- · Low leakage current
- · Excellent stability
- Soft-recovery switching characteristics
- Compact construction.

#### APPLICATIONS

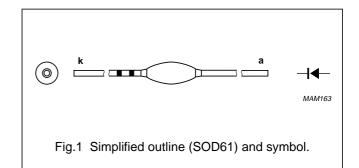
- For high-voltage rectification up to 75 kHz
- High-voltage applications for:
  - Multipliers
  - Slot-wound diode-split-transformers.

#### DESCRIPTION

Rugged glass package, using a high temperature alloyed construction.

This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.

The package is designed to be used in an insulating medium such as resin, oil or SF6 gas.



#### MARKING

 Table 1
 Cathode band colour codes

TYPE NUMBER	PACKAGE CODE	OUTER BAND	INNER BAND	
BY715	SOD61E	green	brown	
BY716	SOD61E	red brown		
BY717	SOD61E	green	red	
BY718	SOD61E	blue	red	
BY719	SOD61E	yellow	red	
BY720	SOD61G	red	green	
BY721	SOD61G	blue	green	
BY722	SOD61K	red	blue	
BY723	SOD61K	green blue		
BY724	SOD61K	rellow blue		

### BY715 to BY724

### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>RSM</sub>	non-repetitive peak voltage				
	BY715		_	5	kV
	BY716		-	6	kV
	BY717		_	10	kV
	BY718		_	12	kV
	BY719		_	14	kV
	BY720		_	17	kV
	BY721		_	19	kV
	BY722		_	22	kV
	BY723		_	24	kV
	BY724		-	30	kV
V <sub>RRM</sub>	repetitive peak reverse voltage				
	BY715		_	5	kV
	BY716		_	6	kV
	BY717		_	10	kV
	BY718		_	12	kV
	BY719		-	14	kV
	BY720		_	17	kV
	BY721		-	19	kV
	BY722		_	22	kV
	BY723		-	24	kV
	BY724		_	30	kV
V <sub>RW</sub>	working reverse voltage				
	BY715		_	4	kV
	BY716		_	5	kV
	BY717		_	9	kV
	BY718		_	10	kV
	BY719		_	12	kV
	BY720		_	14	kV
	BY721		_	16	kV
	BY722		_	18	kV
	BY723		_	20	kV
	BY724		_	24	kV

### BY715 to BY724

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>F(AV)</sub>	average forward current	averaged over any 20 ms period; see Figs 2, 3, 4 and 5			
	BY715		_	20	mA
	BY716		_	20	mA
	BY717		_	4	mA
	BY718		_	4	mA
	BY719		_	4	mA
	BY720		_	3	mA
	BY721		_	3	mA
	BY722		_	3	mA
	BY723		_	3	mA
	BY724		_	3	mA
I <sub>FRM</sub>	repetitive peak forward current		-	500	mA
T <sub>stg</sub>	storage temperature		-65	+120	°C
Tj	junction temperature		-65	+120	°C

## BY715 to BY724

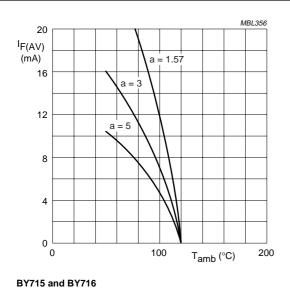
### **ELECTRICAL CHARACTERISTICS**

 $T_j = 25 \ ^{\circ}C$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>F</sub>	forward voltage	$I_F = 100 \text{ mA}; T_j = T_{j \text{ max}};$ see Figs 6, 7 and 8				
	BY715		-	-	28	V
	BY716		-	_	28	V
	BY717		-	_	69	V
	BY718		-	_	69	V
	BY719		-	_	69	V
	BY720		-	_	92	V
	BY721		-	_	92	V
V <sub>F</sub>	forward voltage	$I_F = 50 \text{ mA}; T_j = T_{j \text{ max}}; \text{ see Fig.9}$				
	BY722		-	_	88	V
	BY723		-	_	88	V
	BY724		-	_	88	V
I <sub>R</sub>	reverse current	$V_R = V_{RWmax}; T_j = 120 \ ^{\circ}C$	-	-	3	μA
Qr	recovery charge	when switched from $I_F$ = 100 mA to $V_R \ge 100$ V and $dI_F/dt$ = -200 mA/µs; see Fig.11	-	_	0.4	nC
t <sub>f</sub>	fall time	when switched from $I_F$ = 100 mA to $V_R \ge 100$ V and $dI_F/dt$ = $-200$ mA/µs; see Fig.11	40	-	-	ns
t <sub>rr</sub>	reverse recovery time	when switched from $I_F$ = 100 mA to $V_R \ge 100$ V and $dI_F/dt$ = $-200$ mA/µs; see Fig.11	_	100	-	ns

### BY715 to BY724

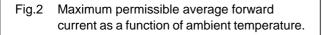
### **GRAPHICAL DATA**

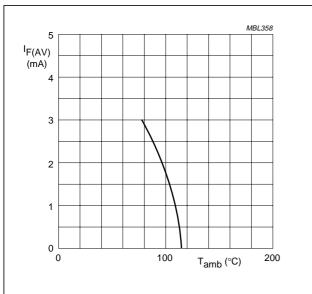


BY715 and BY716

 $a = I_{F(RMS)}/I_{F(AV)}; V_R = V_{RWmax}; R_{th j-a} \le 120 \text{ K/W}.$ 

a = 1.57: half sinewave.

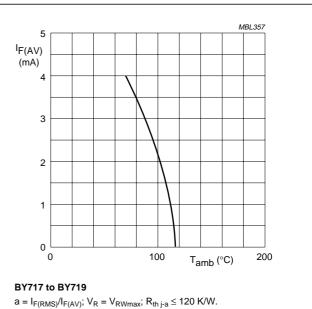




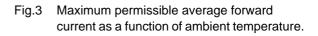
#### BY720 and BY721

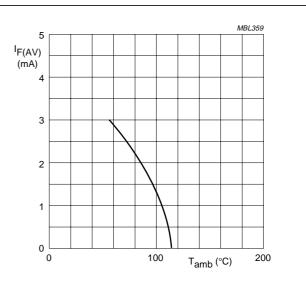
$$\label{eq:RMS} \begin{split} a &= I_{F(RMS)}/I_{F(AV)}; \ V_R = V_{RWmax}; \ R_{th \ j\text{-}a} \leq 120 \ \text{K/W}. \\ a &= 1.57: \ half \ sinewave. \end{split}$$

Fig.4 Maximum permissible average forward current as a function of ambient temperature.



a = 1.57: half sinewave.



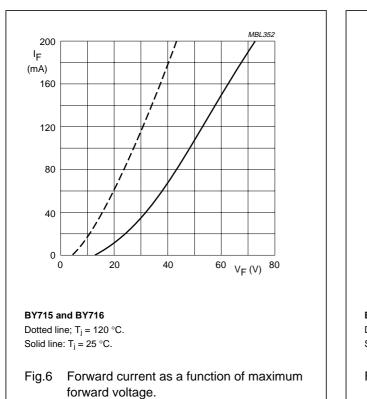


#### BY722 to BY724

$$\label{eq:RMS} \begin{split} a &= I_{F(RMS)}/I_{F(AV)}; \ V_R = V_{RWmax}; \ R_{th \ j\text{-}a} \leq 120 \ \text{K/W}. \\ a &= 1.57: \ half \ sinewave. \end{split}$$

Fig.5 Maximum permissible average forward current as a function of ambient temperature.

### BY715 to BY724



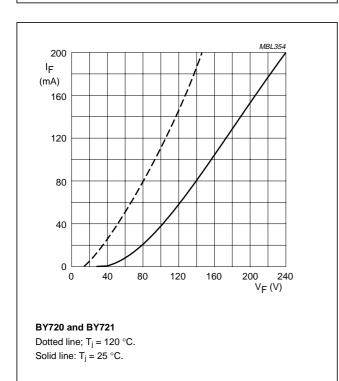


Fig.8 Forward current as a function of maximum forward voltage.

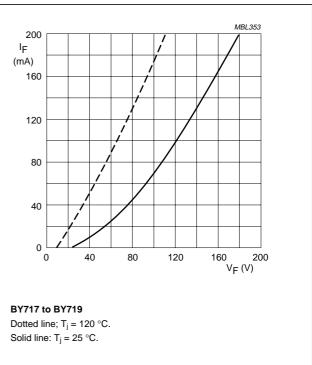
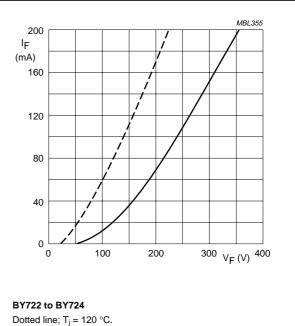


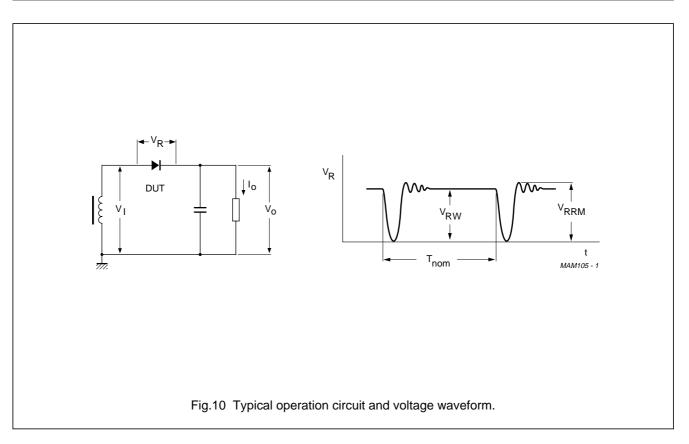
Fig.7 Forward current as a function of maximum forward voltage.

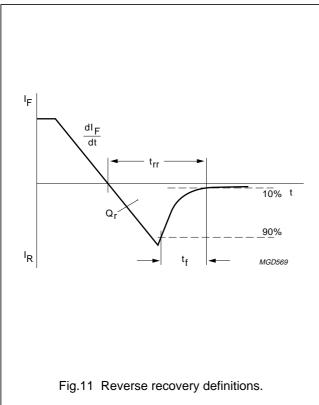


Dotted line;  $T_j = 120^\circ$ Solid line:  $T_j = 25^\circ$ C.

Fig.9 Forward current as a function of maximum forward voltage.

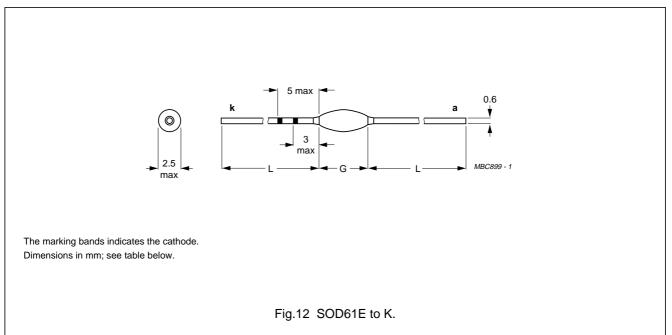
### BY715 to BY724





### BY715 to BY724

### PACKAGE OUTLINE



#### PACKAGE SPECIFICATION

TYPE NUMBER	PACKAGE CODE	L <sub>min</sub> (mm)	G <sub>max</sub> (mm)
BY715	SOD61E	29.7	9.5
BY716	SOD61E	29.7	9.5
BY717	SOD61E	29.7	9.5
BY718	SOD61E	29.7	9.5
BY719	SOD61E	29.7	9.5
BY720	SOD61G	29.0	11.0
BY721	SOD61G	29.0	11.0
BY722	SOD61K	28.2	12.5
BY723	SOD61K	28.2	12.5
BY724	SOD61K	28.2	12.5

### BY715 to BY724

#### DATA SHEET STATUS

DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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### BY715 to BY724

NOTES

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