DISCRETE SEMICONDUCTORS





BYD33 series
Fast soft-recovery
controlled avalanche rectifiers

Product specification
Supersedes data of 1996 Jun 05

1996 Sep 18







BYD33 series

FEATURES

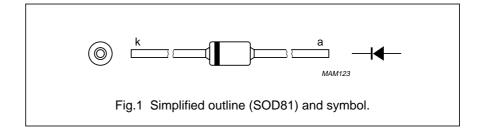
- · Glass passivated
- High maximum operating temperature
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- Available in ammo-pack.

DESCRIPTION

Cavity free cylindrical glass package through Implotec^{™(1)} technology. This package is hermetically sealed

and fatigue free as coefficients of expansion of all used parts are matched.

(1) Implotec is a trademark of Philips.



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{RRM}	repetitive peak reverse voltage				
	BYD33D		_	200	V
	BYD33G		_	400	V
	BYD33J		_	600	V
	BYD33K		_	800	V
	BYD33M		_	1000	V
	BYD33U		_	1200	V
	BYD33V		_	1400	V
V _R	continuous reverse voltage				
	BYD33D		_	200	V
	BYD33G		_	400	V
	BYD33J		_	600	V
	BYD33K		_	800	V
	BYD33M		_	1000	V
	BYD33U		_	1200	V
	BYD33V		_	1400	V
I _{F(AV)}	average forward current	T _{tp} = 55 °C; lead length = 10 mm;			
	BYD33D to M	see Figs 2 and 3;	_	1.30	Α
	BYD33U and V	averaged over any 20 ms period; see also Figs 10 and 11	_	1.26	A
I _{F(AV)}	average forward current	T _{amb} = 65 °C; PCB mounting (see			
	BYD33D to M	Fig.19); see Figs 4 and 5;	_	0.70	Α
	BYD33U and V	averaged over any 20 ms period; see also Figs 10 and 11	_	0.67	A
I _{FRM}	repetitive peak forward current	T _{tp} = 55 °C; see Figs 6 and 7			
	BYD33D to M		_	12	A
	BYD33U and V		_	11	Α

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SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{FRM}	repetitive peak forward current	T _{amb} = 65 °C; see Figs 8 and 9			
	BYD33D to M		_	7	Α
	BYD33U and V		_	6	Α
I _{FSM}	non-repetitive peak forward current	t = 10 ms half sine wave;	_	20	Α
		$T_j = T_{j \text{ max}}$ prior to surge;			
		$V_R = V_{RRMmax}$			
E _{RSM}	non-repetitive peak reverse	$L = 120 \text{ mH}$; $T_j = T_{j \text{ max}}$ prior to			
	avalanche energy	surge; inductive load switched off			
	BYD33D to J		_	10	mJ
	BYD33K to V		-	7	mJ
T _{stg}	storage temperature		-65	+175	°C
Tj	junction temperature	see Figs 12 and 13	-65	+175	°C

ELECTRICAL CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	forward voltage	I _F = 1 A; T _j = T _{j max} ; see Figs 14 and 15	_	_	1.1	V
		I _F = 1 A; see Figs 14 and 15	_	_	1.3	V
$V_{(BR)R}$	reverse avalanche breakdown voltage	I _R = 0.1 mA				
	BYD33D		300	_	_	V
	BYD33G		500	_	_	V
	BYD33J		700	_	_	V
	BYD33K		900	_	_	V
	BYD33M		1100	_	_	V
	BYD33U		1300	_	_	V
	BYD33V		1500	_	_	V
I_R	reverse current	V _R = V _{RRMmax} ; see Fig.16	_	_	1	μΑ
		$V_R = V_{RRMmax}$; $T_j = 165 ^{\circ}\text{C}$; see Fig.16	_	_	100	μΑ
t _{rr}	reverse recovery time	when switched from				
	BYD33D to J	$I_F = 0.5 \text{ A to } I_R = 1 \text{ A};$	_	_	250	ns
	BYD33K and M	measured at $I_R = 0.25 A$ see Fig.21	_	_	300	ns
	BYD33U and V				500	ns
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; see Figs 17 and 18	_	20	_	pF

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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$\frac{ dI_R }{dt}$	maximum slope of reverse recovery current	when switched from $I_F = 1$ A to $V_R \ge 30$ V				
	BYD33D to J	and $dI_F/dt = -1 A/\mu s$; see Fig.20	_	_	6	A/μs
	BYD33K to V	1 19.20	_	_	5	A/μs

THERMAL CHARACTERISTICS

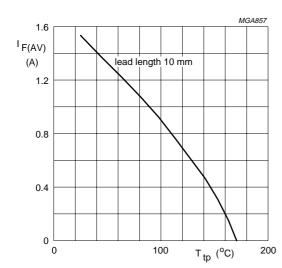
SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-tp}	thermal resistance from junction to tie-point	lead length = 10 mm	60	K/W
R _{th j-a}	thermal resistance from junction to ambient	note 1	120	K/W

Note

1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer \geq 40 μ m, see Fig.19. For more information please refer to the "General Part of associated Handbook".

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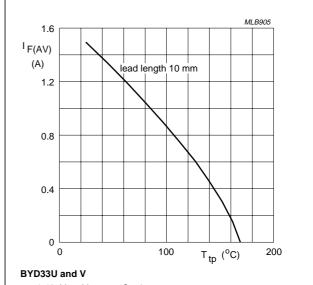
GRAPHICAL DATA



BYD33D to M

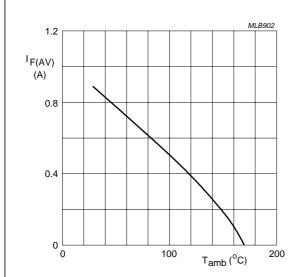
 $a = 1.42; \ V_R = V_{RRMmax}; \ \delta = 0.5.$ Switched mode application.

Fig.2 Maximum permissible average forward current as a function of tie-point temperature (including losses due to reverse leakage).



 $a = 1.42; \ V_R = V_{RRMmax}; \ \delta = 0.5.$ Switched mode application.

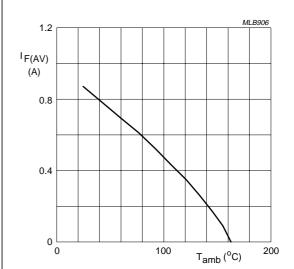
Fig.3 Maximum permissible average forward current as a function of tie-point temperature (including losses due to reverse leakage).



BYD33D to M

 $a=1.42;\ V_R=V_{RRMmax};\ \delta=0.5.$ Device mounted as shown in Fig.19. Switched mode application.

Fig.4 Maximum permissible average forward current as a function of ambient temperature (including losses due to reverse leakage).



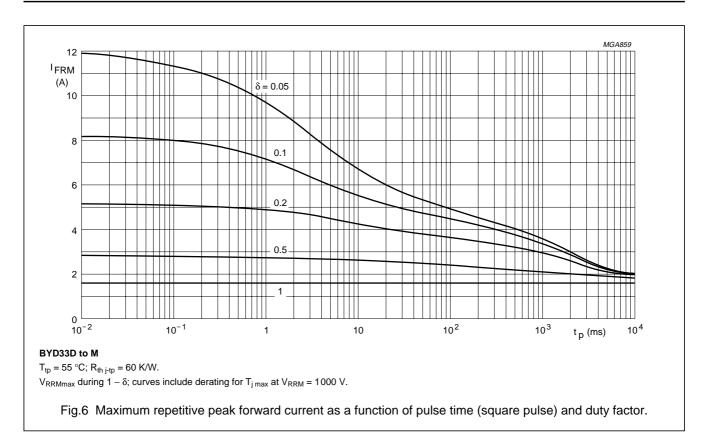
BYD33U and V

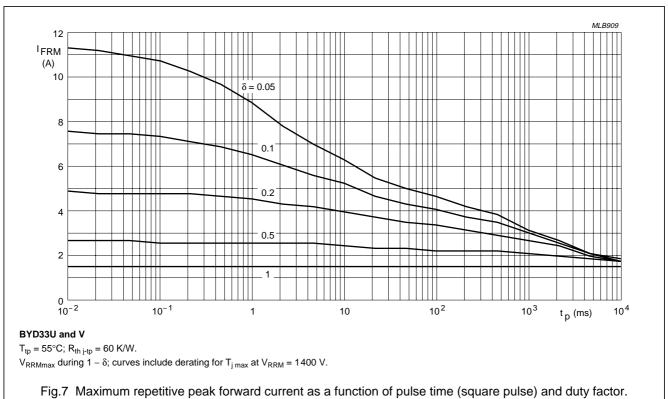
 $a=1.42;\ V_R=V_{RRMmax};\ \delta=0.5.$ Device mounted as shown in Fig.19. Switched mode application.

Fig.5 Maximum permissible average forward current as a function of ambient temperature (including losses due to reverse leakage).

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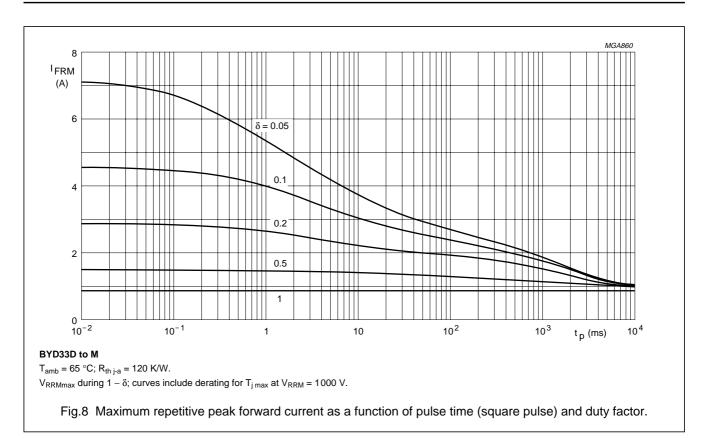
BYD33 series

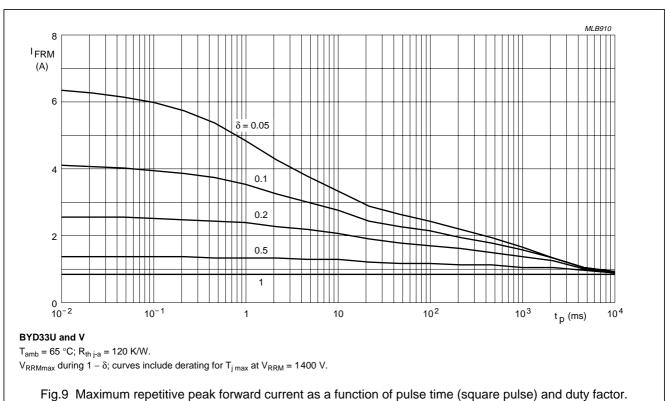




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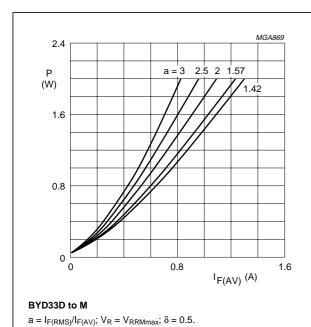


Fig.10 Maximum steady state power dissipation (forward plus leakage current losses, excluding switching losses) as a function of average forward current.

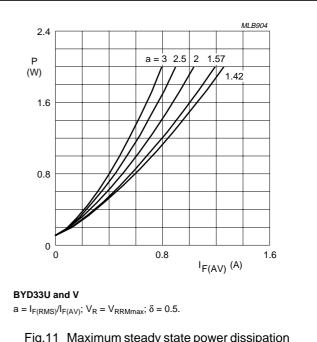
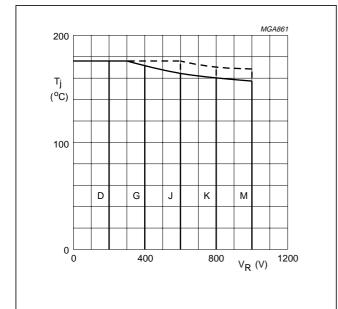


Fig.11 Maximum steady state power dissipation (forward plus leakage current losses, excluding switching losses) as a function of average forward current.

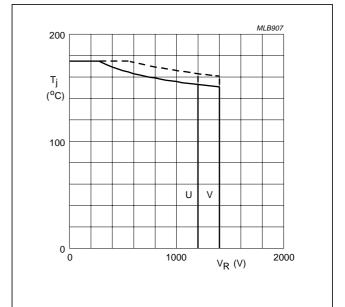


BYD33D to M

Solid line = V_R.

Dotted line = V_{RRM} ; δ = 0.5.

Fig.12 Maximum permissible junction temperature as a function of reverse voltage.



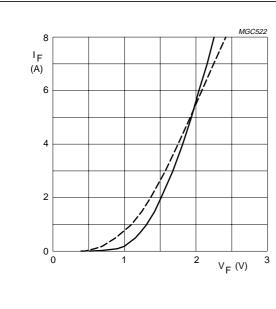
BYD33U and V

Solid line = V_R.

Dotted line = V_{RRM} ; $\delta = 0.5$.

Fig.13 Maximum permissible junction temperature as a function of reverse voltage.

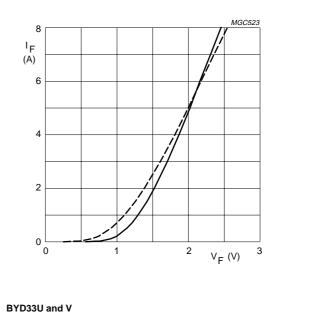
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BYD33D to M

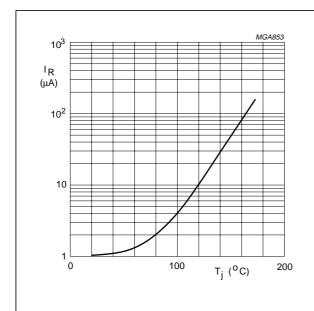
Solid line: $T_j = 25 \,^{\circ}\text{C}$. Dotted line: $T_j = 175 \,^{\circ}\text{C}$.

Fig.14 Forward current as a function of forward voltage; maximum values.



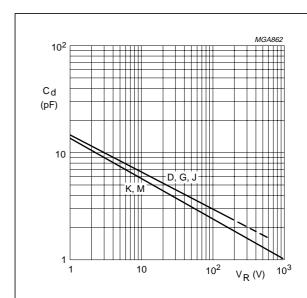
Solid line: $T_j = 25 \,^{\circ}\text{C}$. Dotted line: $T_j = 175 \,^{\circ}\text{C}$.

Fig.15 Forward current as a function of forward voltage; maximum values.



 $V_R = V_{RRMmax}$

Fig.16 Reverse current as a function of junction temperature; maximum values.

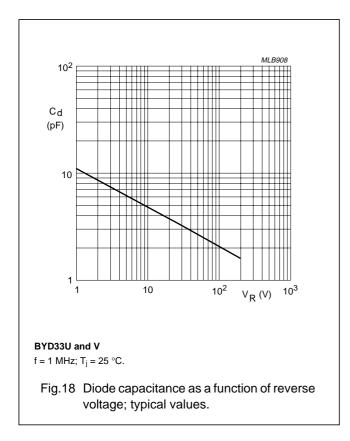


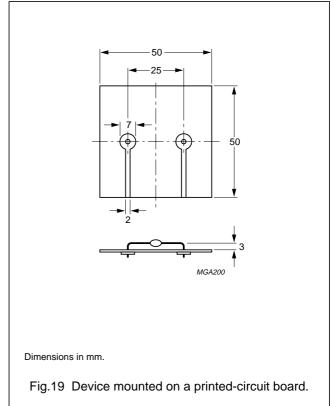
BYD33D to M

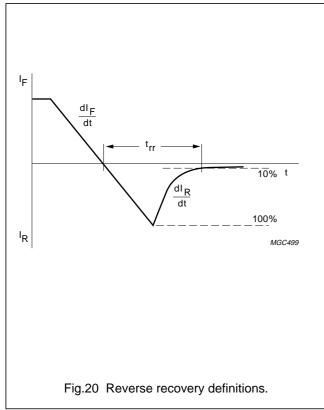
 $f = 1 \text{ MHz}; T_j = 25 \,^{\circ}\text{C}.$

Fig.17 Diode capacitance as a function of reverse voltage; typical values.

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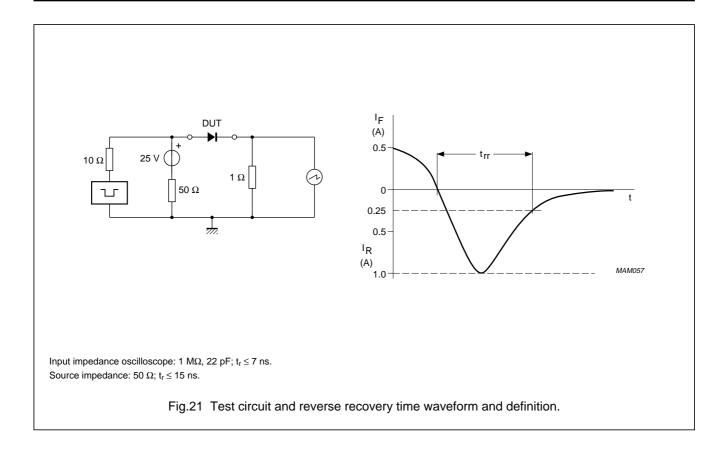






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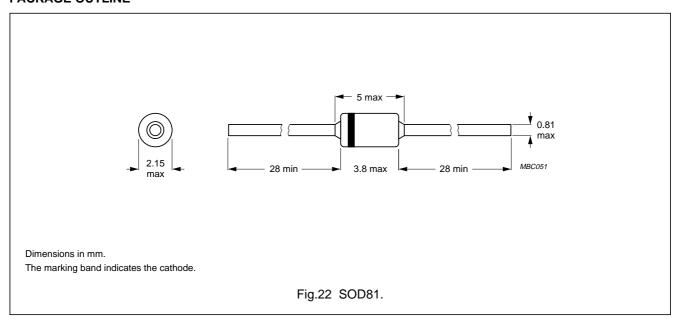
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PACKAGE OUTLINE



DEFINITIONS

Data Sheet Status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
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Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.