DISCRETE SEMICONDUCTORS





BYV2100
Fast soft-recovery
controlled avalanche rectifier

Product specification







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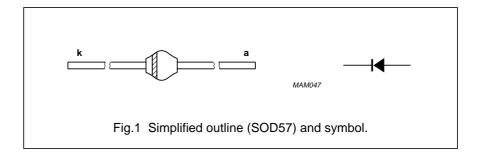
FEATURES

- · Glass passivated
- High maximum operating temperature
- · Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- Available in ammo-pack.

DESCRIPTION

Rugged glass SOD57 package, using a high temperature alloyed construction.

This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{RRM}	repetitive peak reverse voltage		_	100	V
V _R	continuous reverse voltage		_	100	V
I _{F(AV)}	average forward current	T _{tp} = 80 °C; lead length = 10 mm; averaged over any 20 ms period; see Fig.2; see also Fig.4	_	2.0	A
		T _{amb} = 60 °C; PCB mounting (see Fig.12); averaged over any 20 ms period; see Fig.3; see also Fig.4	_	1.3	A
I _{FRM}	repetitive peak forward current	T _{tp} = 80 °C; see Fig.6	_	18	А
		T _{amb} = 60 °C; see Fig.7	_	12	А
I _{FSM}	non-repetitive peak forward current	t = 10 ms half sine wave; $T_j = T_{j \text{ max}}$ prior to surge; $V_R = V_{RRMmax}$	_	50	A
E _{RSM}	non-repetitive peak reverse avalanche energy	L = 120 mH; $T_j = T_{j \text{ max}}$ prior to surge; inductive load switched off	_	20	mJ
T _{stg}	storage temperature		-65	+175	°C
Tj	junction temperature		-65	+175	°C

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ELECTRICAL CHARACTERISTICS

 $T_i = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	forward voltage	$I_F = 2 \text{ A}$; $T_j = T_{j \text{ max}}$; see Fig.5	_	_	0.78	V
		I _F = 2 A; see Fig.5	_	_	0.98	V
V _{(BR)R}	reverse avalanche breakdown voltage	I _R = 0.1 mA	120	_	_	V
I _R	reverse current	V _R = V _{RRMmax} ; see Fig.8	_	_	5	μΑ
		$V_R = V_{RRMmax}$; $T_j = 165 ^{\circ}C$; see Fig.8	_	_	150	μΑ
t _{rr}	reverse recovery time	when switched from $I_F = 0.5 \text{ A}$ to $I_R = 1 \text{ A}$; measured at $I_R = 0.25 \text{ A}$; see Fig.10	-	_	12.5	ns
C _d	diode capacitance	$f = 1 \text{ MHz}$; $V_R = 0 \text{ V}$; see Fig.9	_	135	_	pF
$\left \frac{dI_R}{dt} \right $	maximum slope of reverse recovery current	when switched from $I_F = 1$ A to $V_R \ge 30$ V and $dI_F/dt = -1$ A/ μ s; see Fig.11	-	-	2	A/μs

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-tp}	thermal resistance from junction to tie-point	lead length = 10 mm	46	K/W
R _{th j-a}	thermal resistance from junction to ambient	note 1	100	K/W

Note

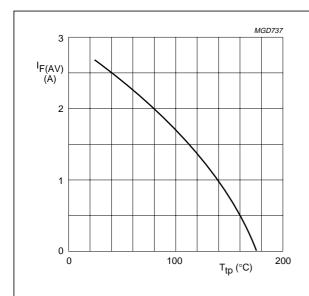
1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer \geq 40 μ m, see Fig.12. For more information please refer to the "General Part of associated Handbook".

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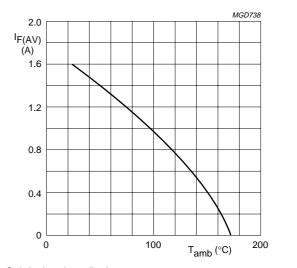
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GRAPHICAL DATA



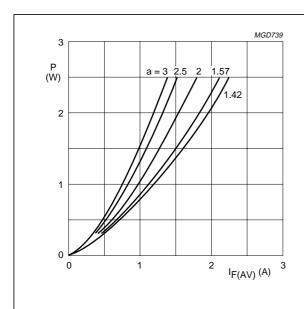
Switched mode application. a = 1.42; $\delta = 0.5$; $V_R = V_{RRMmax}$.

Fig.2 Maximum permissible average forward current as a function of tie-point temperature (including losses due to reverse leakage).



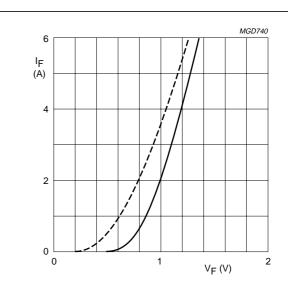
 $Switched \ mode \ application.$ $a=1.42; \ \delta=0.5; \ V_R=V_{RRMmax}.$ Device mounted as shown in Fig.12.

Fig.3 Maximum permissible average forward current as a function of ambient temperature (including losses due to reverse leakage).



 $a = I_{F(RMS)}/I_{F(AV)}; \delta = 0.5; V_R = V_{RRMmax}.$ Fig. 4. Maximum at a dy at at

Fig.4 Maximum steady state power dissipation (forward plus leakage current losses, excluding switching losses) as a function of average forward current.



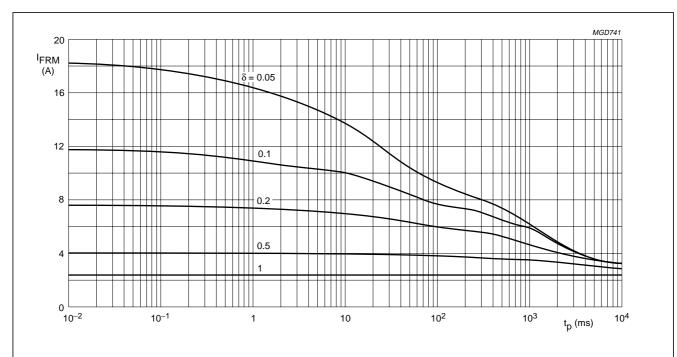
Dotted line: $T_j = 175$ °C. Solid line: $T_j = 25$ °C.

Fig.5 Maximum forward voltage as a function of forward current.

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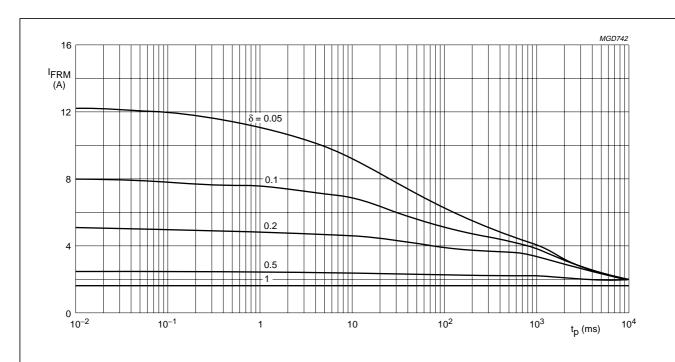
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 T_{tp} = 80 °C; $R_{th\;j\text{-}tp}$ = 46 K/W; V_R = V_{RRMmax} during 1 - $\delta.$

Fig.6 Maximum repetitive peak forward current as a function of pulse time (square pulse) and duty factor.



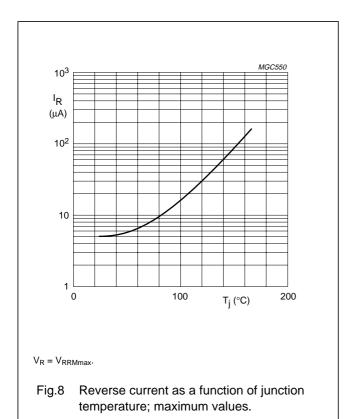
 T_{amb} = 60 °C; $R_{th~j\text{-}a}$ = 100 K/W; V_R = V_{RRMmax} during 1 - $\delta.$

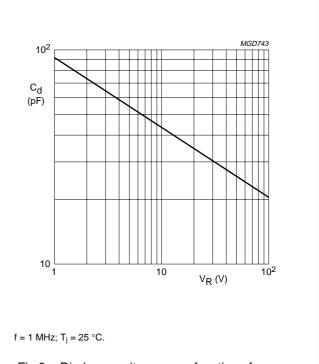
Fig.7 Maximum repetitive peak forward current as a function of pulse time (square pulse) and duty factor.

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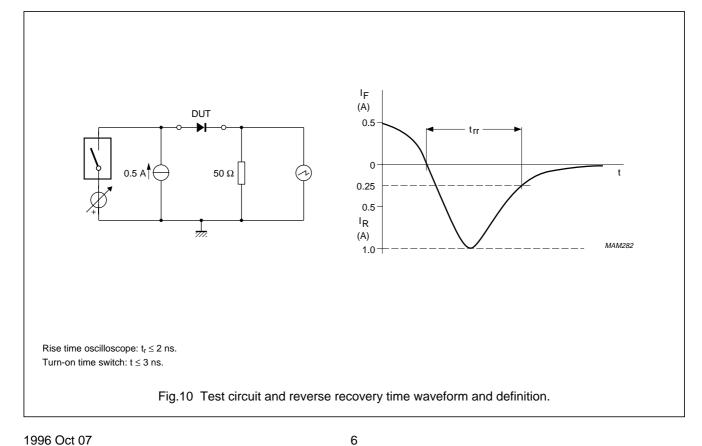
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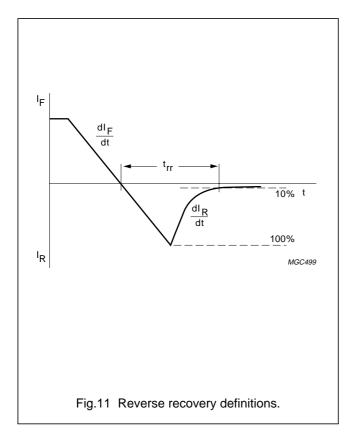


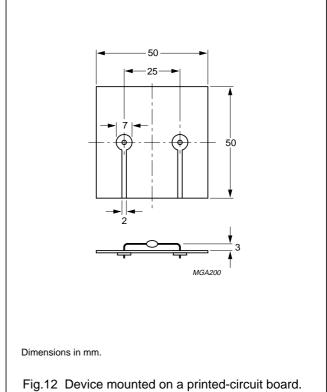
Diode capacitance as a function of reverse voltage; typical values.



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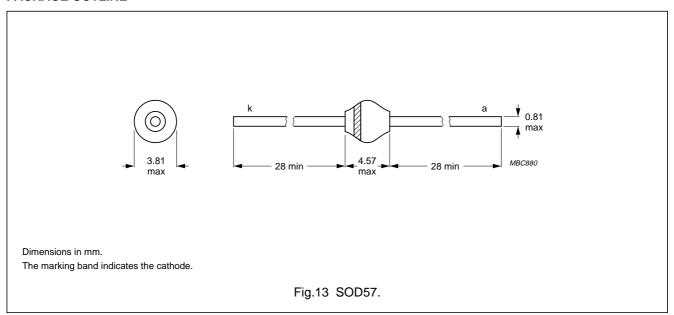


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PACKAGE OUTLINE



DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
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Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

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