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- EPIC™ (Enhanced-Performance Implanted CMOS) 1-μm Process
- Package Options Include Plastic Small-Outline (DW), Shrink Small-Outline (DB), Thin Shrink Small-Outline (PW), and DIP (N) Packages, Ceramic Chip Carriers (FK), Flat (W), and DIP (J) Packages

### description

These octal buffers and line drivers are designed specifically to improve the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters.

The 'AC240 are organized as two 4-bit buffers/drivers with separate output-enable  $(\overline{OE})$  inputs. When  $\overline{OE}$  is low, the device passes inverted data from the A inputs to the Y outputs. When  $\overline{OE}$  is high, the outputs are in the high-impedance state.

The SN54AC240 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74AC240 is characterized for operation from -40°C to 85°C.

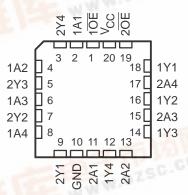
FUNCTION TABLE (each buffer)

INP	JTS	OUTPUT
OE	Α	Y
L	Н	L
L	L	Н
Н	X	Z

SN54AC240 . . . J OR W PACKAGE SN74AC240 . . . DB, DW, N, OR PW PACKAGE (TOP VIEW)

1 <u>0E</u>	1	U	20	] V <sub>CC</sub>
1A1	2		19	V <sub>CC</sub> 20E
2Y4	3		18	] 1Y1
1A2	4			2A4
2Y3	5		16	] 1Y2
1A3	6		15	] 2A3
2Y2	7		14	1Y3
1A4	8		13	] 2A2
2Y1	9		12	1Y4
GND	10		11	2A1
			_	

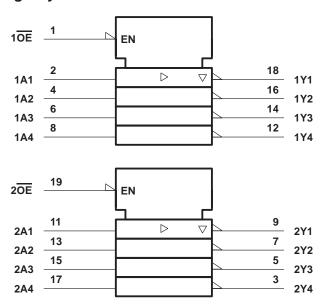
SN54AC240 . . . FK PACKAGE (TOP VIEW)



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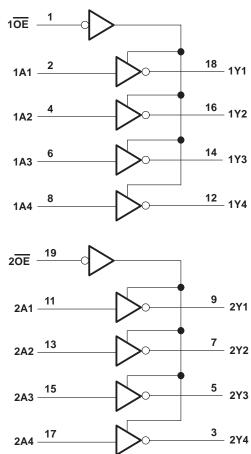
FPIC is a trademark of Texas Instruments Incorporated.

#### logic symbol†



<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

### logic diagram (positive logic)

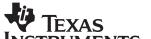


## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

N package PW packag	$\begin{array}{cccccccccccccccccccccccccccccccccccc$
Storage temperature range, T <sub>stg</sub>	

<sup>‡</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
  - 2. The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils, except for the N package, which has a trace length of zero.



## recommended operating conditions (see Note 3)

			SN54A	SN54AC240		SN54AC240 SN74AC240			UNIT
			MIN	MAX	MIN	MAX	UNII		
Vcc	Supply voltage		2	6	2	6	V		
		V <sub>CC</sub> = 3 V	2.1		2.1				
٧ <sub>IH</sub>	High-level input voltage	V <sub>CC</sub> = 4.5 V	3.15		3.15		V		
		V <sub>CC</sub> = 5.5 V	3.85		3.85				
		V <sub>CC</sub> = 3 V		0.9		0.9			
VIL	Low-level input voltage	V <sub>CC</sub> = 4.5 V		1.35		1.35	V		
		V <sub>CC</sub> = 5.5 V		1.65		1.65			
٧ <sub>I</sub>	Input voltage		0	VCC	0	VCC	V		
٧o	Output voltage		0	VCC	0	VCC	V		
		V <sub>CC</sub> = 3 V		-12		-12			
loH	High-level output current	V <sub>CC</sub> = 4.5 V		-24		-24	mA		
		V <sub>CC</sub> = 5.5 V		-24		-24			
		V <sub>CC</sub> = 3 V		12		12			
loL	Low-level output current	V <sub>CC</sub> = 4.5 V		24		24	mA		
		V <sub>CC</sub> = 5.5 V		24		24			
Δt/Δν	Input transition rise or fall rate		0	8	0	8	ns/V		
TA	Operating free-air temperature	-	-55	125	-40	85	°C		

NOTE 3: Unused inputs must be held high or low to prevent them from floating.

## SN54AC240, SN74AC240 OCTAL BUFFERS/DRIVERS WITH 3-STATE OUTPUTS

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# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	DAMETED	TEST CONDITIONS	V	T,	Δ = 25°C	;	SN54A	C240	SN74AC240		LINIT	
PA	RAMETER	TEST CONDITIONS	VCC	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT	
			3 V	2.9			2.9		2.9			
		IOH = - 50 μA	4.5 V	4.4			4.4		4.4			
		5.5 V	5.4			5.4		5.4				
\/a		I <sub>OH</sub> = - 12 mA	3 V	2.56			2.4		2.46		V	
VOH			4.5 V	3.86			3.7		3.76		V	
		I <sub>OL</sub> = – 24 mA	5.5 V	4.86			4.7		4.76			
		I <sub>OH</sub> = -50 mA <sup>†</sup>	5.5 V				3.85					
		I <sub>OH</sub> = -75 mA <sup>†</sup>	5.5 V						3.85			
			3 V			0.1		0.1		0.1		
		I <sub>OL</sub> = 50 μA	4.5 V			0.1		0.1		0.1	V	
			5.5 V			0.1		0.1		0.1		
\/a.		I <sub>OL</sub> = 12 mA	3 V			0.36		0.5		0.44		
VOL		lo. = 24 mA	4.5 V			0.36		0.5		0.44	V	
		I <sub>OL</sub> = 24 mA	5.5 V			0.36		0.5		0.44		
		I <sub>OL</sub> = 50 mA <sup>†</sup>	5.5 V					1.65				
		I <sub>OL</sub> = 75 mA <sup>†</sup>	5.5 V							1.65		
1.	Data inputs	V <sub>I</sub> = V <sub>CC</sub> or GND	5.5 V			±0.1		±1		±1		
Ħ	Control inputs	$V_I = V_{CC}$ or GND	3.5 V			±0.1		±1		±1	μΑ	
l <sub>OZ</sub> ‡		$V_O = V_{CC}$ or GND, $V_{I(OE)} = V_{IL}$ or $V_{IH}$	5.5 V			±0.25		±5		±2.5	μА	
ICC		$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V			4		80		40	μΑ	
Ci		$V_I = V_{CC}$ or GND	5 V		2.5						pF	

<sup>†</sup> Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

# switching characteristics over recommended operating free-air temperature range, $V_{\text{CC}}$ = 3.3 V $\pm$ 0.3 V (unless otherwise noted) (see Figure 1)

PARAMETER FROM TO		T,	<b>Վ = 25°</b> C	;	SN54A	C240	SN74A	C240	UNIT		
PARAMETER	(INPUT)	(OUTPUT)	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT	
t <sub>PLH</sub>	А	V	1.5	6	8	1	11	1	9	ns	
<sup>t</sup> PHL		1	1.5	5.5	8	1	10.5	1	8.5	115	
<sup>t</sup> PZH		<del>OE</del> Y	V	1.5	6	10.5	1	11.5	1	11	20
t <sub>PZL</sub>	OE	Ĭ	1.5	7	10	1	13	1	11	ns	
t <sub>PHZ</sub>	ŌĒ	OF V	V	1.5	7	10	1	12.5	1	10.5	nc
t <sub>PLZ</sub>	J OE	·	1.5	7.5	10.5	1	13.5	1	11.5	ns	



<sup>‡</sup> For I/O ports, the parameter I<sub>OZ</sub> includes the input leakage current.

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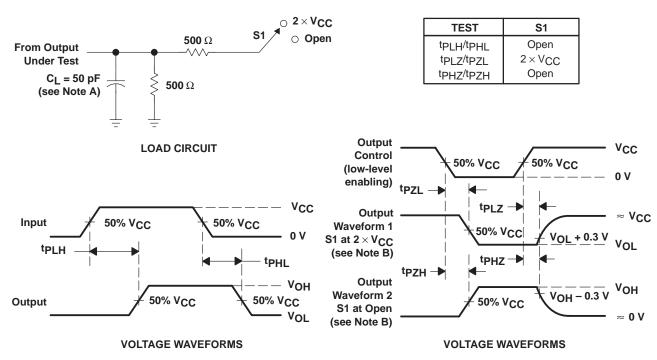
# switching characteristics over recommended operating free-air temperature range, $V_{CC}$ = 5 V $\pm$ 0.5 V (unless otherwise noted) (see Figure 1)

PARAMETER FROM TO		T,	<b>Վ = 25°</b> C	;	SN54A	C240	SN74A	C240	UNIT			
PARAMETER	(INPUT)	(OUTPUT)	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT		
t <sub>PLH</sub>	^	V	1.5	4.5	6.5	1	8.5	1	7	20		
<sup>t</sup> PHL	А	Ť	1.5	4.5	6	1	8	1	6.5	ns		
<sup>t</sup> PZH	ŌĒ	<del></del>	<u></u>	V	1.5	5	7	1	9	1	8	200
tPZL		ī	1.5	5.5	8	1	10.5	1	8.5	ns		
<sup>t</sup> PHZ	ŌĒ	V	2.5	6.5	9	1	10.5	1	9.5	ne		
t <sub>PLZ</sub>	OE .	T T	2	6.5	9	1	11	1	9.5	ns		

### operating characteristics, V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C

	PARAMETER	TEST CONDITIONS	TYP	UNIT
C <sub>pd</sub>	Power dissipation capacitance per buffer/driver	$C_L = 50 \text{ pF},  f = 1 \text{ MHz}$	45	pF

#### PARAMETER MEASUREMENT INFORMATION



NOTES: A. C<sub>L</sub> includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics:  $PRR \le 1 \text{ MHz}$ ,  $Z_O = 50 \Omega$ ,  $t_f \le 2.5 \text{ ns}$ ,  $t_f \le 2.5 \text{ ns}$ .
- D. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms



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