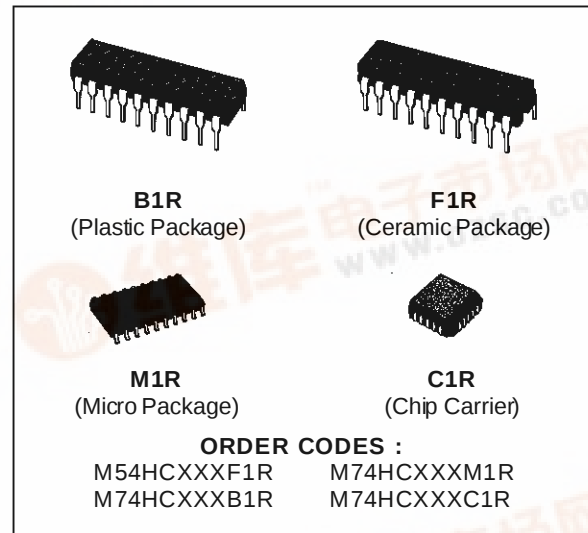




M54/74HC696/697
M54/74HC698/699

HC696/698 U/D DECADE COUNTER/REGISTER (3-STATE)
HC697/699 U/D 4 BIT BINARY COUNTER/REGISTER (3-STATE)

- **HIGH SPEED**
 $f_{MAX} = 50 \text{ MHz (TYP.) AT } V_{CC} = 5 \text{ V}$
- **LOW POWER DISSIPATION**
 $I_{CC} = 4 \mu\text{A (MAX.) AT } T_A = 25^\circ\text{C}$
- **HIGH NOISE IMMUNITY**
 $V_{NIH} = V_{NIL} = 28 \% V_{CC} \text{ (MIN.)}$
- **OUTPUT DRIVE CAPABILITY**
15 LSTTL LOADS (for Q_A to Q_D)
10 LSTTL LOADS (for RCO)
- **SYMMETRICAL OUTPUT IMPEDANCE**
 $|I_{OH}| = I_{OL} = 6 \text{ mA (MIN.) FOR } Q_A \text{ TO } Q_D$
 $|I_{OH}| = I_{OL} = 4 \text{ mA (MIN.) FOR RCO OUTPUT}$
- **BALANCED PROPAGATION DELAYS**
 $t_{PLH} = t_{PHL}$
- **WIDE OPERATING VOLTAGE RANGE**
 $V_{CC} \text{ (OPR)} = 2 \text{ V TO } 6 \text{ V}$
- **PIN AND FUNCTION COMPATIBLE**
WITH LSTTL 54/74LS696/697/698/699



DESCRIPTION

The HC696/697 are high speed CMOS up/down counters fabricated with silicon gate C^2 MOS technology. They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation. The HC696/698 are BCD DECADE COUNTER, and the HC697/699 are 4-BIT BINARY COUNTER. Both devices have register.

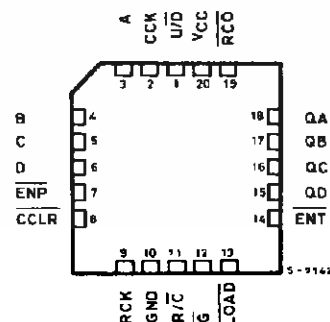
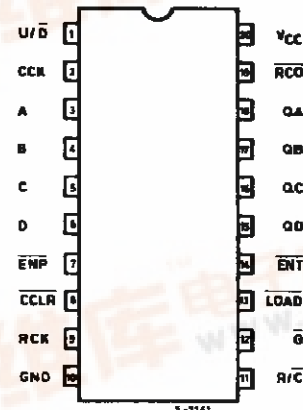
They count on the positive edge of the counter clock input (CCK) when selected by the "Counter Mode". If the input $U/\bar{D}$ is held "H", the internal counter counts up, and held "L", counts down. The internal counter's outputs are stored in the output register at the positive edge of register clock (RCK).

The counter features enable P and enable T and a ripple-carry output for easy expansion. the register/counter select input, R/C, selects the counter when low or the register when high for the three state outputs, Q_A , Q_B , Q_C and Q_D .

Both the counter clock CCK and register clock RCK are positive-edge triggered. The counter clear CCLR is active low and is synchronous for HC698/699, and asynchronous for HC696/697.

All inputs are equipped with protection circuits against static discharge and transient excess voltage.

PIN CONNECTIONS (top view)



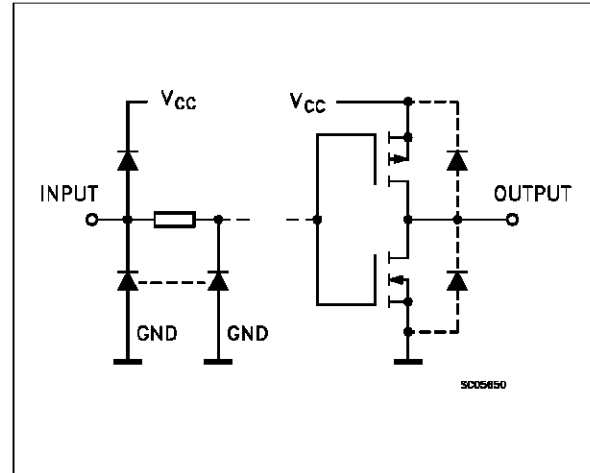
NC =
No Internal
Connection

M54/M74HC696/697/698/699

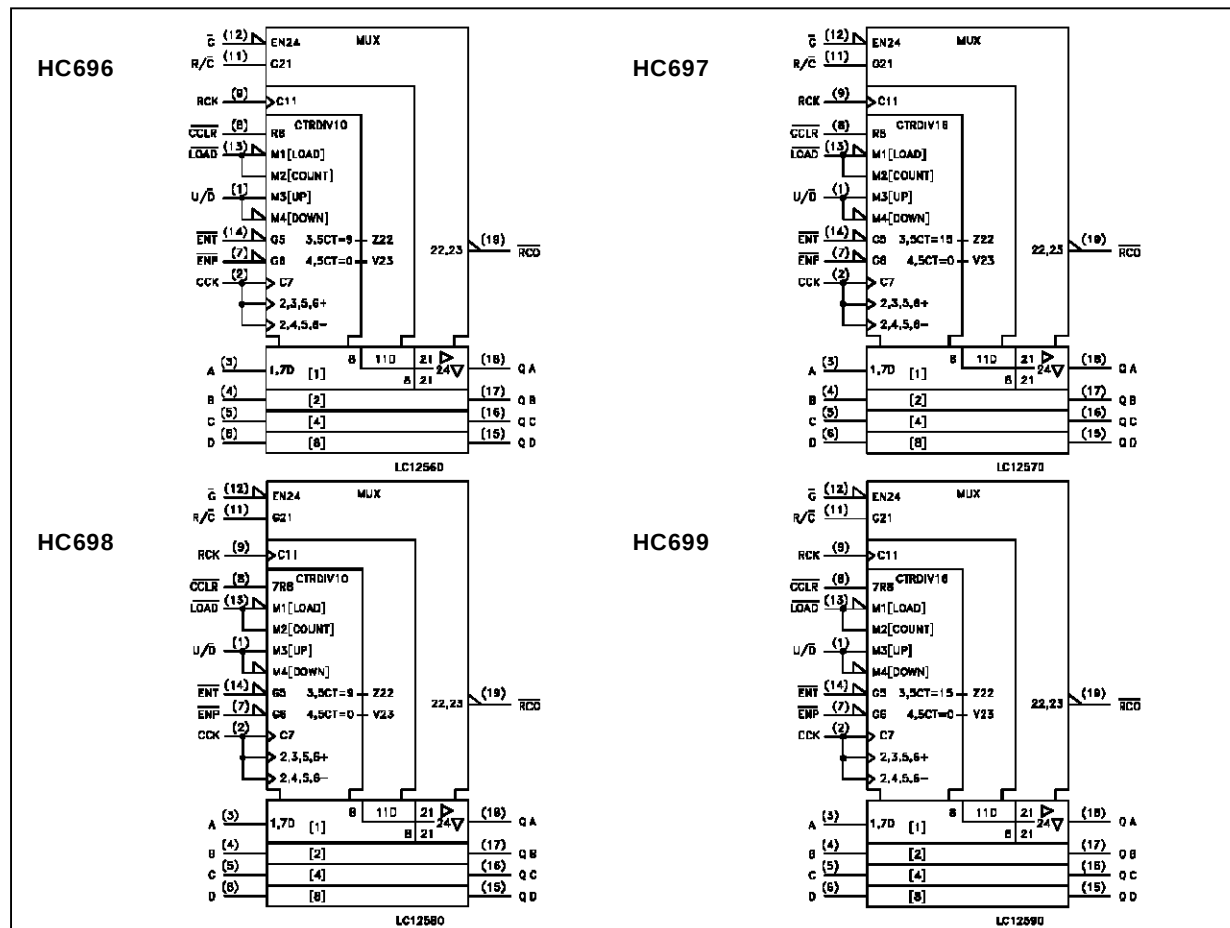
PIN DESCRIPTION

PIN No	SYMBOL	NAME AND FUNCTION
1	U/D	Up Down Counter Selector
2	CCK	Counter Clock
3, 4, 5, 6	A to D	Data Inputs
7, 14	EMP/ENT	Enable $\bar{P}$ and $\bar{T}$
8	CCLR	Counter Clear (Active LOW)
9	RCK	Register Clock
11	R/C	Register Counter Selector
12	$\bar{G}$	Enable Input
13	LOAD	Load Counter (Active LOW)
15, 16, 17, 18	QA to QD	Data Outputs
19	RCO	Load Counter (Active HIGH)
10	GND	Ground (0V)
20	V _{CC}	Positive Supply Voltage

INPUT AND OUTPUT EQUIVALENT CIRCUIT



IEC LOGIC SYMBOL



TRUTH TABLE

INPUTS									OUTPUTS				FUNCTION
CCLR	LOAD	ENP	ENT	CCK	U/D	RCK	R/C	G	QA	QB	QC	QD	
X	X	X	X	X	X	X	X	H	Z	Z	Z	Z	HIGH IMPEDANCE
L	X	X	X	X (*)	X	X	L	L	L	L	L	L	CLEAR COUNTER
H	L	X	X		X	X	L	L	a	b	c	d	LOAD COUNTER
H	H	H	X		X	X	L	L	NO CHANGE				NO COUNT
H	H	X	H		X	X	L	L	NO CHANGE				NO COUNT
H	H	L	L		H	X	L	L	COUNT UP				COUNT UP
H	H	L	L		L	X	L	L	COUNT DOWN				COUNT DOWN
H	X	X	X		X	X	L	L	NO CHANGE				NO COUNT
X	X	X	X	X	X		H	L	a'	b'	c'	d'	LOAD REGISTER
X	X	X	X	X	X		H	L	NO CHANGE				NO LOAD

(*) : For HC698/699

X : Don't care

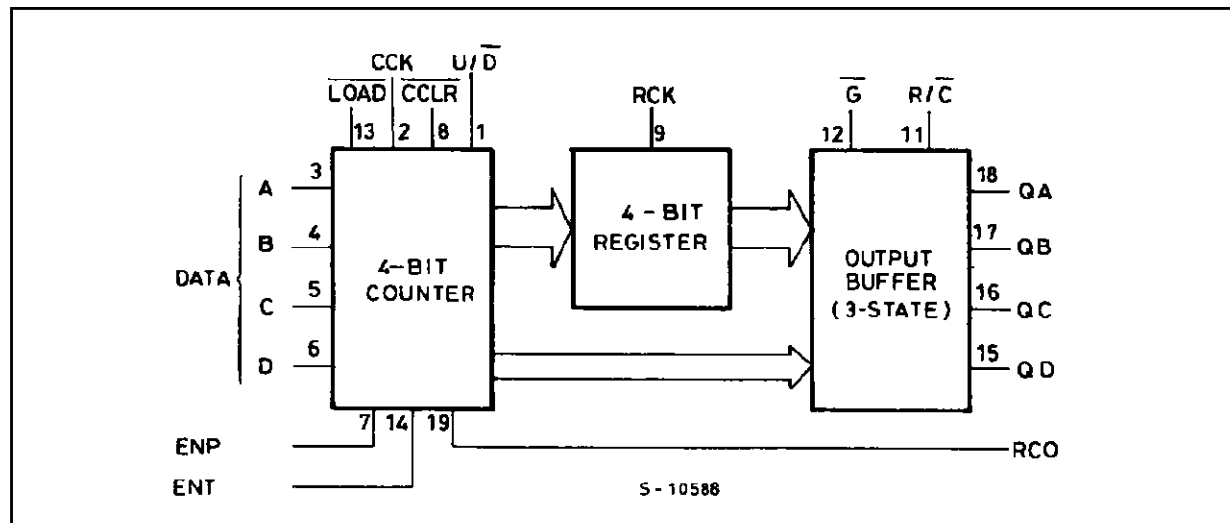
Z : High impedance

a-d : The level of steady state input at inputs a through D respectively

a'-d' : The level of steady state outputs at internal counter outputs QA' through QD' respectively

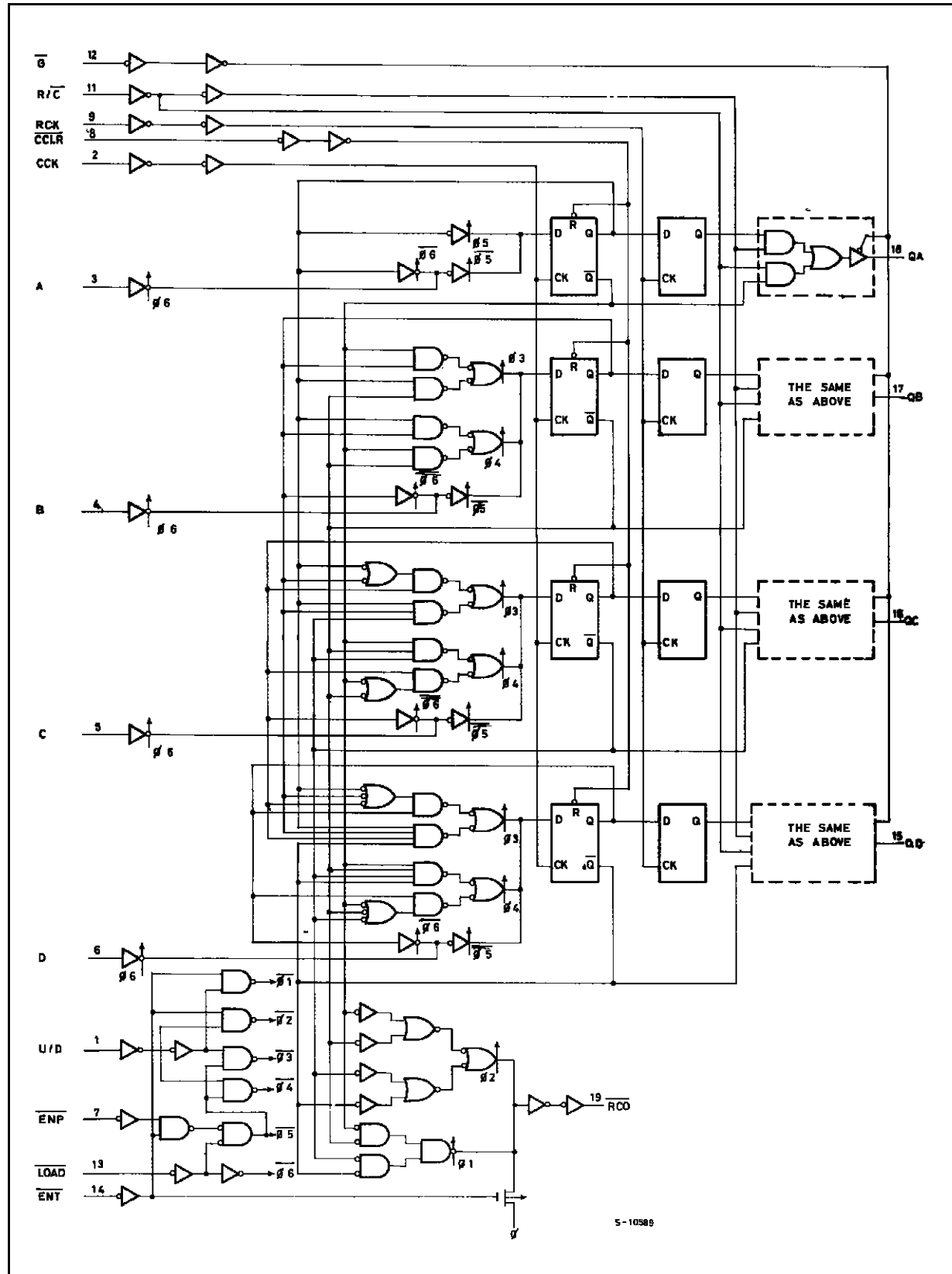
RCO function : HC696/8 - $RCO = (UP \cdot QA \cdot QD \cdot ENT + \overline{UP} \cdot \overline{QA} \cdot \overline{QD} \cdot ENT)$ HC697/9 - $RCO = (UP \cdot QA \cdot QB \cdot QC \cdot QD \cdot ENT + \overline{UP} \cdot \overline{QA} \cdot \overline{QB} \cdot \overline{QC} \cdot \overline{QD} \cdot ENT)$

BLOCK DIAGRAM

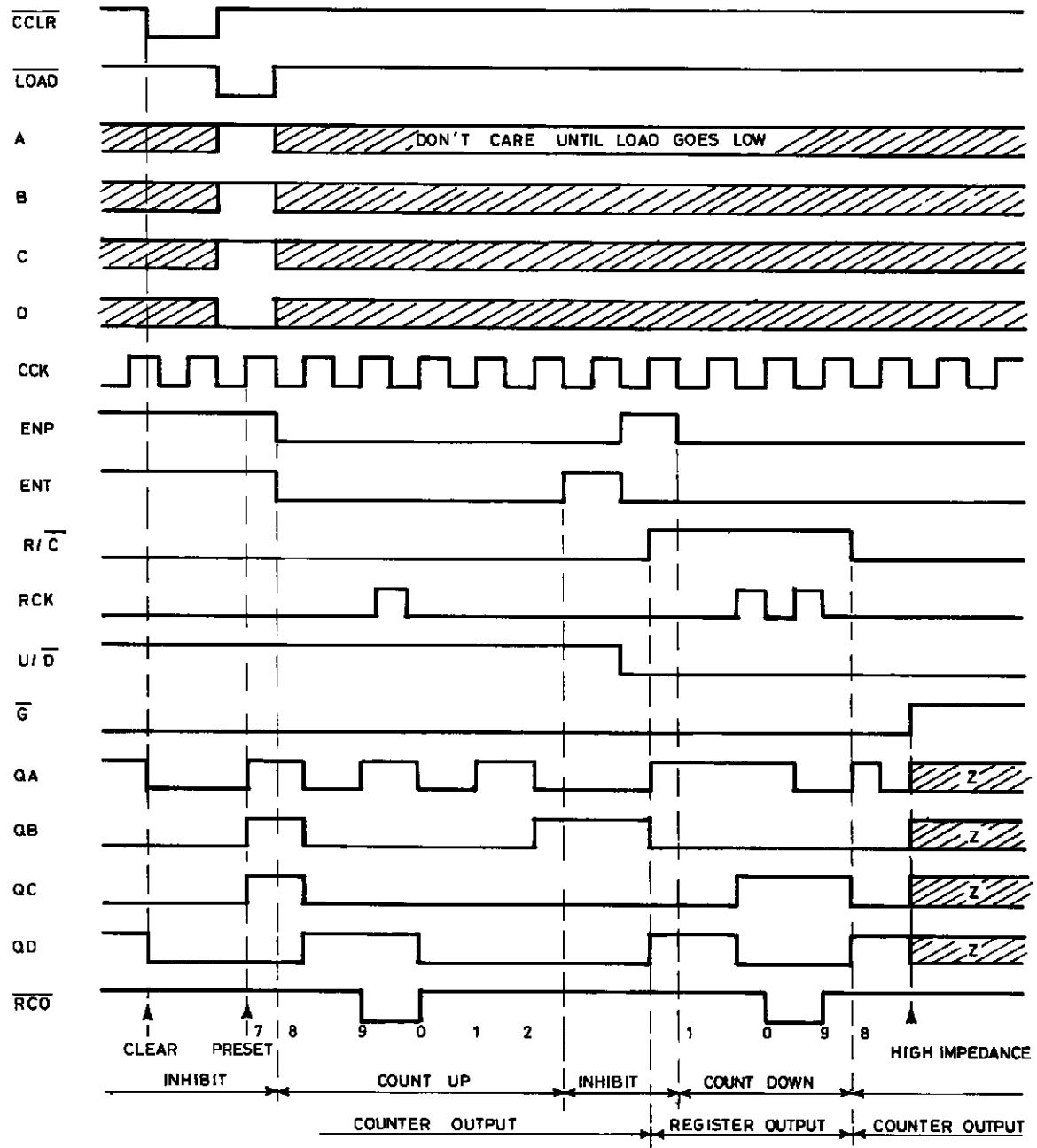


M54/M74HC696/697/698/699

LOGIC DIAGRAM (HC696)



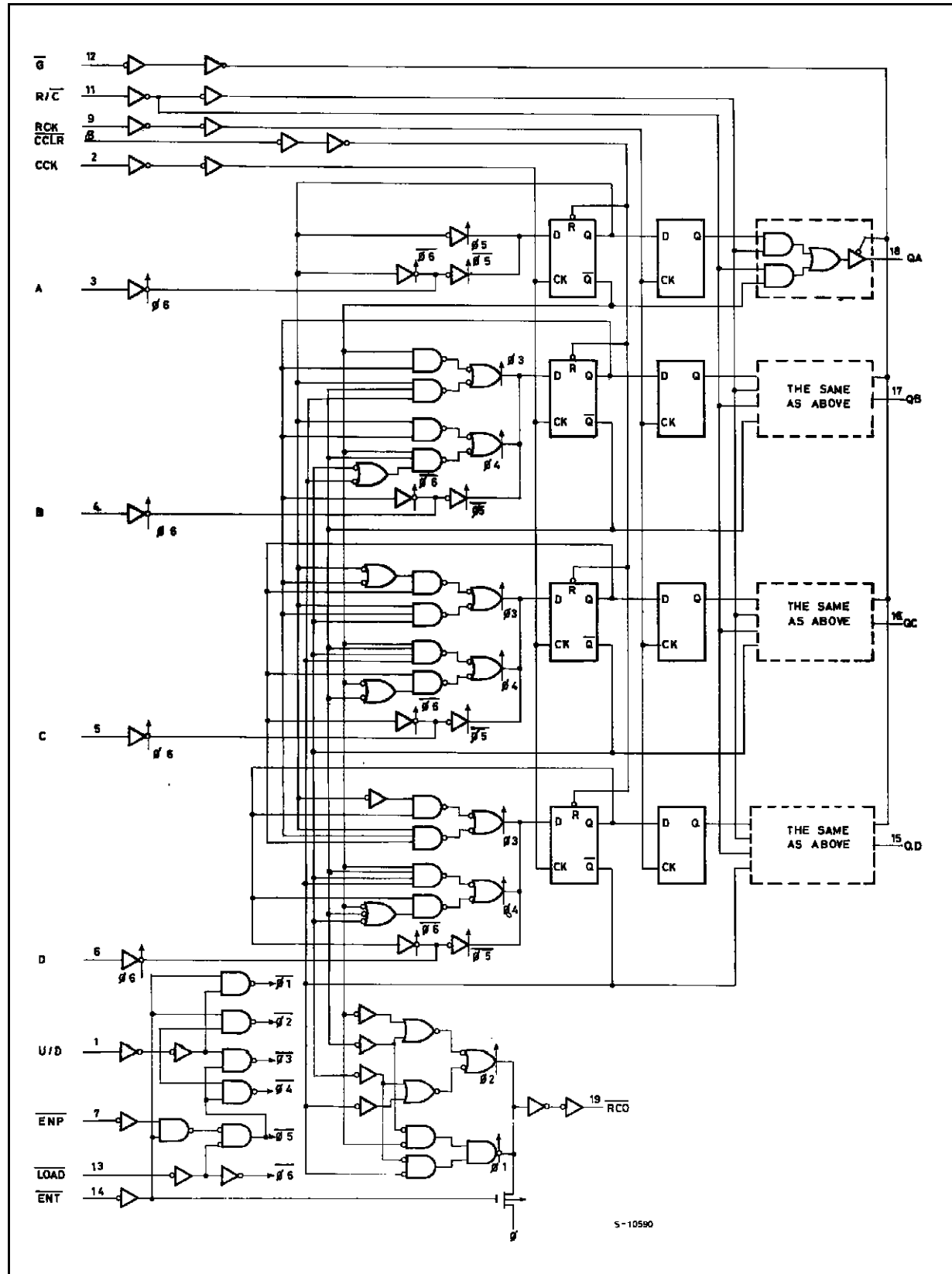
TIMING CHART (HC696)



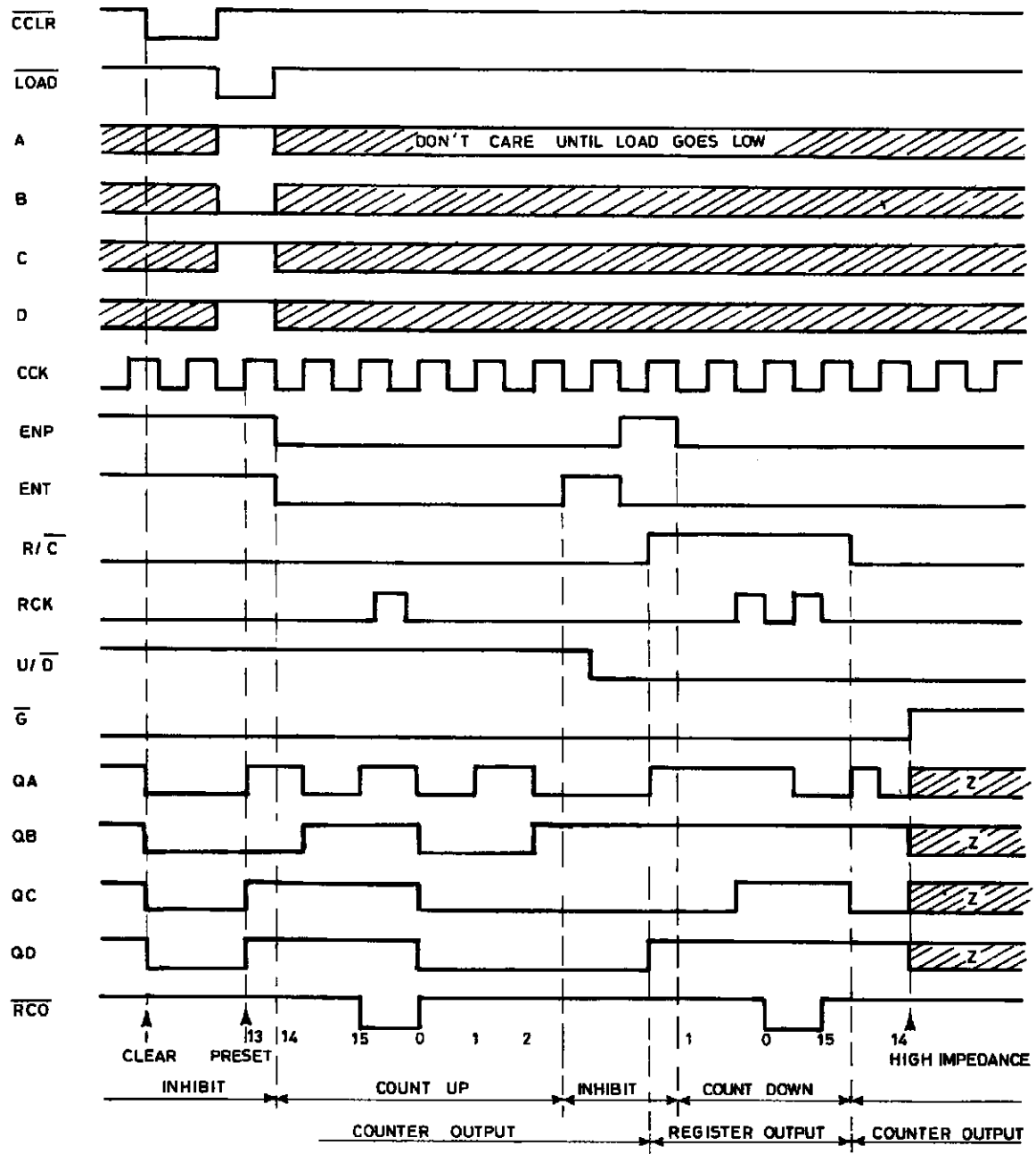
S-10591

M54/M74HC696/697/698/699

LOGIC DIAGRAM (HC697)

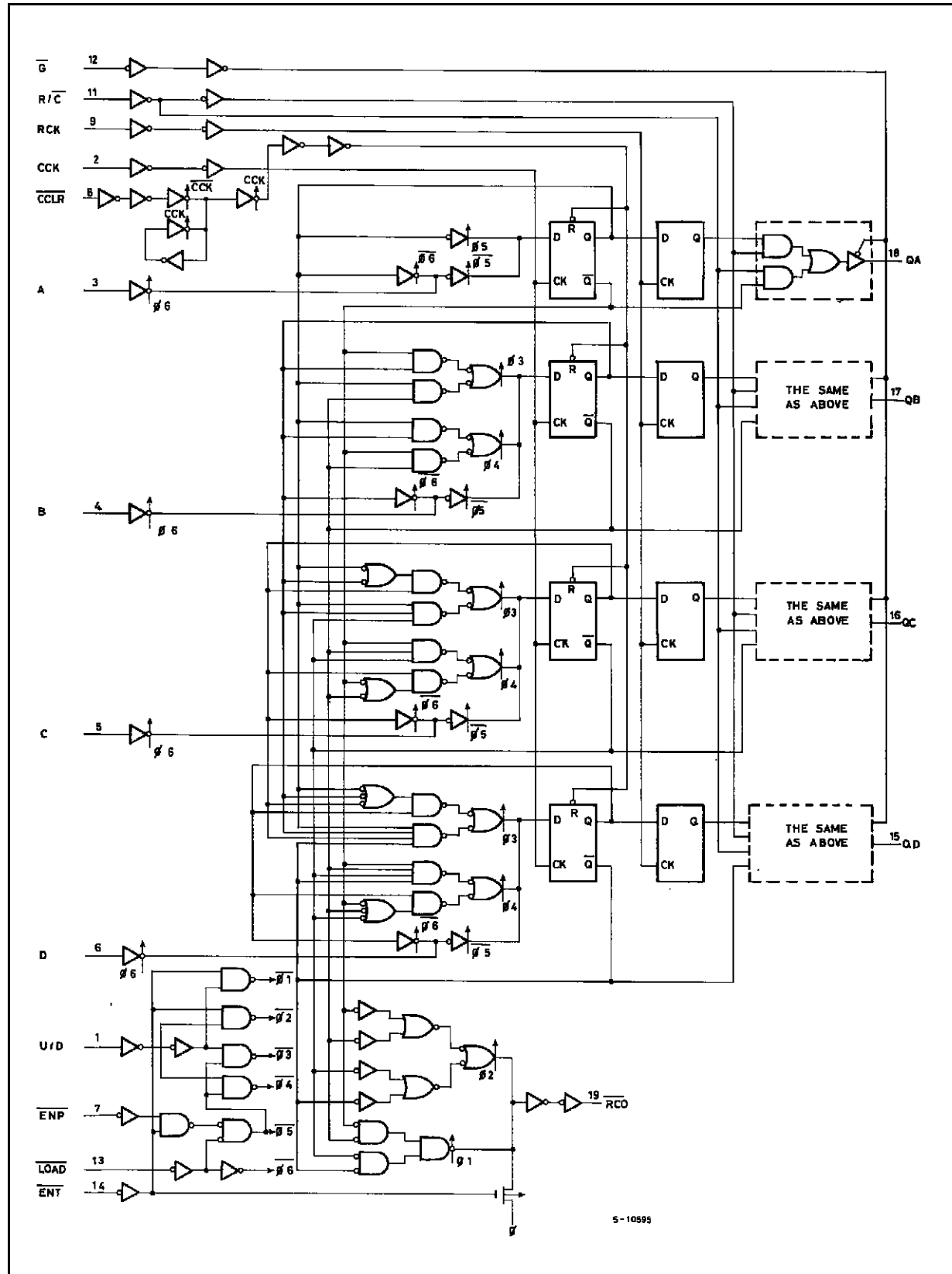


TIMING CHART (HC697)

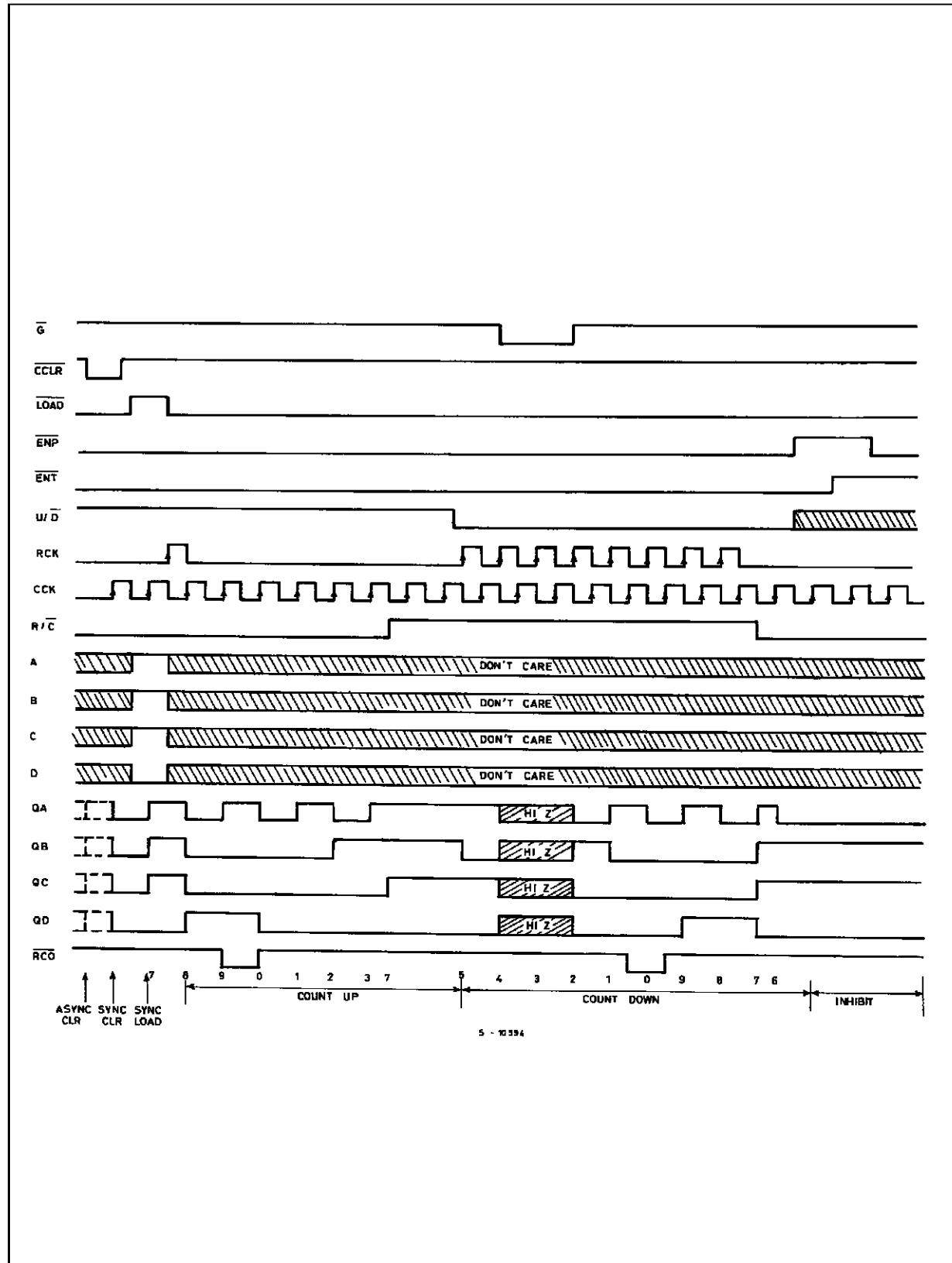


M54/M74HC696/697/698/699

LOGIC DIAGRAM (HC698)

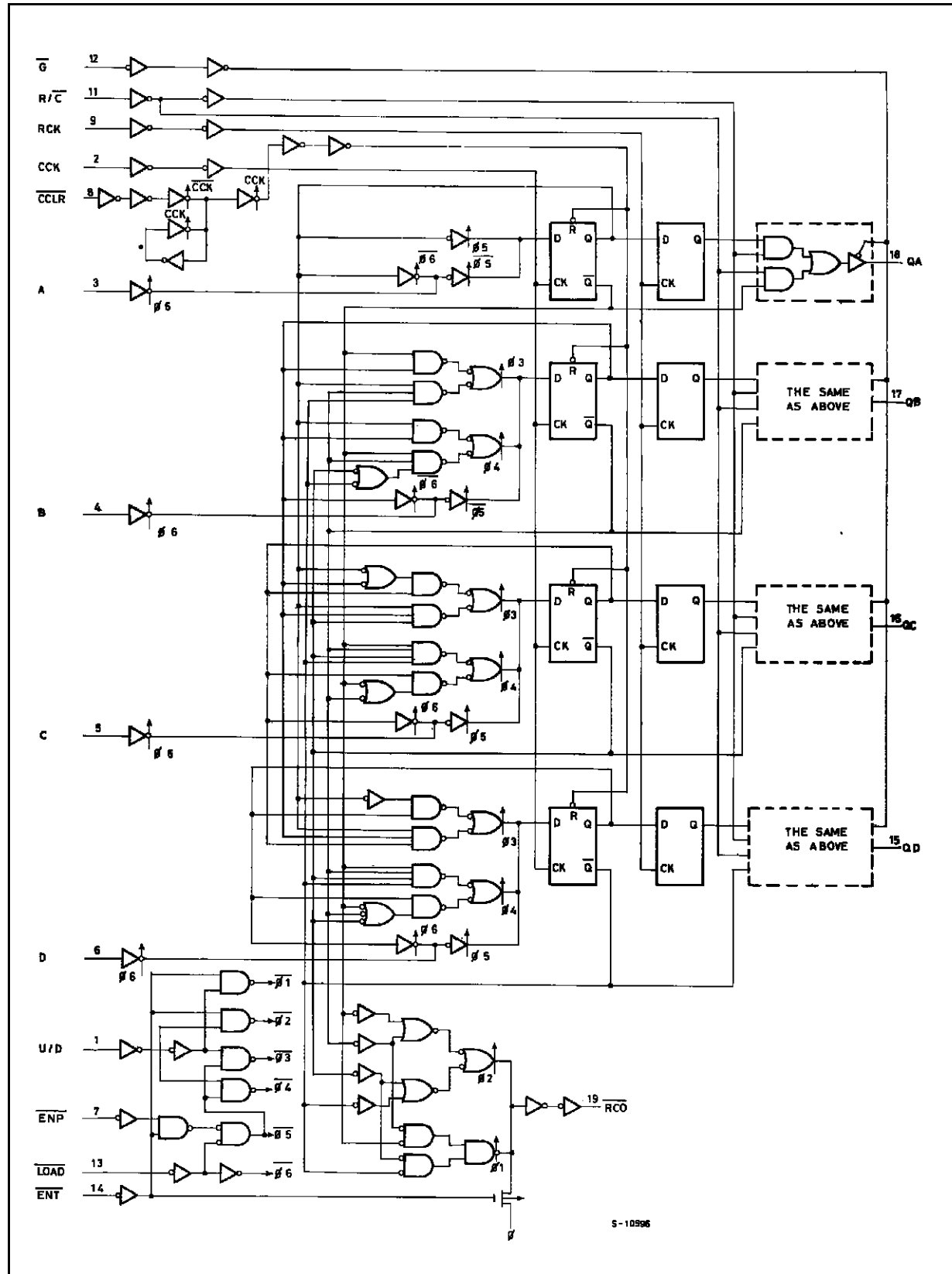


DECADE COUNTER, SYNCHRONOUS CLEAR (HC698)

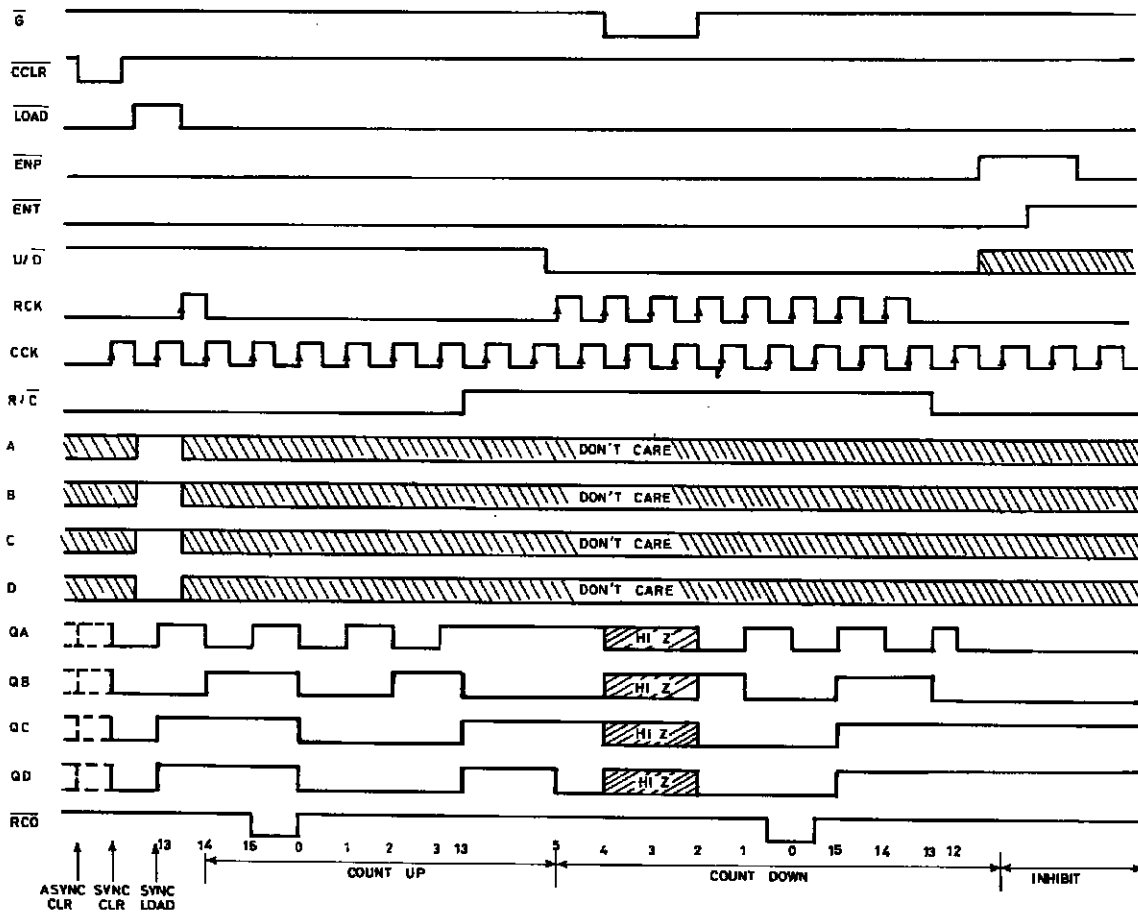


M54/M74HC696/697/698/699

LOGIC DIAGRAM (HC699)



BINARY COUNTER, SYNCHRONOUS CLEAR (HC699)



M54/M74HC696/697/698/699

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage	-0.5 to +7	V
V _I	DC Input Voltage	-0.5 to V _{CC} + 0.5	V
V _O	DC Output Voltage	-0.5 to V _{CC} + 0.5	V
I _{IK}	DC Input Diode Current	± 20	mA
I _{OK}	DC Output Diode Current	± 20	mA
I _O	DC Output Source Sink Current Per Output Pin (RCO) (QA to QD)	± 25 ± 35	mA
I _{CC} or I _{GND}	DC V _{CC} or Ground Current	± 70	mA
P _D	Power Dissipation	500 (*)	mW
T _{stg}	Storage Temperature	-65 to +150	°C
T _L	Lead Temperature (10 sec)	300	°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

(*) 500 mW: ≡ 65 °C derate to 300 mW by 10mW/°C: 65 °C to 85 °C

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage	2 to 6	V
V _I	Input Voltage	0 to V _{CC}	V
V _O	Output Voltage	0 to V _{CC}	V
T _{op}	Operating Temperature:	M54HC Series M74HC Series	-55 to +125 -40 to +85 °C
t _r , t _f	Input Rise and Fall Time	V _{CC} = 2 V	0 to 1000
		V _{CC} = 4.5 V	0 to 500
		V _{CC} = 6 V	0 to 400

DC SPECIFICATIONS

Symbol	Parameter	Test Conditions		Value						Unit		
		V _{CC} (V)		T _A = 25 °C 54HC and 74HC			-40 to 85 °C 74HC		-55 to 125 °C 54HC			
				Min.	Typ.	Max.	Min.	Max.	Min.		Max.	
V _{IH}	High Level Input Voltage	2.0		1.5			1.5		1.5		V	
		4.5		3.15		3.15		3.15				
		6.0		4.2		4.2		4.2				
V _{IL}	Low Level Input Voltage	2.0				0.5		0.5		0.5	V	
		4.5			1.35		1.35		1.35			
		6.0			1.8		1.8		1.8			
V _{OH}	High Level Output Voltage (RCO)	2.0	V _I = V _{IH} or V _{IL}	I _O =-20 μA	1.9	2.0		1.9		1.9		V
		4.5			4.4	4.5		4.4		4.4		
		6.0			5.9	6.0		5.9		5.9		
		4.5		I _O =-4.0 mA	4.18	4.31		4.13		4.10		
		6.0			I _O =-5.2 mA	5.68	5.8		5.63		5.60	
V _{OH}	High Level Output Voltage (QA - QD)	2.0	V _I = V _{IH} or V _{IL}	I _O =-20 μA		1.9	2.0		1.9		1.9	
		4.5			4.4	4.5		4.4		4.4		
		6.0			5.9	6.0		5.9		5.9		
		4.5		I _O =-6.0 mA	4.18	4.31		4.13		4.10		
		6.0			I _O =-7.8 mA	5.68	5.8		5.63		5.60	
V _{OL}	Low Level Output Voltage (RCO)	2.0	V _I = V _{IH} or V _{IL}	I _O = 20 μA			0.0	0.1		0.1		0.1
		4.5				0.0	0.1		0.1		0.1	
		6.0				0.0	0.1		0.1		0.1	
		4.5		I _O = 4.0 mA		0.17	0.26		0.33		0.40	
		6.0			I _O = 5.2 mA		0.18	0.26		0.33		0.40
V _{OL}	Low Level Output Voltage (QA - QD)	2.0	V _I = V _{IH} or V _{IL}	I _O = 20 μA			0.0	0.1		0.1		0.1
		4.5				0.0	0.1		0.1		0.1	
		6.0				0.0	0.1		0.1		0.1	
		4.5		I _O = 6.0 mA		0.17	0.26		0.33		0.40	
		6.0			I _O = 7.8 mA		0.18	0.26		0.33		0.40
I _I	Input Leakage Current	6.0	V _I = V _{CC} or GND				±0.1		±1		±1	μA
I _{OZ}	3 State Output Off State Current	6.0	V _I = V _{IH} or V _{IL} V _O = V _{CC} or GND			±0.5		±5		±10	μA	
I _{CC}	Quiescent Supply Current	6.0	V _I = V _{CC} or GND			4		40		80	μA	

M54/M74HC696/697/698/699

AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 6$ ns)

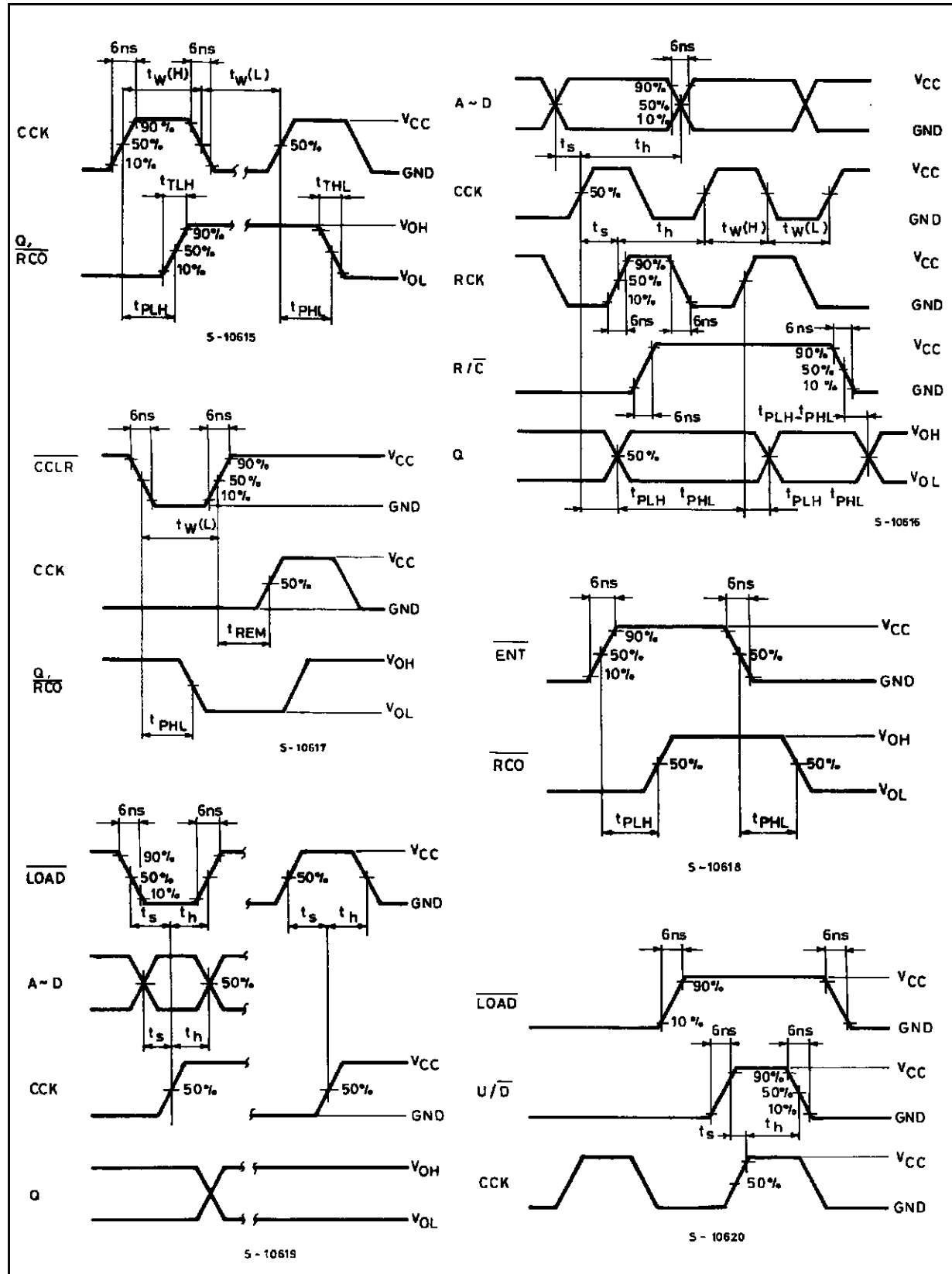
Symbol	Parameter	Test Conditions			Value						Unit	
		V _{CC} (V)	C _L (pF)		T _A = 25 °C 54HC and 74HC			-40 to 85 °C 74HC		-55 to 125 °C 54HC		
					Min.	Typ.	Max.	Min.	Max.	Min.		Max.
t _{TLH} t _{THL}	Output Transition Time (Q _n)	2.0 4.5 6.0	50		25 7 6	60 12 10		75 19 13		90 18 15	ns	
t _{TLH} t _{THL}	Output Transition Time (RCO)	2.0 4.5 6.0	50		30 8 7	75 15 13		95 19 16		115 23 20	ns	
t _{PLH} t _{PHL}	Propagation Delay Time (CCK - Q)	2.0 4.5 6.0	50		90 28 24	215 43 37		270 54 46		325 65 55	ns	
		2.0 4.5 6.0	150		103 32 27	245 49 42		305 61 52		370 74 63	ns	
t _{PLH} t _{PHL}	Propagation Delay Time (RCK - Q)	2.0 4.5 6.0	50		82 24 20	185 37 31		230 46 39		280 56 48	ns	
		2.0 4.5 6.0	150		95 28 24	215 43 37		270 54 46		325 65 55	ns	
t _{PLH} t _{PHL}	Propagation Delay Time (CCK - RCO)	2.0 4.5 6.0	50		109 32 27	245 49 42		305 61 52		370 74 63	ns	
t _{PLH} t _{PHL}	Propagation Delay Time (R/C - Q)	2.0 4.5 6.0	50		61 20 17	155 31 26		195 39 33		235 47 40	ns	
		2.0 4.5 6.0	150		73 24 20	185 37 31		230 46 39		280 56 48	ns	
t _{PLH} t _{PHL}	Propagation Delay Time (ENT - RCO)	2.0 4.5 6.0	50		63 18 15	140 28 24		175 35 30		210 42 36	ns	
t _{PLH} t _{PHL}	Propagation Delay Time (CCLR - Q)	2.0 4.5 6.0	50	for HC696 HC697	78 26 22	195 39 33		245 49 42		295 59 50	ns	
		2.0 4.5 6.0	150		90 30 26	235 47 40		295 59 50		355 71 60	ns	
t _{PLH} t _{PHL}	Propagation Delay Time (CCLR - RCO)	2.0 4.5 6.0	50		98 29 25	220 44 37		275 55 47		330 66 56	ns	
f _{MAX}	Maximum Clock Frequency	2.0 4.5 6.0	50		5 25 30	12 45 53		4 20 24		3.4 17 20	MHz	

AC ELECTRICAL CHARACTERISTICS (Continued)

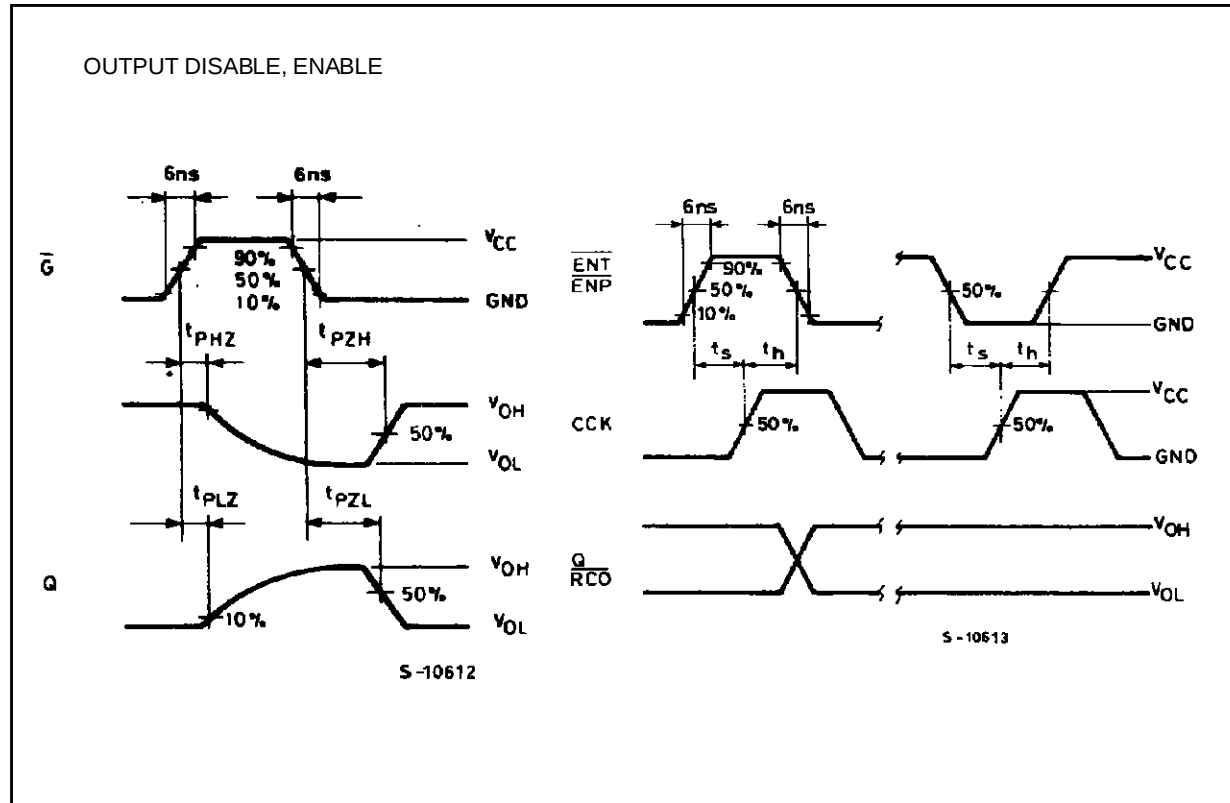
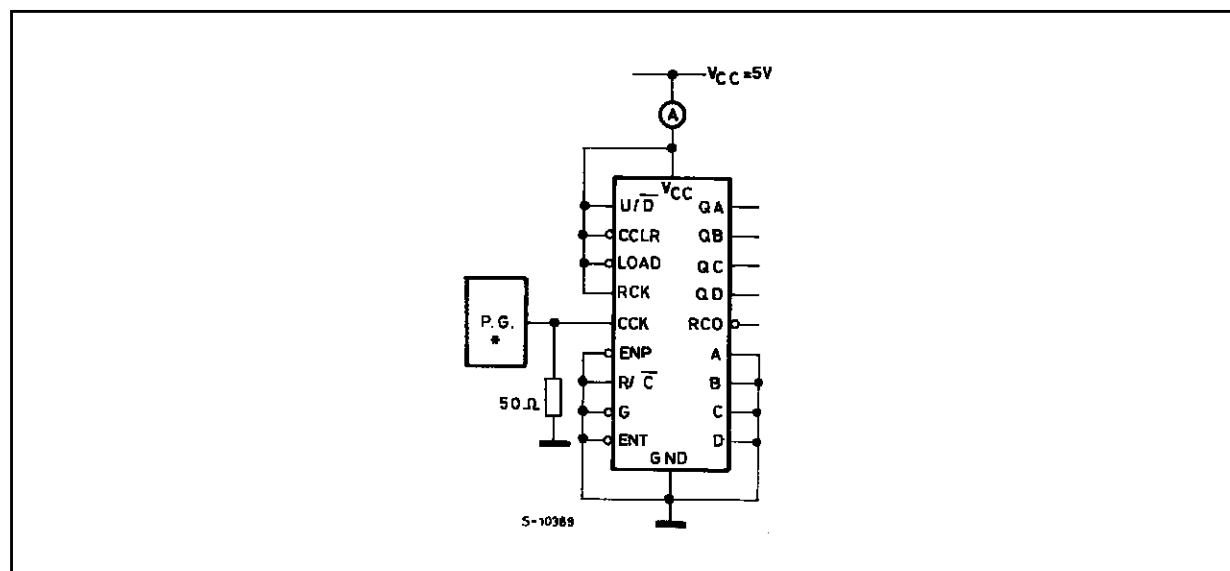
Symbol	Parameter	Test Conditions			Value						Unit		
		V _{CC} (V)	C _L (pF)		T _A = 25 °C 54HC and 74HC			-40 to 85 °C 74HC		-55 to 125 °C 54HC			
					Min.	Typ.	Max.	Min.	Max.	Min.		Max.	
t _{PZL} t _{PZH}	Output Enable Time	2.0	50	R _L = 1KΩ		45	120		150		180	ns	
		4.5				15	24		30		36		
		6.0				13	20		26		31		
		2.0	150	R _L = 1KΩ		57	150		190		225	ns	
		4.5				19	30		38		45		
		6.0				16	26		32		38		
t _{PLZ} t _{PHZ}	Output Disable Time	2.0	50	R _L = 1KΩ		32	115		145		175	ns	
		4.5				17	23		29		35		
		6.0				14	20		25		30		
t _{w(H)} t _{w(L)}	Minimum Pulse Width (CCK, RCK)	2.0	50			40	75		95		110	ns	
		4.5				8	15		19		22		
		6.0				7	13		16		19		
t _{w(L)}	Minimum Pulse Width (CCLR)	2.0	50	for HC696 HC697		40	75		95		110	ns	
		4.5				8	15		19		22		
		6.0				7	13		16		19		
t _s	Minimum Set-up Time (CCLR)	2.0	50	for HC698 HC699		16	50		65		75	ns	
		4.5				4	10		13		15		
		6.0				3	9		11		13		
t _s	Minimum Set-up Time (LOAD, ENT, ENP)	2.0	50			64	150		190		220	ns	
		4.5				16	30		38		44		
		6.0				14	26		32		37		
t _s	Minimum Set-up Time (A, B, C, D)	2.0	50			16	50		65		75	ns	
		4.5				4	10		13		15		
		6.0				3	9		11		13		
t _s	Minimum Set-up Time (CCK, RCK)	2.0	50			44	100		125		150	ns	
		4.5				11	20		25		30		
		6.0				9	17		21		26		
t _s	Minimum Set-up Time (U/D)	2.0	50			44	100		125		145	ns	
		4.5				11	20		25		29		
		6.0				9	17		21		25		
t _h	Minimum Hold Time	2.0	50				5		5		5	ns	
		4.5					5		5		5		
		6.0					5		5		5		
t _{REM}	Minimum Removal Time	2.0	50	for HC696 HC697			5		5		5	ns	
		4.5					5		5		5		
		6.0					5		5		5		
C _{IN}	Input Capacitance						5	10		10		10	pF
C _{PD} (*)	Power Dissipation Capacitance		HC696/697/699 HC698				71 77						pF

(*) C_{PD} is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to Test Circuit). Average operating current can be obtained by the following equation. I_{CC(OPR)} = C_{PD} • V_{CC} • f_{IN} + I_{CC2} (per circuit)

SWITCHING CHARACTERISTICS TEST WAVEFORM (HC696/697)

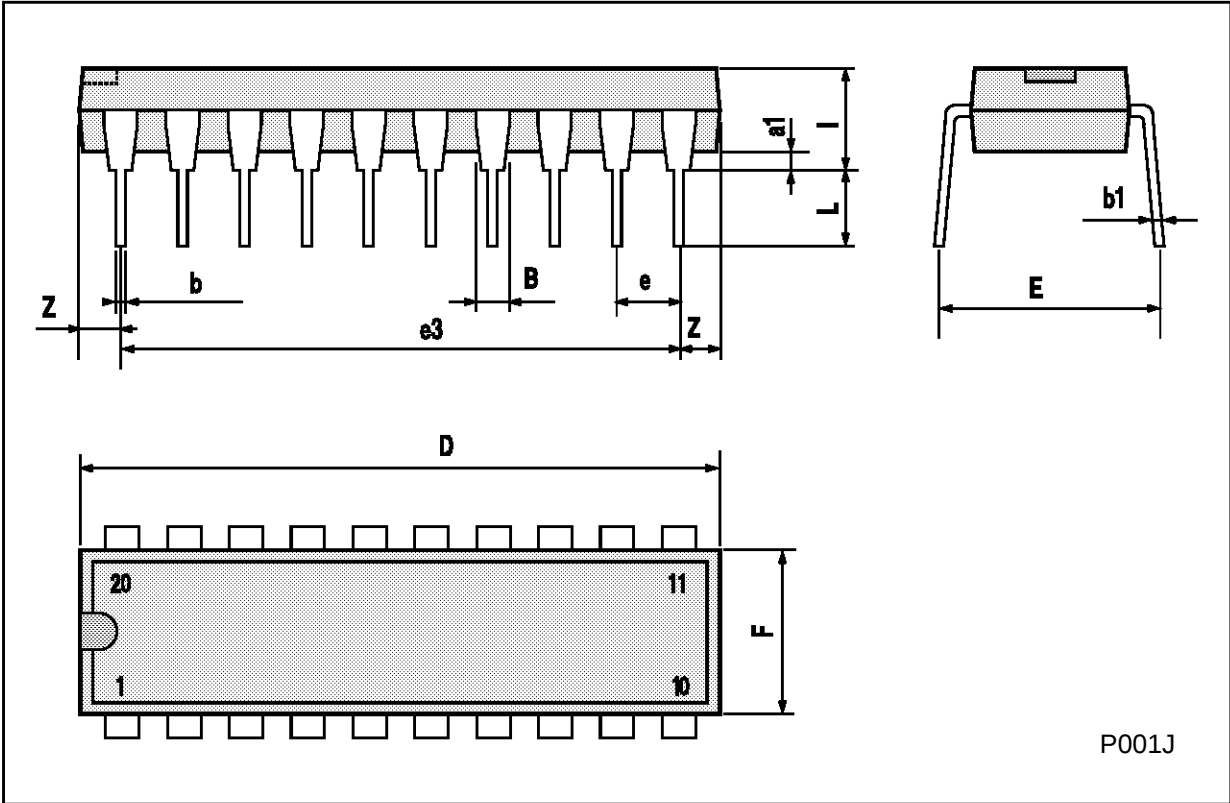


SWITCHING CHARACTERISTICS TEST WAVEFORM (continued)

TEST CIRCUIT I_{CC} (Opr.)

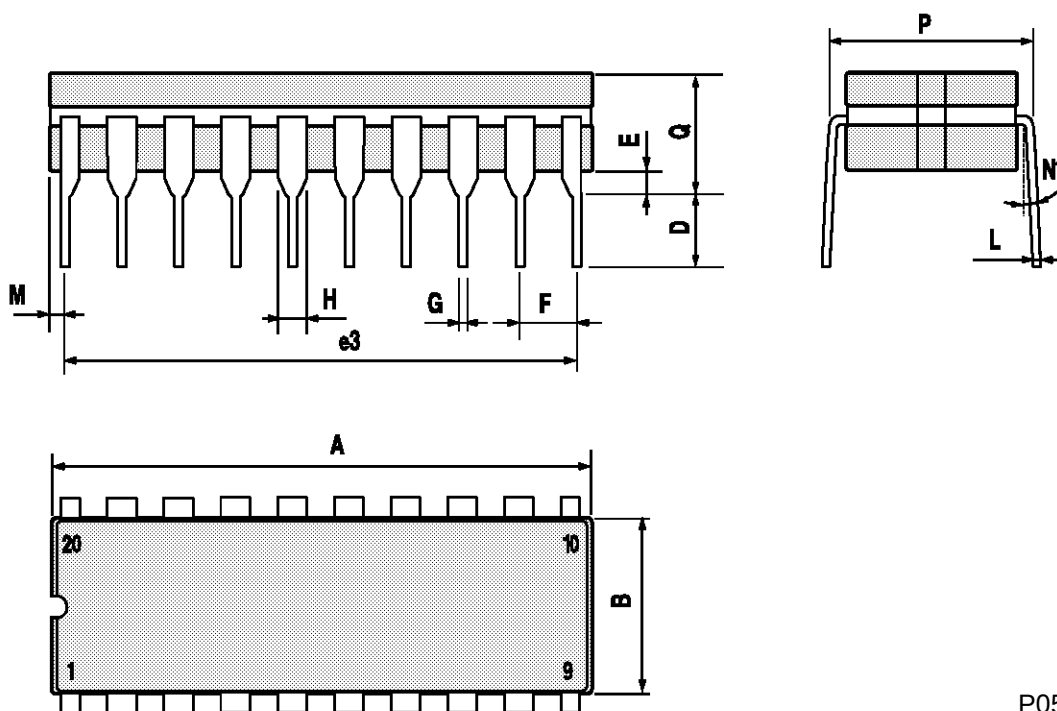
Plastic DIP20 (0.25) MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.254			0.010		
B	1.39		1.65	0.055		0.065
b		0.45			0.018	
b1		0.25			0.010	
D			25.4			1.000
E		8.5			0.335	
e		2.54			0.100	
e3		22.86			0.900	
F			7.1			0.280
I			3.93			0.155
L		3.3			0.130	
Z			1.34			0.053



Ceramic DIP20 MECHANICAL DATA

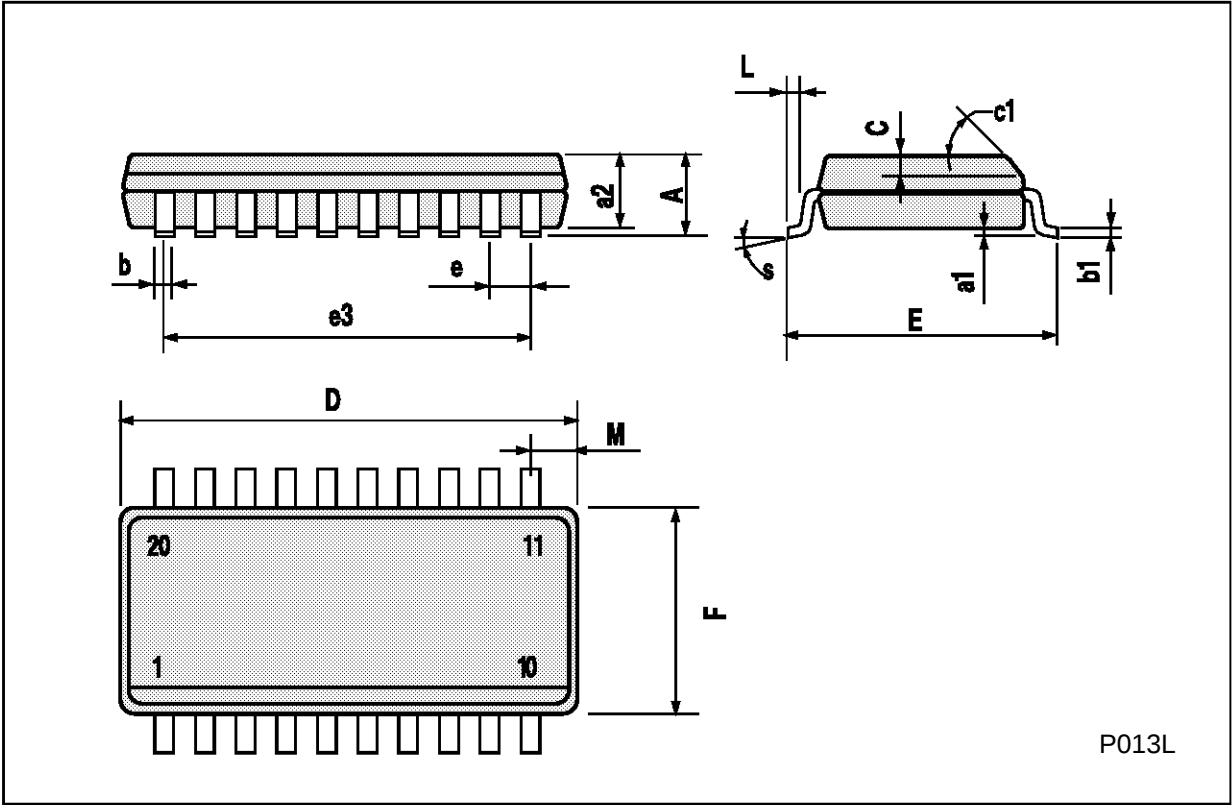
DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			25			0.984
B			7.8			0.307
D		3.3			0.130	
E	0.5		1.78	0.020		0.070
e3		22.86			0.900	
F	2.29		2.79	0.090		0.110
G	0.4		0.55	0.016		0.022
I	1.27		1.52	0.050		0.060
L	0.22		0.31	0.009		0.012
M	0.51		1.27	0.020		0.050
N1	4° (min.), 15° (max.)					
P	7.9		8.13	0.311		0.320
Q			5.71			0.225



P057H

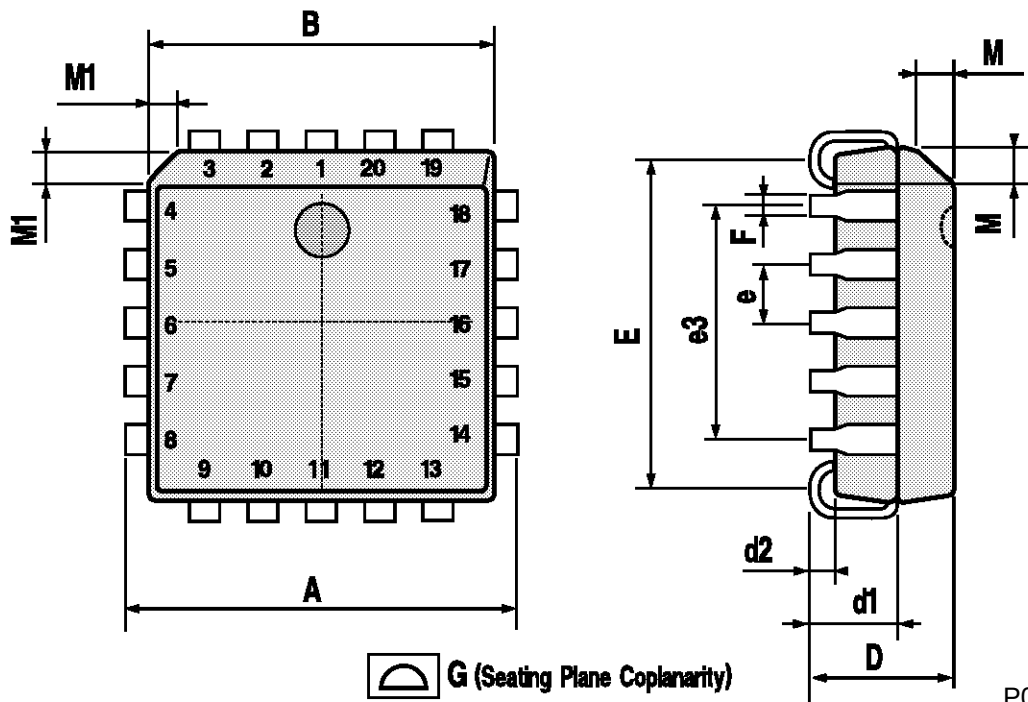
SO20 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			2.65			0.104
a1	0.10		0.20	0.004		0.007
a2			2.45			0.096
b	0.35		0.49	0.013		0.019
b1	0.23		0.32	0.009		0.012
C		0.50			0.020	
c1	45° (typ.)					
D	12.60		13.00	0.496		0.512
E	10.00		10.65	0.393		0.419
e		1.27			0.050	
e3		11.43			0.450	
F	7.40		7.60	0.291		0.299
L	0.50		1.27	0.19		0.050
M			0.75			0.029
S	8° (max.)					



PLCC20 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	9.78		10.03	0.385		0.395
B	8.89		9.04	0.350		0.356
D	4.2		4.57	0.165		0.180
d1		2.54			0.100	
d2		0.56			0.022	
E	7.37		8.38	0.290		0.330
e		1.27			0.050	
e3		5.08			0.200	
F		0.38			0.015	
G			0.101			0.004
M		1.27			0.050	
M1		1.14			0.045	



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