

COMPOUND TRANSISTOR

on-chip resistor NPN silicon epitaxial transistor For mid-speed switching

The CE2F3P is a transistor of on-chip high hFE resistor incorporating dumper diode in collector to emitter as protect elements. This transistor is ideal for actuator drives of OA equipments and electric equipments.

FEATURES

- On-chip bias resistor: $R_1 = 2.2 \text{ k}\Omega$, $R_2 = 10 \text{ k}\Omega$
- Low power consumption during driving: $V_{OL} = 0.12 \ V \quad @V_I = 5.0 \ V, \ I_C = 0.5 \ A$
- On-chip dumper diode for reverse cable

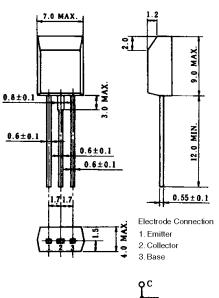
ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	Vсво	60	V
Collector to emitter voltage	VCEO	60	V
Emitter to base voltage	Vebo	15	V
Collector current (DC)	IC(DC)	±2.0	А
Collector current (Pulse)	IC(pulse) *	±3.0	А
Base current (DC)	IB(DC)	0.03	А
Total power dissipation	Р⊤	1.0	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

* PW \leq 10 ms, duty cycle \leq 50 %

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

PACKAGE DRAWING (UNIT: mm)



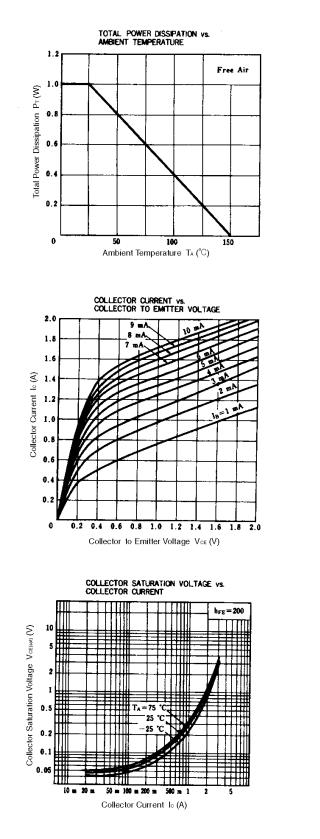


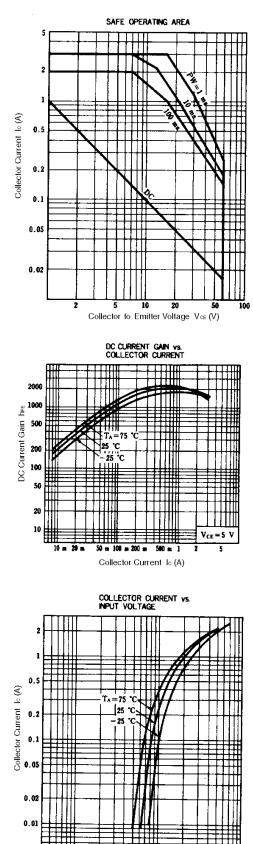
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	$V_{CB} = 40 \text{ V}, \text{ I}_{E} = 0$			100	nA
DC current gain	hfe1 **	Vce = 5.0 V, Ic = 0.2 A	700	1200		-
DC current gain	hFE2 **	Vce = 5.0 V, Ic = 1.0 A	1000	1600	3000	-
DC current gain	hfe3 **	Vce = 5.0 V, Ic = 2.0 A	500	1200		-
Low level output voltage	Vol **	VI = 5.0 V, Ic = 0.5 A		0.12	0.3	V
Low level input voltage	VIL **	$V_{CE} = 12 \text{ V}, \text{ Ic} = 100 \ \mu\text{A}$		0.5	0.4	V
Input resistance 1	R1		1.54	2.2	2.86	kΩ
Input resistance 2	R2		7.0	10.0	13.0	kΩ
Turn-on time	ton	Ic = 1.0 A		0.4		μs
Storage time	tstg	Iві = —Iв2 = 10 mA		1.4		μs
Fall time	tr	$V_{CC} = 20 \text{ V}, \text{ RL} = 20 \Omega$		0.5		μs

** Pulse test PW \leq 350 μ s, duty cycle \leq 2 %

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TYPICAL CHARACTERISTICS (Ta = 25°C)



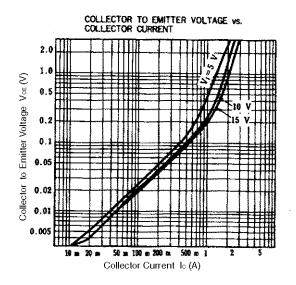


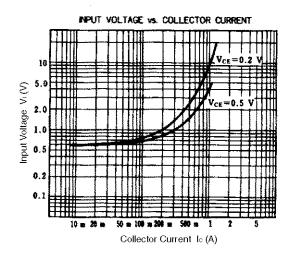
0.1 0.2

0.5 1 2

Input Voltage VI(V)

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