

# CEP6031LS2/CEB6031LS2

## ELECTRICAL CHARACTERISTICS ( $T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	30			V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=24\text{V}, \text{V}_{\text{GS}}=0\text{V}$			10	$\mu\text{A}$
Gate-Body Leakage	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 16\text{V}, \text{V}_{\text{DS}}=0\text{V}$			$\pm 100$	nA
<b>ON CHARACTERISTICS<sup>a</sup></b>						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1	1.6	3	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=26\text{A}$		8.8	12	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=21\text{A}$		12	17	$\text{m}\Omega$
On-State Drain Current	$\text{I}_{\text{D(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{V}_{\text{DS}}=10\text{V}$	60			A
Forward Transconductance	$\text{g}_{\text{FS}}$	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=26\text{A}$		32		S
<b>DYNAMIC CHARACTERISTICS<sup>b</sup></b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=15\text{V}, \text{V}_{\text{GS}}=0\text{V}$ $f=1.0\text{MHz}$		1920	2500	pF
Output Capacitance	$\text{C}_{\text{oss}}$			960	1250	pF
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$			300	400	pF
<b>SWITCHING CHARACTERISTICS<sup>b</sup></b>						
Turn-On Delay Time	$t_{\text{D(ON)}}$	$\text{V}_{\text{DD}}=15\text{V},$ $\text{I}_D=52\text{A},$ $\text{V}_{\text{GS}}=10\text{V},$ $\text{V}_{\text{GEN}}=24\Omega$		10	16	ns
Rise Time	$t_r$			190	250	ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			55	90	ns
Fall Time	$t_f$			130	200	ns
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=52\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$		50	60	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$			8		nC
Gate-Drain Charge	$\text{Q}_{\text{gd}}$			8		nC

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## BODY DIODE & SCHOTTKY DIODE RATINGS AND CHARACTERISTICS

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>DRAIN-SOURCE DIODE CHARACTERISTICS<sup>a</sup></b>						
Body Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0V, I_S = 26A$		0.93	1.3	V
Schottky Forward Voltage	$V_F$	$I_F=2A, T_C=25^\circ C$		0.55	0.55	V
Average Forward Rectified Current	$I_{F(AV)}$			2	2	A

### Notes

a. Pulse Test: Pulse Width  $\leq 300 \mu s$ , Duty Cycle  $\leq 2\%$ .

b. Guaranteed by design, not subject to production testing.

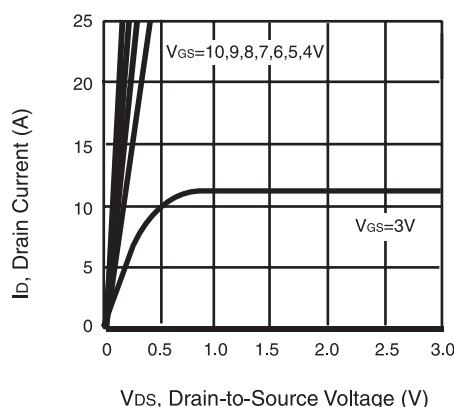


Figure 1. Output Characteristics

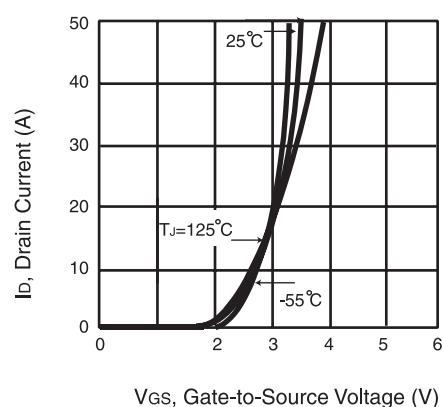


Figure 2. Transfer Characteristics

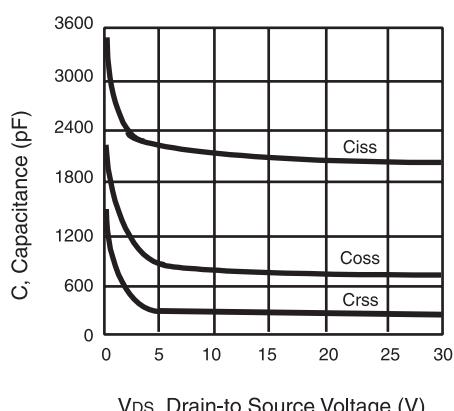


Figure 3. Capacitance

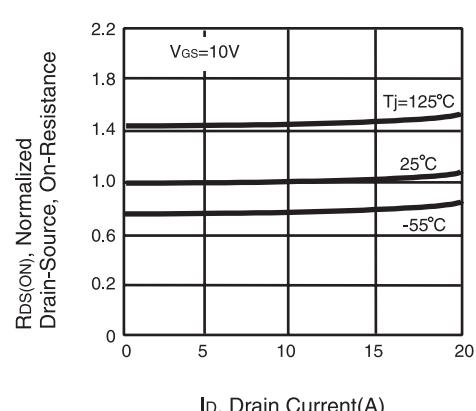
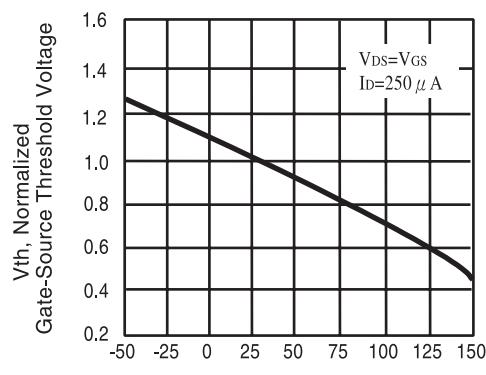


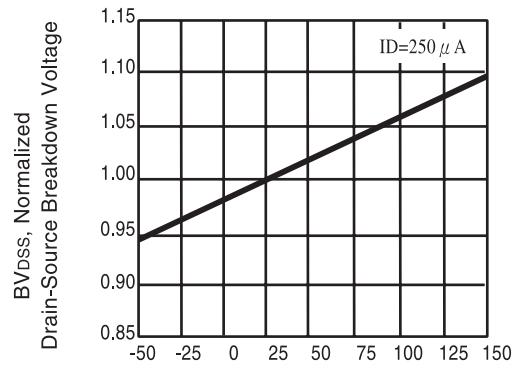
Figure 4. On-Resistance Variation with Drain Current and Temperature

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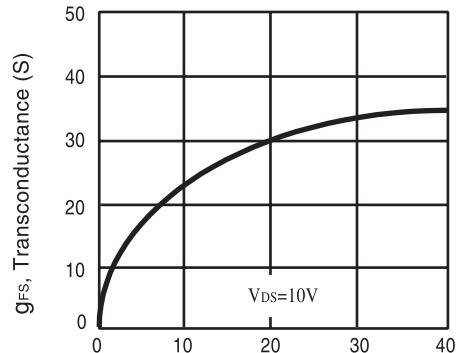
$T_j$ , Junction Temperature (°C)

**Figure 5. Gate Threshold Variation with Temperature**



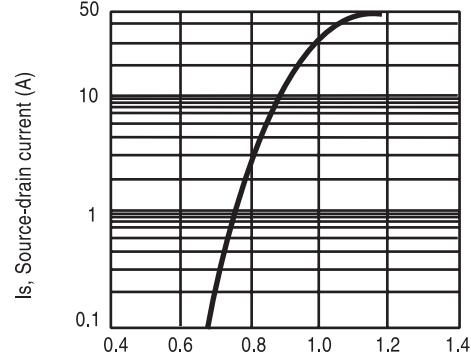
$T_j$ , Junction Temperature (°C)

**Figure 6. Breakdown Voltage Variation with Temperature**



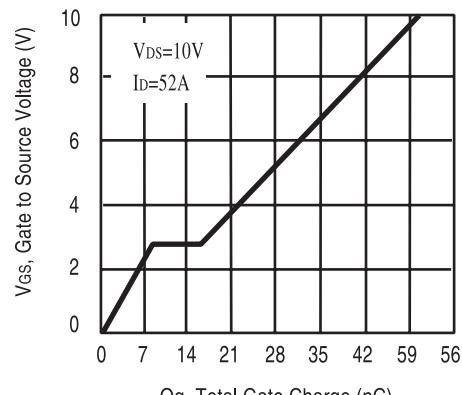
$I_{ds}$ , Drain-Source Current (A)

**Figure 7. Transconductance Variation with Drain Current**



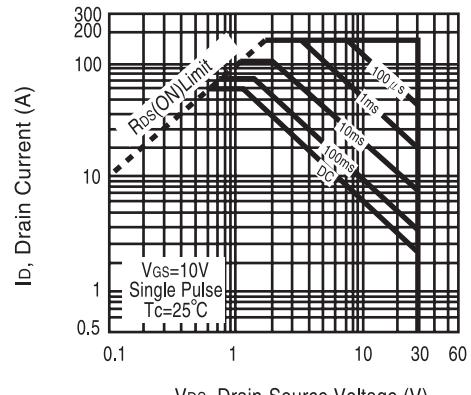
$V_{sd}$ , Body Diode Forward Voltage (V)

**Figure 8. Body Diode Forward Voltage Variation with Source Current**



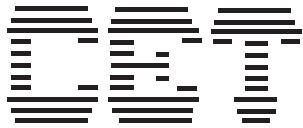
$Q_g$ , Total Gate Charge (nC)

**Figure 9. Gate Charge**



**Figure 10. Maximum Safe Operating Area**

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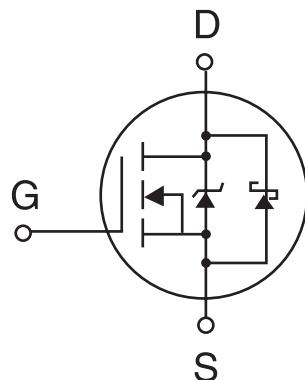
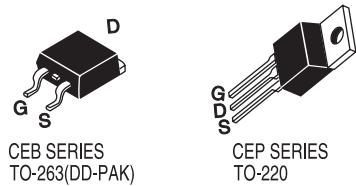


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## N-Channel Logic Level Enhancement Mode Field Effect Transistor

### FEATURES

- 30V , 60A ,  $R_{DS(ON)}=13.5\text{m}\Omega$  @  $V_{GS}=10\text{V}$ .  
 $R_{DS(ON)}=18.5\text{m}\Omega$  @  $V_{GS}=4.5\text{V}$ .
- Super high dense cell design for extremely low  $R_{DS(ON)}$ .
- High power and current handling capability.
- TO-220 & TO-263 package.



### ABSOLUTE MAXIMUM RATINGS ( $T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous -Pulsed	$I_D$	60	A
	$I_{DM}$	180	A
Drain-Source Diode Forward Current	$I_S$	60	A
Maximum Power Dissipation Derate above $25^\circ\text{C}$	$P_D$	50	W
		0.4	W/ $^\circ\text{C}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-65 to 175	$^\circ\text{C}$

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### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.5	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$

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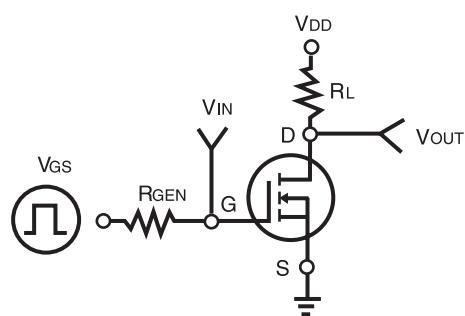


Figure 11. Switching Test Circuit

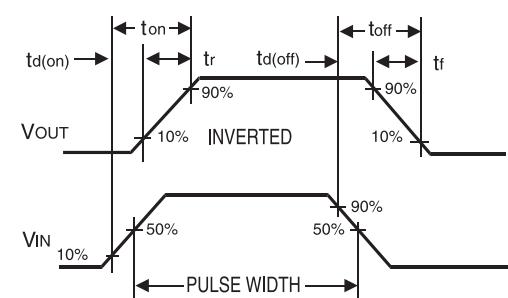


Figure 12. Switching Waveforms

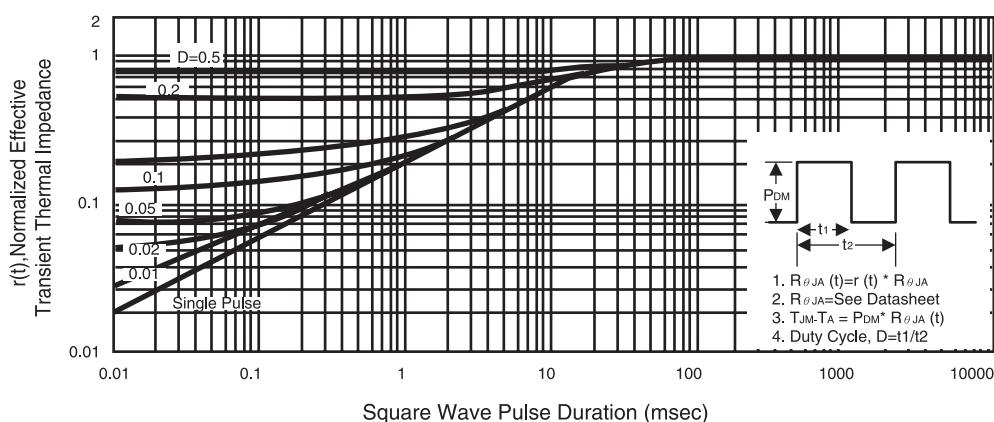


Figure 13. Normalized Thermal Transient Impedance Curve