

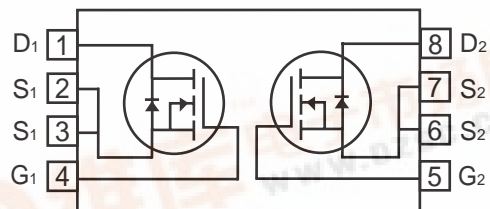
CEG9926

Nov. 2002

Dual N-Channel Enhancement Mode Field Effect Transistor

FEATURES

- 20V , 4.5A , $R_{DS(ON)}=30m\Omega$ @ $V_{GS}=4.5V$.
 $R_{DS(ON)}=40m\Omega$ @ $V_{GS}=2.5V$.
- Super high dense cell design for extremely low $R_{DS(ON)}$.
- High power and current handing capability.
- TSSOP-8 for Surface Mount Package.



TSSOP-8

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ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	20	V
Gate-Source Voltage	VGS	± 8	V
Drain Current-Continuous ^a -Pulsed ^b	ID	± 4.5	A
	IDM	± 25	A
Drain-Source Diode Forward Current ^a	IS	1.7	A
Maximum Power Dissipation ^a	PD	1.0	W
Operating Junction and Storage Temperature Range	TJ, TSTG	-55 to 150	$^{\circ}C$

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^a	$R_{\theta JA}$	125	$^{\circ}C/W$
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ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250μA	20			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V			1	μA
Gate-Body Leakage	I _{GSS}	V _{GS} =±8V, V _{DS} =0V			±100	nA
ON CHARACTERISTICS^b						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.5		1.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4.5A		24	30	mΩ
		V _{GS} =4.0V, I _D =5A		23		mΩ
		V _{GS} =2.5V, I _D =3.5A		32	40	mΩ
On-State Drain Current	I _{D(ON)}	V _{DS} =5V, V _{GS} =4.5V	10			A
Forward Transconductance	g _{FS}	V _{DS} =10V, I _D =4.5A		10		S
DYNAMIC CHARACTERISTICS^c						
Input Capacitance	C _{ISS}	V _{DS} = 8V, V _{GS} = 0V f = 1.0MHz		500		pF
Output Capacitance	C _{OSS}			300		pF
Reverse Transfer Capacitance	C _{RSS}			140		pF
SWITCHING CHARACTERISTICS^c						
Turn-On Delay Time	t _{D(ON)}	V _{DD} = 10V, I _D = 1A, V _{GEN} = 4.5V, R _{GEN} = 6Ω		20	40	ns
Rise Time	t _r			18	40	ns
Turn-Off Delay Time	t _{D(OFF)}			60	108	ns
Fall time	t _f			28	56	ns
Total Gate Charge	Q _g	V _{DS} = 10V, I _D = 4.5A, V _{GS} = 4.5V		10	15	nC
Gate-Source Charge	Q _{gs}			2.3		nC
Gate-Drain Charge	Q _{gd}			2.9		nC

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ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS^b						
Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{V}, I_S = 1.7\text{A}$		0.8	1.2	V

Notes

- a. Surface Mounted on FR4 Board, $t \leq 10\text{sec}$.
- b. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2\%$.
- c. Guaranteed by design, not subject to production testing.

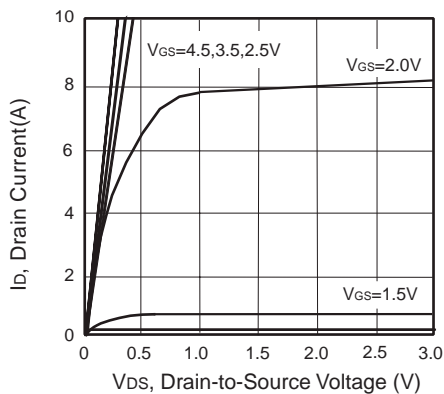


Figure 1. Output Characteristics

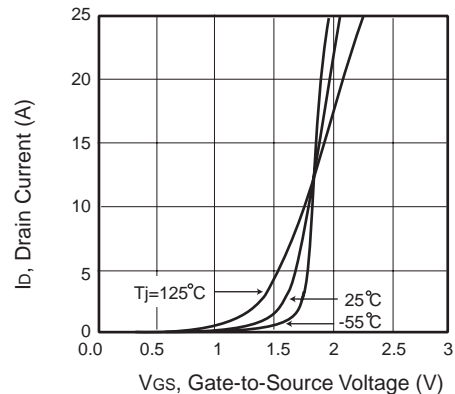


Figure 2. Transfer Characteristics

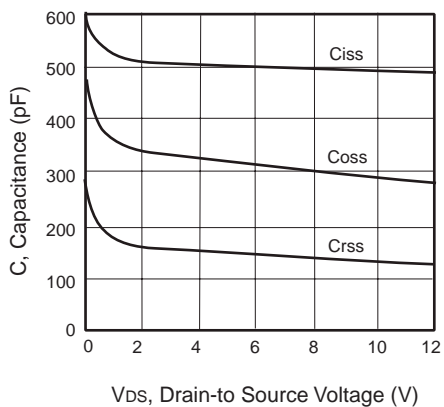


Figure 3. Capacitance

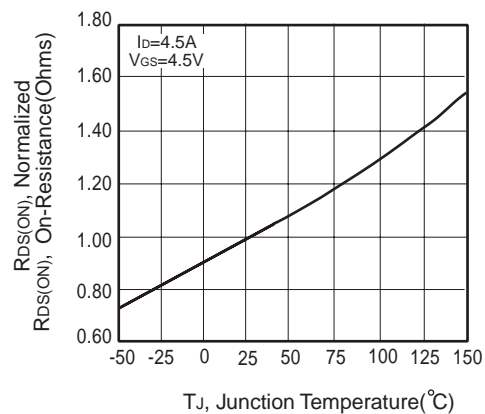


Figure 4. On-Resistance Variation with Temperature

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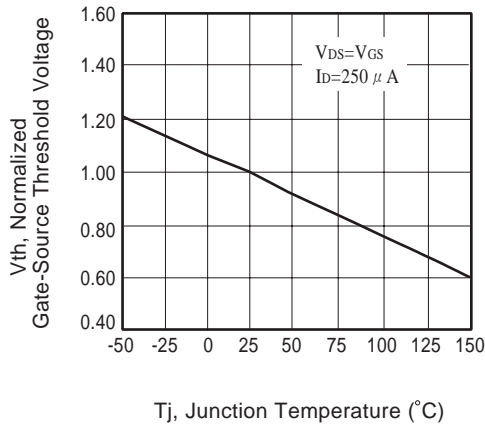


Figure 5. Gate Threshold Variation with Temperature

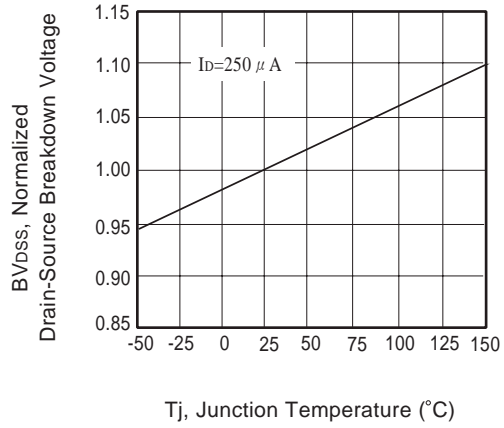


Figure 6. Breakdown Voltage Variation with Temperature

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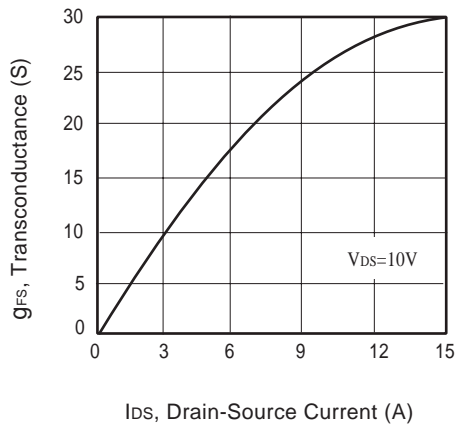


Figure 7. Transconductance Variation with Drain Current

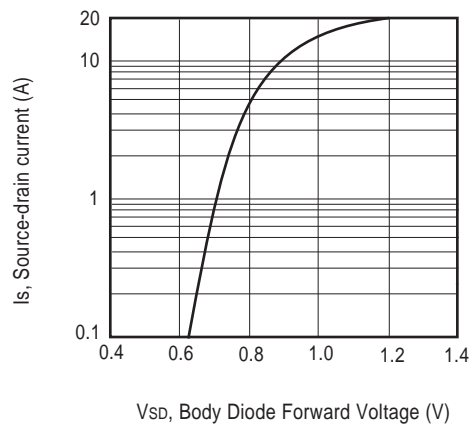


Figure 8. Body Diode Forward Voltage Variation with Source Current

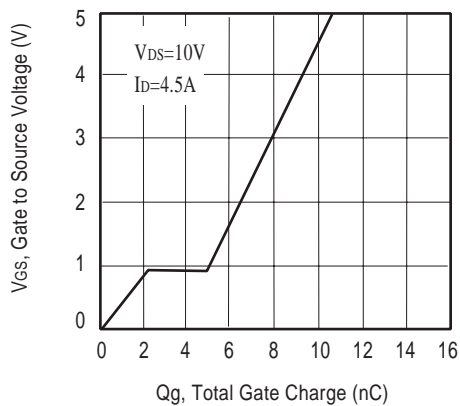


Figure 9. Gate Charge

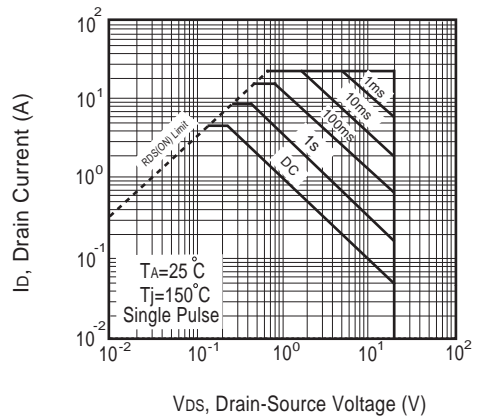


Figure 10. Maximum Safe Operating Area

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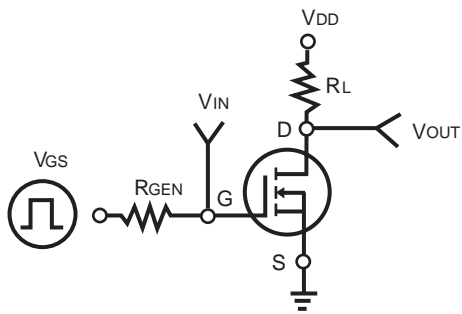


Figure 11. Switching Test Circuit

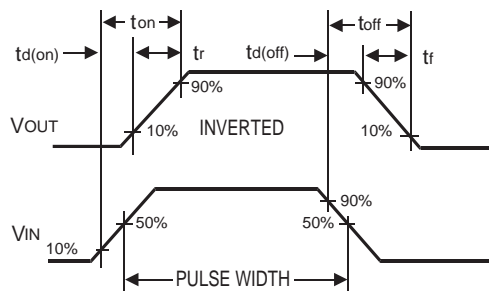


Figure 12. Switching Waveforms

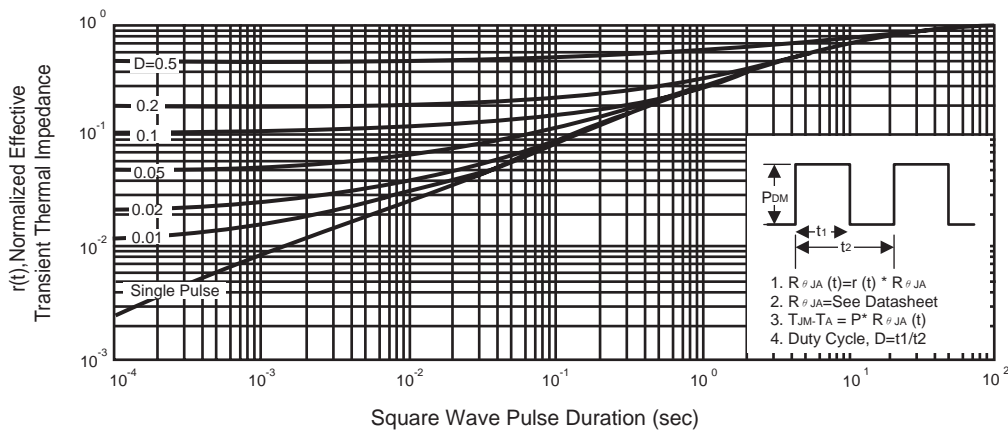


Figure 13. Normalized Thermal Transient Impedance Curve