Advanced Product Information May 1996 (1 of 2)

Features

- ☐ High Gain
- ☐ +36 dBm Power Output
- □ Proprietary Power FET Process
- □ >45% Linear Power Added Efficiency
- ☐ +33 dBm with 30 dBc Third Order Products

Applications

- □ RF ID/POS Base Stations
- ☐ Wireless Local Loop

Description

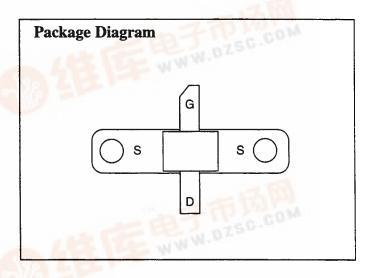
The CFH2162-P5 is a high-gain, linear FET intended for driver amplifier applications in high-power systems, and output stage usage in medium power applications at power levels up to +36 dBm. The device is easily matched and pro-

Specifications (TA = 25° C) The following specifications are guaranteed at room temperature in Celeritek test fixture at 2.45 GHz.

| Parameters | Conditions | Min | Тур | Max | Units |
|---------------------------|-----------------------|------|------|-----|-------|
| $V_d = 10V, I_d$ | = 1100 mA (Quiescent) | | | | |
| P-1dB | | 35.5 | 36.5 | _ | dBm |
| G _{-1 dB} | | 11.0 | 12.0 | 1 | dB |
| 3rd Order Products (1) | E3 | 30 | 35 | CO. | dBc |
| Efficiency | @ P1dB | | 43 | | % |
| $V_d = 8V, I_d =$ | = 1300 mA (Quiescent) | | | | |
| P-1dB | | _ | 35.5 | | dBm |
| G _{-1 dB} | | | 10.0 | | dB |

| Parameters | Conditions | Min | Тур | Max | Units |
|---------------------|--------------------------|-----|------|-----|-------|
| $g_{\mathbf{m}}$ | Vds = 2.0V, Vgs = 0V | _ | 1700 | | mS |
| Idss | Vds = 2.0V, Vgs = 0V | _ | 2.8 | - | A |
| $\overline{v_p}$ | Vds = 3.0V, Ids = 65 mA | _ | -1.8 | | Volts |
| BV _{GD} | Igd = 6.5 mA | 20 | 24 | -=- | Volts |
| Θ _{JL} (2) | @150°C TCH | | 8 | _ | °C/W |

2.3 to 2.5 GHz +36 dBm Power GaAs FET



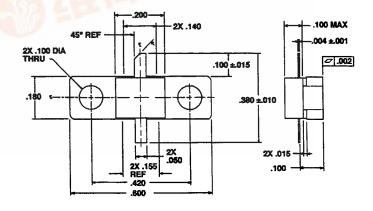
vides excellent linearity at 4 Watts. Manufactured in Celeritek's proprietary power FET process, this device is assembled in a power flange package.

Absolute Maximum Ratings

| Parameter | Symbol | Rating 15V (3) | | |
|------------------------|-----------------|-----------------|--|--|
| Drain-Source Voltage | V _{DS} | | | |
| Gate-Source Voltage | VGS | -5V | | |
| Drain Current | IDS | Idss | | |
| Continuous Dissipation | P_{T} | 10W | | |
| Channel Temperature | T_{CH} | 175°C | | |
| Storage Temperature | TSTG | -65°C to +175°C | | |

- 1. Sum to two tones with 1 MHz spacing = 33 dBm.
- 2. See thermal considerations information.
- 3. Maximum potential difference across the device (Vd + Vg) cannot exceed 18V.

Power Flange Package Physical Dimensions





Ordering Information

The CFH2162-P5 power stage is available in a SOIC-8 surface mount package. Devices are available in tape and reel. Ordering part numbers are listed.

Part Number for Ordering

Function

Package

CFH2162-P5

2.3 - 2.5 GMHz Power Stage

Power flange package

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