

# 300mA Low ESR CMOS LDO WITH ENABLE

### **GENERAL DESCRIPTION**

The CM2838 family is a positive voltage linear regulator developed utilizing CMOS technology featured low quiescent current (30 $\mu$ A typ.), low dropout voltage, and high output voltage accuracy, making them ideal for battery applications. EN input connected to CMOS has low bias current. The space-saving SOT-23-5 package is attractive for "Pocket" and "Hand Held" applications.

These rugged devices have both Thermal Shutdown, and Current limit to prevent device failure under the "Worst" of operating conditions.

In application requiring a low noise, regulated supply, place a 1000pF capacitor between Bypass and Ground.

The CM2838 is stable with a Low ESR output capacitance of  $1.0\mu F$  or greater.

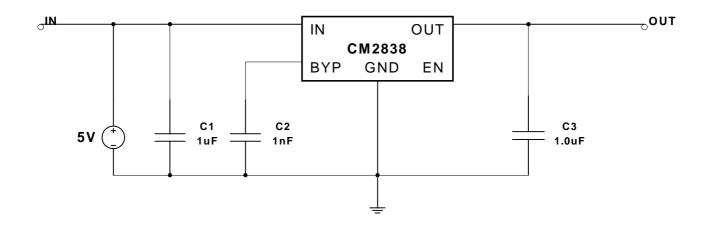
### **FEATURES**

- Very Low Dropout Voltage
- ♦ Low Current Consumption: Typ. 30μA, Max. 35μA
- Output Voltage: 1.8V, 2.5V, 2.8V, 3.0V, and 3.3V
- ♦ High Accuracy Output Voltage: +/- 1.5%
- Guaranteed 300mA Output
- ◆ Input Range up to 7.0V
- ◆ Thermal Shutdown
- Current Limiting
- ◆ Stability with Low ESR Capacitors
- ◆ Compact Package: SOT-23-5
- ◆ Factory Pre-set Output Voltages
- ◆ Low Temperature Coefficient

### **APPLICATIONS**

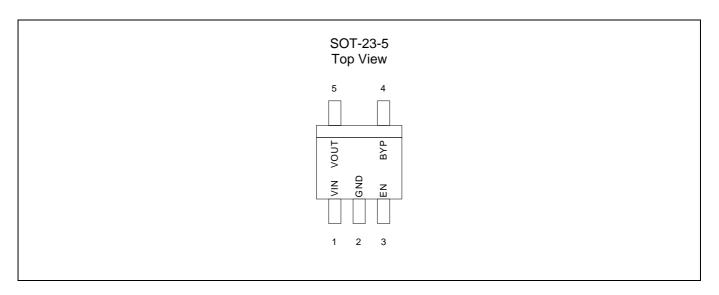
- Battery-powered devices
- Personal communication devices
- ♦ Home electric/electronic appliances
- ♦ PC peripherals

### TYPICAL APPLICATIONS

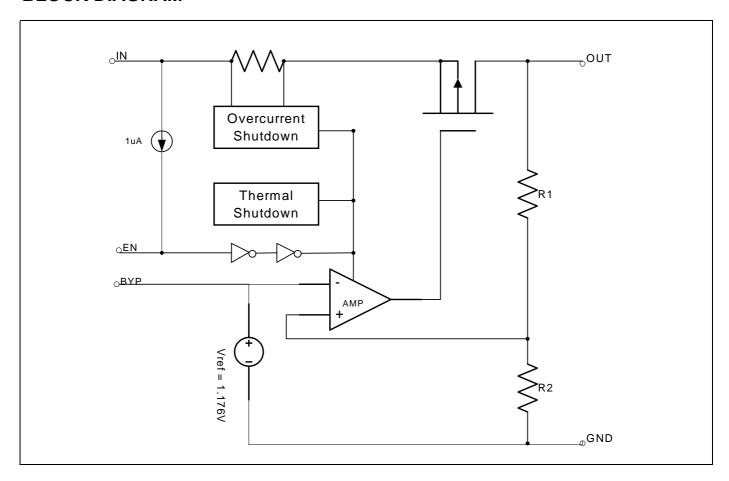




### **PIN CONFIGURATION**



### **BLOCK DIAGRAM**





### ORDERING INFORMATION

Part Number	Output Voltage	Temperature Range	Package
CM2838DIM25	1.8V	-40 ~ +85	SOT-23-5
CM2838KIM25	2.5V	-40 ~ +85	SOT-23-5
CM2838NIM25	2.8V	-40 ~ +85	SOT-23-5
CM2838PIM25	3.0V	-40 ~ +85	SOT-23-5
CM2838SIM25	3.3V	-40 ~ +85	SOT-23-5
CM2838GDIM25	1.8V	-40 ~ +85	SOT-23-5
CM2838GKIM25	2.5V	-40 ~ +85	SOT-23-5
CM2838GNIM25	2.8V	-40 ~ +85	SOT-23-5
CM2838GPIM25	3.0V	-40 ~ +85	SOT-23-5
CM2838GSIM25	3.3V	-40 ~ +85	SOT-23-5

Note: For other pre-set output voltage requirements, please contact Champion Sales office.

### **ABSOLUTE MAXIMUM RATINGS**

### **OPERATING RATINGS**

Input Voltage	+7V	Ambient Temperature Range (T <sub>A</sub> )4	0 to +85
Output Current	$P_D$ / ( $V_{IN}$ - $Vo)$ mA	Junction Temperature Range	to +125
Output Voltage GND-	0.3V to V <sub>IN</sub> +0.3V		
CCD Classification	В		

### THERMAL INFORMATION

Parameter		Maximum	Unit
Thermal Resistance ( jc)	SOT-23-5	160	/W
Internal Power Dissipation ( $P_D$ ) ( $T = 100$ )	SOT-23-5	250	mW
Maximum Junction Temperature		150	
Maximum Lead Temperature (10 Sec)		300	

Caution: Stress above the listed absolute rating may cause permanent damage to the device.



# **ELECTRICAL CHARACTERISTICS**

 $T_A = +25$ ; unless otherwise noted

		Test Conditions		CM2838				
Parameter	Symbol			Min.	Тур.	Max.	Unit	
Input Voltage	$V_{IN}$			Note 1		7	V	
Output Voltage Accuracy	V <sub>OUT</sub>	I <sub>O</sub> = 1mA to 300mA		-1.5		1.5	%	
Dropout Voltage	V <sub>DROPOUT</sub>	1.2V <v<sub>O(NOM) ≤ 2.0V</v<sub>		$V < V_{O(NOM)} \le 2.0V$			1300	
		$I_{O} = 300 \text{mA},$ $V_{OUT} = V_{O(NOM)} - 1.5\%,$ 2.	2.0V	$V < V_{O(NOM)} \le 2.5V$			800	mV
			2	$2.5V < V_{O(NOM)}$			300	<u> </u>
Output Current	Io	V <sub>OUT</sub> >	> 1.2V	/	300			mA
Current Limit	I <sub>LIM</sub>	V <sub>OUT</sub> >	> 1.2V	/	300	450		mA
Quiescent Current	IQ	I <sub>O</sub> = 0	0mA			30	35	μΑ
Ground Pin Current	$I_{GND}$	$I_0 = 1 \text{mA}$	to 300	)mA		30	50	μΑ
Line Regulation	REG <sub>LINE</sub>	I <sub>OUT</sub> =5mA, V <sub>IN</sub> =V	/ <sub>OUT</sub> +1	1 to V <sub>OUT</sub> +2	-0.1	0.02	0.1	%
Load Regulation	REG <sub>LOAD</sub>	I <sub>O</sub> =1mA to 300mA			0.2	1	%	
Over Temperature Shutdown	OTS					150		
Over Temperature Hysteresis	OTH					30		
V <sub>OUT</sub> Temperature Coefficient	TC					40		ppm/
	PSRR	f=1kHz			60		]	
Power Supply Rejection		C <sub>0</sub> =2.2µF ceramic		f=10kHz		50		dB
			f=100kHz		40			
Power Supply Rejection	PSRR	$I_0 = 100 \text{mA}$		f=1kHz		75		
		C <sub>0</sub> =2.2µF ceramic		f=10kHz		55		dB
		C <sub>BYP</sub> =0.01µF		f=100kHz		30		
Output Valtage Noice	eN	f=10Hz to 100kHz	<u>.</u>	C <sub>O</sub> =2.2µF		30		μ Vrms
Output Voltage Noise		$I_O = 10$ mA, $C_{BYP} = 0$ $\mu$	ιF	C <sub>O</sub> =100µF		20		
Output Valtage Naise	eN	f=10Hz to 100kHz	<u>.</u>	C <sub>O</sub> =2.2µF		30		\/rma
Output Voltage Noise		$I_{O} = 10 \text{mA}, C_{BYP} = 0.07$	1μF	C <sub>O</sub> =100µF		20		μ Vrms
Shutdown Supply Current	I <sub>SD</sub>	$V_{IN}$ =5.0V, $V_{OUT}$ =0V, $V_{EN}$ < $V_{EL}$			2.0	3.0	μΑ	
EN Input Bias Current	I <sub>EH</sub>	$V_{EN}=V_{IN}$ , $V_{IN}=2.6V$ to $7V$				0.1	μA	
EN IIIPUL DIAS CUITEITL	I <sub>EL</sub>	V <sub>EN</sub> =0, V <sub>IN</sub> =2.6V to 7V			2.0	3.0	μA	
EN Input Threshold	$V_{EH}$	V <sub>IN</sub> =2.6V to 7V		2		V <sub>IN</sub>	V	
National Management of the Control o	$V_{EL}$	$V_{IN}$ =2.6V to 7V		0		0.4	V	

Note 1.  $V_{IN(MIN)} = V_{OUT} + V_{DROPOUT}$ 

#### **DETAILED DESCRIPTION**

The CM2838 family of CMOS regulators contain a PMOS pass transistor, voltage reference, error amplifier, over-current protection, thermal shutdown.

The P-channel pass transistor receives data from the error amplifier, over-current protection, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. Over-current and Thermal shutdown circuits become active when the junction temperature exceeds 150 , or the current exceeds 300mA. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops below 120 .

The CM2838 switches from voltage mode to current mode when the load exceeds the rated output current. This prevents over-stress.

#### **ENABLE**

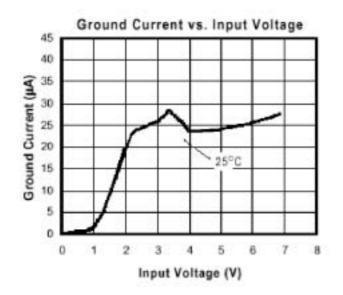
The Enable pin normally floats high. When actively, pulled low, the PMOS pass transistor shut off, and all internal circuits are powered down. In this state, the quiescent current is less than  $2\mu$ A. This pin behaves much like an electronic switch.

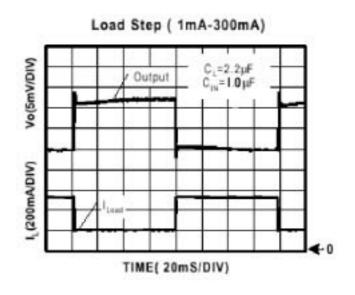
#### **EXTERNAL CAPACITOR**

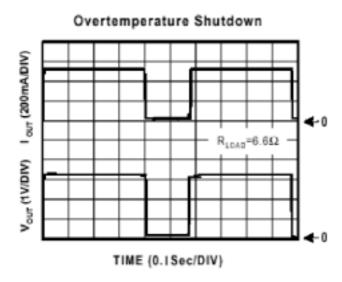
The CM2838 is stable with a Low ESR output capacitor to ground of  $1.0\mu\text{F}$  or greater. It can keep stable even with higher ESR capacitors. A second capacitor is recommended between the input and ground to stabilize VIN. The input capacitor should be larger than  $0.1\mu\text{F}$  to have a beneficial effect. All capacitors should be placed in close proximity to the pins. A "quiet" ground termination is desirable.

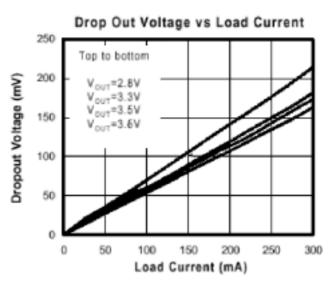


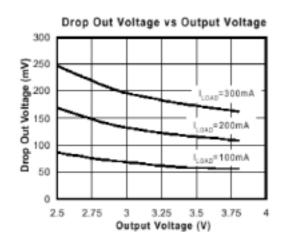
#### TYPICAL CHARACTERISTICS

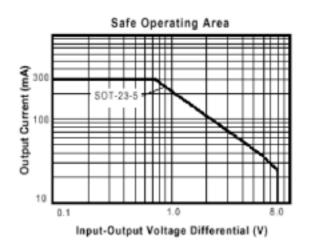




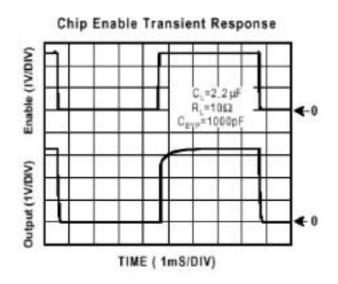


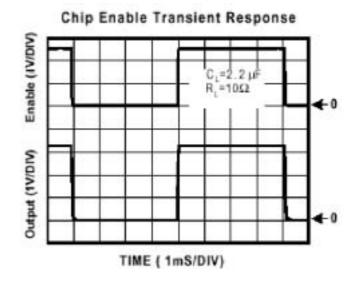


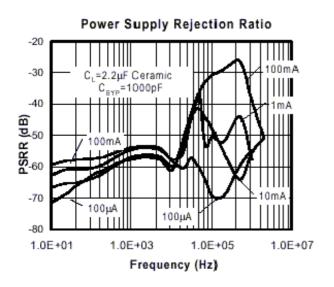


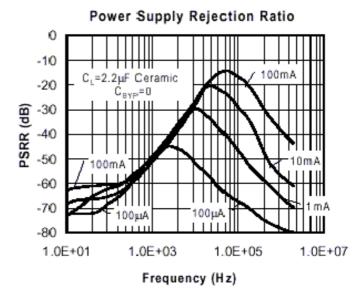


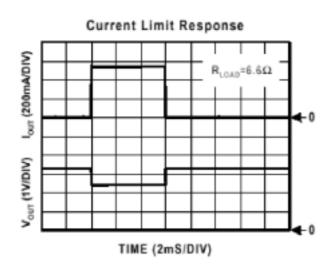






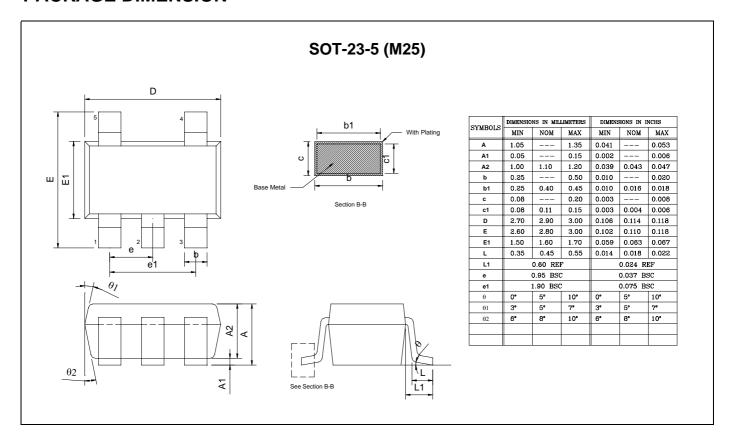








### **PACKAGE DIMENSION**





### NUMBERING SCHEME

Ordering Number: CM2838XYZ (note1)
Ordering Number: CM2838GXYZ (note2)

#### note1:

CM2838: 300mA CMOS LDO with enable  $\underline{X}$ : Suffix for voltage output (note 3)  $\underline{Y}$ : Suffix for Temperature Range (note 4)  $\underline{Z}$ : Suffix for Package Type (note 5)

#### note2:

CM2838: 300mA CMOS LDO with enable

G: Suffix for Pb Free Product
X: Suffix for voltage output (note 3)
Y: Suffix for Temperature Range (note 4)
Z: Suffix for Package Type (note 5)

note 3: see CMOS LDO Voltage Suffix Table CM2838 will provide options of D(1.8V), K(2.5V), N(2.8V), P(3.0V), S(3.3V)

#### note 4:

Y= I: -40 ~+85 (only I grade support for all CMOS LDOs)

#### note 5:

Z is single alphabet with or without digits

M25: SOT-25 (TR only)

#### **CMOS LDO Voltage Suffix Table**

Output Voltage	Suffix	Output Voltage	Suffix
1.5V	Α	3.0V	Р
1.6V	В	3.1V	Q
1.7V	С	3.2V	R
1.8V	D	3.3V	S
1.9V	E	3.4V	Т
2.0V	F	3.5V	U
2.1V	G	3.6V	V
2.2V	Н	3.7V	W
2.3V	I	3.8V	X
2.4V	J	3.9V	Υ
2.5V	K	4.0V	Z
2.6V	L		
2.7V	M		
2.8V	N		
2.9V	0		

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### **IMPORTANT NOTICE**

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