捷多邦,专业PCB打样工厂,24小时

SOT-23 Formed SMD Package

CMBT 6520

HIGH-VOLTAGE TRANSISTOR

P-N-P transistor

Marking CMBT6520 = 2Z PACKAGE OUTLINE DETAILS ALL DIMENSIONS IN mm

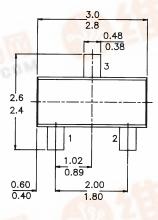


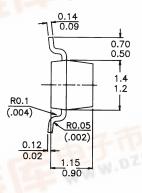
1 = BASE

2 = EMITTER

3 = COLLECTOR







ABSOLUTE MAXIMUM RATINGS

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Collector-base voltage (open emitter)	$-V_{CBO}$	max.	350	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	350	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5	V
Collector current (d.c.)	$-I_C$	max.	500	mA
Total power dissipation at $T_{amb} = 25$ °C	P_{tot}	max	225	mW
D.C. current gain				
$-I_C = 10 \text{ mA: } -V_{CF} = 10 \text{ V}$	hee	min.	30	

RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified)

Limiting values				
Collector-base voltage (open emitter)	$-V_{CBO}$	max.	350	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	350	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5	V
Collector current (d.c.)	$-I_C$	max.	<i>500</i>	mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	P_{tot}	max	225	mW
Storage temperature	T_{stg}	−55 to +150		${}^{\circ}C$
Junction temperature	Tj	max.	<i>150</i>	${}^{\circ}C$



CMBT 6520

20

30

30 200

20

200

15

2 V

20 MHz200 MHz

min.

min.

min.

max.

min. max.

min.

max.

min.

max.

from junction to ambient $R_{th j-a}$ 556 °C/mW **CHARACTERISTICS** (at $T_A = 25$ °C unless otherwise specified) Collector-emitter breakdown voltage $-I_C = 1 \text{ mA}$ -V_{(BR)CEO} min. 350 V Collector-base breakdown voltage $-I_C = 100 \,\mu A$ -V_(BR)CBO min. 350 V Emitter-base breakdown voltage $-I_E = 10 \,\mu A$ -V_{(BR)EBO} min. 5 VCollector cut-off current $-V_{CB} = 250 V$ $-I_{CBO}$ 50 nA max. Emitter cut-off current $-V_{EB} = 4 V$ -I_{EBO} 50 nA max. Output capacitance at f = 1 MHz рF $-V_{CB} = 20 V$ C_c max. Input capacitance at f = 1 MHz $-V_{EB} = 0.5 V$ C_e max. 100 pF Saturation voltages 0.3 V $-I_C = 10 \text{ mA}; -I_B = 1 \text{ mA}$ -V_{CEsat} max. -V_{BEsat} max. 0.75 V $-I_C = 20 \text{ mA}$; $-I_B = 2 \text{ mA}$ -V_{CEsat} 0.35 V max. -V_{BEsat} max. 0.85 V $-I_C = 30 \text{ mA}; -I_B = 3 \text{ mA}$ -V_{CEsat} 0.5 V max. 0.9 V-V_{BEsat} max. $-I_C = 50 \text{ mA}; -I_B = 5 \text{ mA}$ 1.0 -V_{CEsat} max. D.C. current gain

 h_{FE}

 h_{FE}

 h_{FE}

 h_{FE}

 h_{FE}

 f_T

 $V_{BE(on)}$

THERMAL CHARACTERISTICS $T_j = P(R_{th j-t} + R_{th s-a}) + T_{amb}$

 $-I_C = 1 \text{ mA; } -V_{CE} = 10 \text{ V}$

 $-I_C = 10 \text{ mA; } -V_{CE} = 10 \text{ V}$

 $-I_C = 30 \text{ mA}; -V_{CE} = 10 \text{ V}$

 $-I_C = 50 \text{ mA}; -V_{CE} = 10 \text{ V}$

 $-I_C = 100 \text{ mA; } -V_{CE} = 10 \text{ V}$

 $-V_{CE} = 20 \ V; \ -I_{C} = 10 \ mA; \ f = 20 \ MHz$

 $I_C = 100 \text{ mA}; V_{CE} = 10 \text{ V}$

Base emitter voltage

Transition frequency

Thermal resistance

Customer Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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