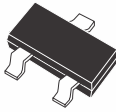


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**CMPF4391
CMPF4392
CMPF4393**

N-CHANNEL JFET



SOT-23 CASE

Central
Semiconductor Corp.

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMPF4391 series types are N-Channel Silicon Field Effect Transistors manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for switching applications.

Marking Codes are 6J, 6K, and 6G Respectively.

MAXIMUM RATINGS ($T_A=25^{\circ}C$)

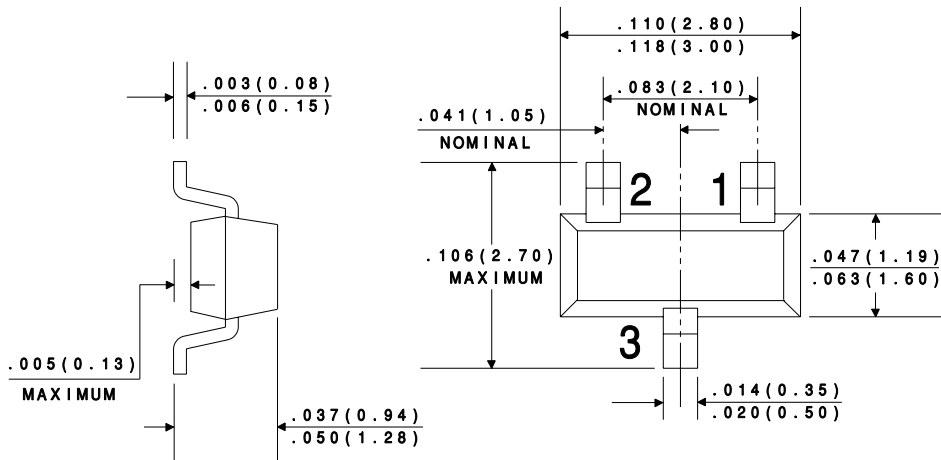
	SYMBOL		UNITS
Drain-Gate Voltage	V_{GD}	40	V
Gate-Source Voltage	V_{GS}	40	V
Drain-Source Voltage	V_{DS}	40	V
Gate Current	I_G	50	mA
Power Dissipation	P_D	350	mW
Operating and Storage Junction Temperature	T_J, T_{stg}	-65 to +150	$^{\circ}C$
Thermal Resistance	θ_{JA}	357	$^{\circ}C/W$

ELECTRICAL CHARACTERISTICS ($T_A=25^{\circ}C$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	CMPF4391		CMPF4392		CMPF4393		UNITS
		MIN	MAX	MIN	MAX	MIN	MAX	
I_{GSS}	$V_{GS}=20V$		0.1		0.1	0.1		nA
I_{GSS}	$V_{GS}=20V, T_A=100^{\circ}C$		0.2		0.2	0.2		μA
I_{DSS}	$V_{DS}=20V$	50	150	25	75	5.0	30	mA
$I_{D(OFF)}$	$V_{DS}=20V, V_{GS}=12V$		0.1		-		-	nA
$I_{D(OFF)}$	$V_{DS}=20V, V_{GS}=7.0V$		-		0.1		-	nA
$I_{D(OFF)}$	$V_{DS}=20V, V_{GS}=5.0V$		-		-		0.1	nA
$I_{D(OFF)}$	$V_{DS}=20V, V_{GS}=12V, T_A=100^{\circ}C$		0.2		-		-	μA
$I_{D(OFF)}$	$V_{DS}=20V, V_{GS}=7.0V, T_A=100^{\circ}C$		-		0.2		-	μA
$I_{D(OFF)}$	$V_{DS}=20V, V_{GS}=5.0V, T_A=100^{\circ}C$		-		-		0.2	μA
BV_{GSS}	$I_G=1.0\mu A$	40		40		40		V
$V_{GS(OFF)}$	$V_{DS}=20V, I_D=1.0nA$	4.0	10	2.0	5.0	0.5	3.0	V
$V_{GS(f)}$	$I_G=1.0mA$		1.0		1.0		1.0	V
$V_{DS(ON)}$	$I_D=12mA$		0.4		-		-	V
$V_{DS(ON)}$	$I_D=6.0mA$		-		0.4		-	V
$V_{DS(ON)}$	$I_D=3.0mA$		-		-		0.4	V

SYMBOL	TEST CONDITIONS	CMPF4391		CMPF4392		CMPF4393		UNITS
		MIN	MAX	MIN	MAX	MIN	MAX	
$r_{DS(ON)}$	$I_D=1.0\text{mA}, V_{GS}=0$		30		60		100	Ω
$r_{ds(ON)}$	$V_{GS}=0, I_D=0, f=1.0\text{kHz}$		30		60		100	Ω
C_{iss}	$V_{DS}=20\text{V}, V_{GS}=0, f=1.0\text{MHz}$		14		14		14	pF
C_{rss}	$V_{GS}=12\text{V}, V_{DS}=0, f=1.0\text{MHz}$		3.5		-		-	pF
C_{rss}	$V_{GS}=7.0\text{V}, V_{DS}=0, f=1.0\text{MHz}$		-		3.5		-	pF
C_{rss}	$V_{GS}=5.0\text{V}, V_{DS}=0, f=1.0\text{MHz}$		-		-		3.5	pF
t_{ON}	$I_D(ON)=12\text{mA}$		15		-		-	ns
t_{ON}	$I_D(ON)=6.0\text{mA}$		-		15		-	ns
t_{ON}	$I_D(ON)=3.0\text{mA}$		-		-		15	ns
t_{OFF}	$V_{GS(OFF)}=12\text{V}$		20		-		-	ns
t_{OFF}	$V_{GS(OFF)}=7.0\text{V}$		-		35		-	ns
t_{OFF}	$V_{GS(OFF)}=5.0\text{V}$		-		-		50	ns

All dimensions in inches (mm).



LEAD CODE:

- 1) DRAIN
- 2) SOURCE
- 3) GATE