Ordering number : ENN6998

N-Channel Silicon MOSFET

CPH3408

Ultrahigh-Speed Switching Applications

Features

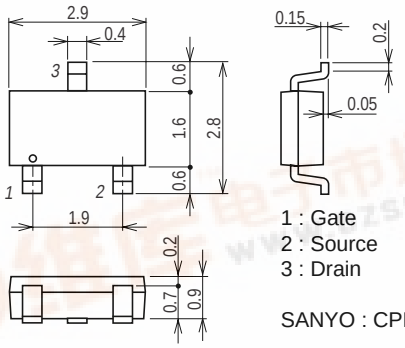
- Low ON-state resistance.
- Ultrahigh-speed switching.
- 4V drive.

Package Dimensions

unit : mm

2152A

[CPH3408]



1 : Gate
2 : Source
3 : Drain

SANYO : CPH3

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		30	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		5	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	20	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (900mm²×0.8mm)	1.2	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0	30			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=30V, V_{GS}=0			1	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±16V, V_{DS}=0			±10	μA
Cutoff Voltage	$V_{GS(off)}$	V_{DS}=10V, I_D=1mA	1.0		2.4	V
Forward Transfer Admittance	|yfs|	V_{DS}=10V, I_D=3A	4.5	6.5		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	I_D=3A, V_{GS}=10V		33	43	mΩ
	$R_{DS(on)2}$	I_D=1A, V_{GS}=4V		48	68	mΩ

Marking : KH

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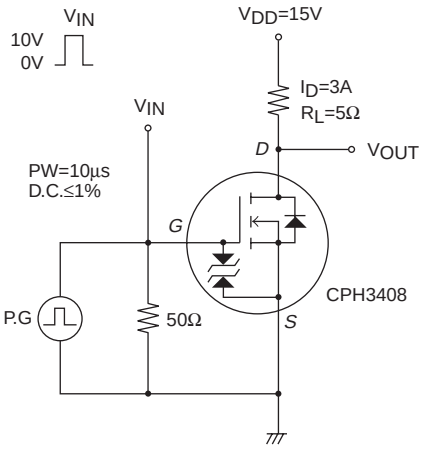
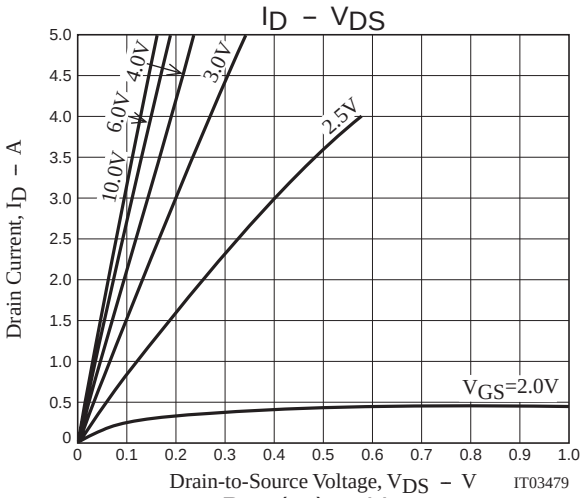
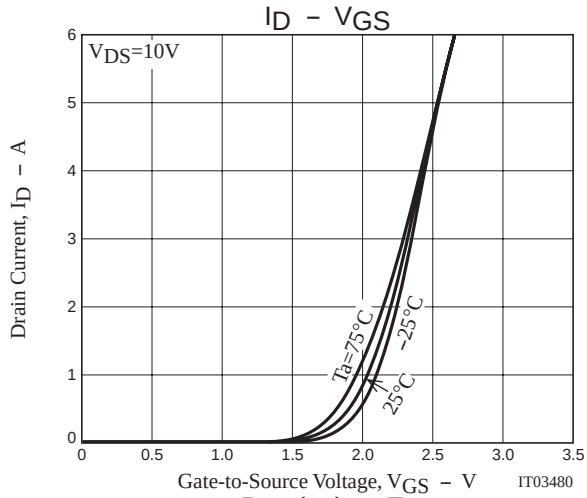
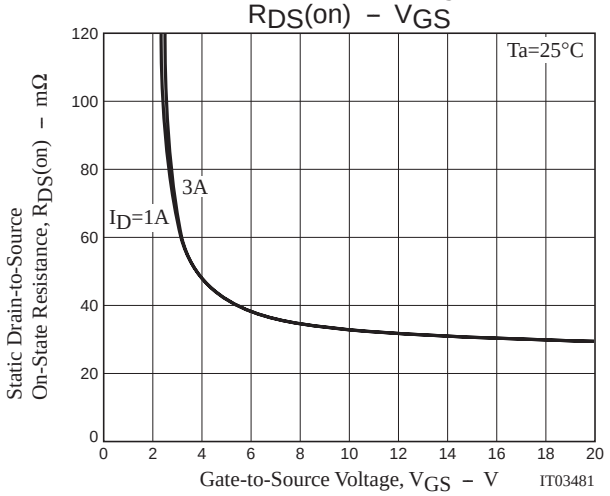
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V_{DS}=10V, f=1MHz		480		pF
Output Capacitance	Coss	V_{DS}=10V, f=1MHz		130		pF
Reverse Transfer Capacitance	Crss	V_{DS}=10V, f=1MHz		70		pF
Turn-ON Delay Time	t_{d}(on)	See specified Test Circuit		9		ns
Rise Time	t_{r}	See specified Test Circuit		64		ns
Turn-OFF Delay Time	t_{d}(off)	See specified Test Circuit		36		ns
Fall Time	t_{f}	See specified Test Circuit		45		ns
Total Gate Charge	Qg	V_{DS}=10V, V_{GS}=10V, I_{D}=5A		11		nC
Gate-to-Source Charge	Qgs	V_{DS}=10V, V_{GS}=10V, I_{D}=5A		1.0		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=10V, V_{GS}=10V, I_{D}=5A		2.0		nC
Diode Forward Voltage	V_{SD}	I_{S}=5A, V_{GS}=0		0.8	1.2	V

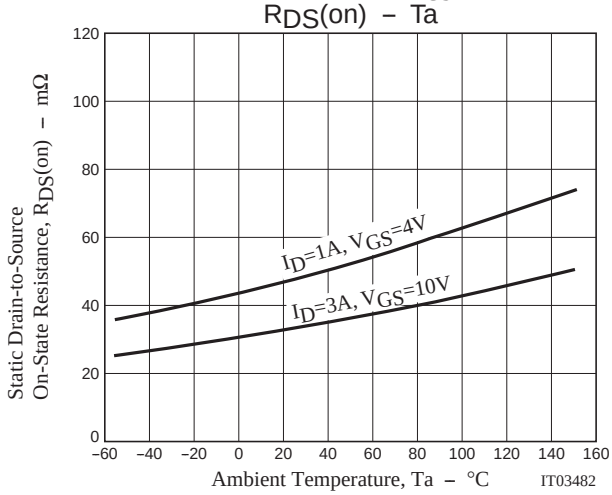
Switching Time Test Circuit

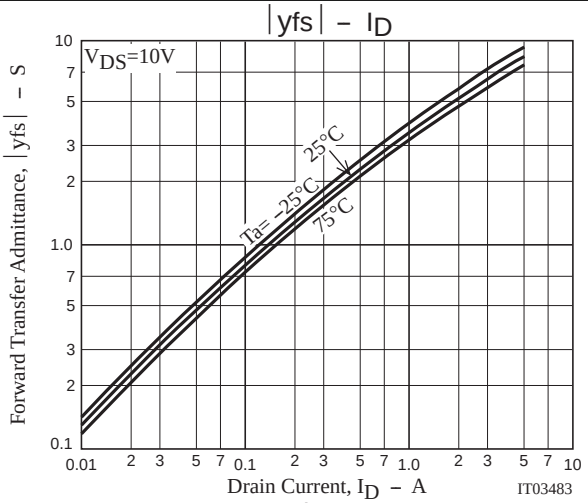


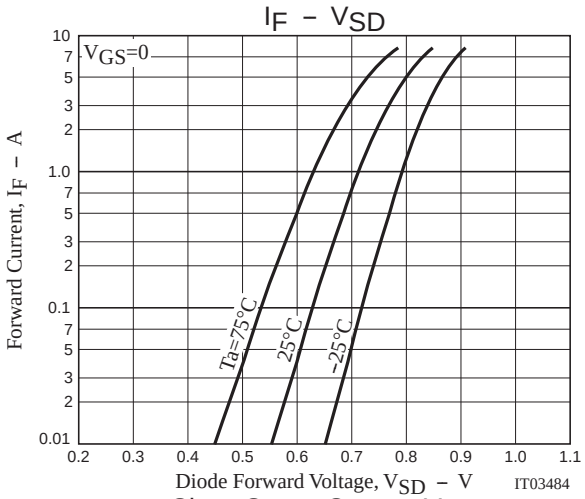


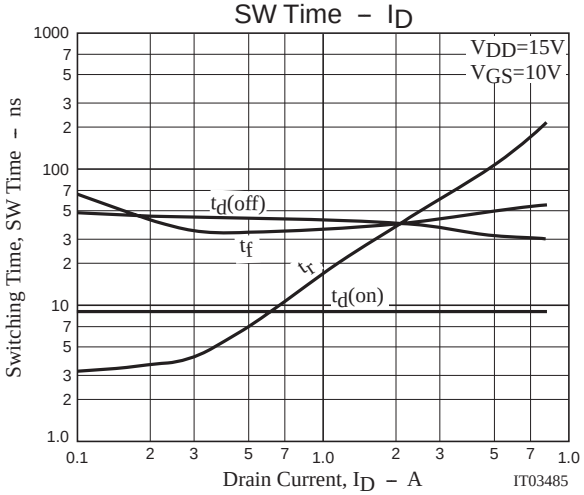


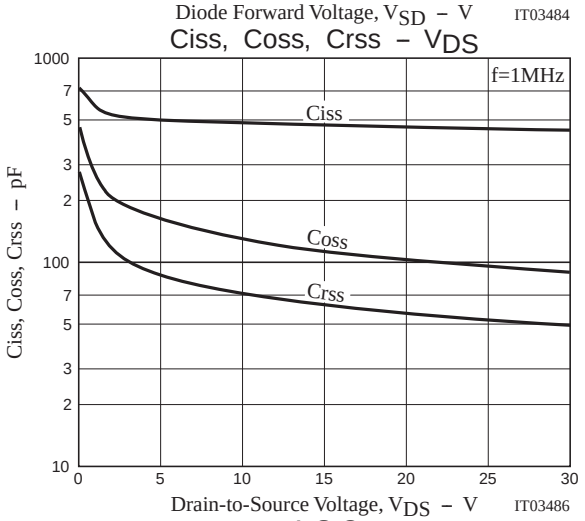


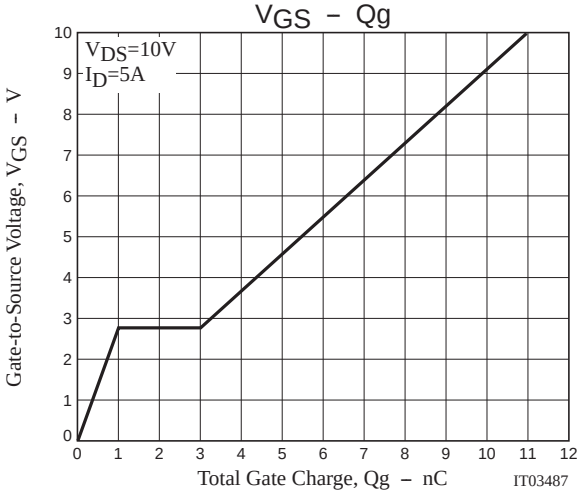


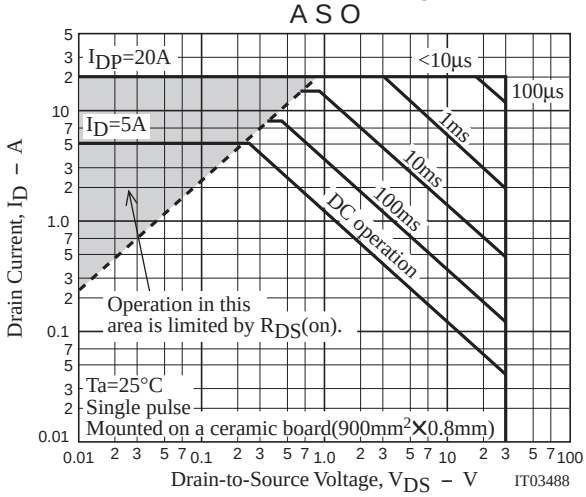

|yfs| – ID
VDS=10V
25°C
Ta= –25°C
75°C
Forward Transfer Admittance, |yfs| – S
Drain Current, ID – A
IT03483

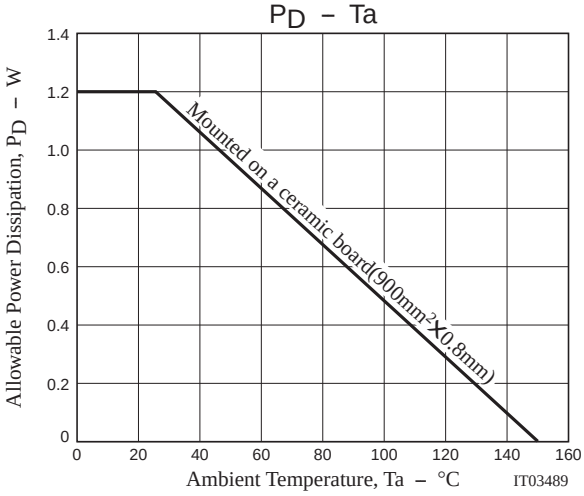

IF – VSD
VGS=0
Ta=75°C
25°C
–25°C
Forward Current, IF – A
Diode Forward Voltage, VSD – V
IT03484


SW Time – ID
VDD=15V
VGS=10V
td(off)
tf
tr
td(on)
Switching Time, SW Time – ns
Drain Current, ID – A
IT03485


Ciss, Coss, Crss – VDS
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V
IT03486


VGS – Qg
VDS=10V
ID=5A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT03487


A S O
IDP=20A
ID=5A
<10µs
100µs
1ms
10ms
100ms
DC operation
Operation in this
area is limited by RDS(on).
Ta=25°C
Single pulse
Mounted on a ceramic board(900mm2X0.8mm)
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT03488


PD – Ta
Mounted on a ceramic board(900mm2X0.8mm)
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C
IT03489