Ordering number : ENN6862

N-Channel Silicon MOSFET





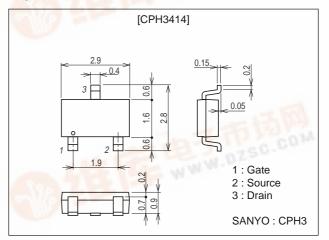
# **Ultrahigh-Speed Switching Applications**

#### **Features**

- Low ON-resistance.
- Ultrahigh-speed switching.
- 4V drive.

### **Package Dimensions**

unit : mm 2152A



# **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		2.2	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	8.8	A
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm <sup>2</sup> X0.8mm)	1.0	W
Channel Temperature	Tch	- A TO THE PARTY OF THE PARTY O	150	°C
Storage Temperature	Tstg	A 15 15	-55 to +150	°C

#### **Electrical Characteristics** at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>G</sub> S=0	30			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =30V, V <sub>GS</sub> =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±16V, VDS=0			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.2		2.6	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =1A	1.4	2.0	-	S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =1A, V <sub>GS</sub> =10V		115	150	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =0.5A, V <sub>GS</sub> =4V		190	270	mΩ

Marking : KP

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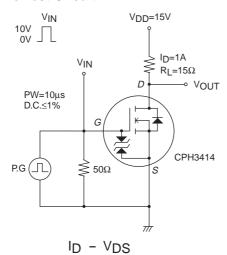
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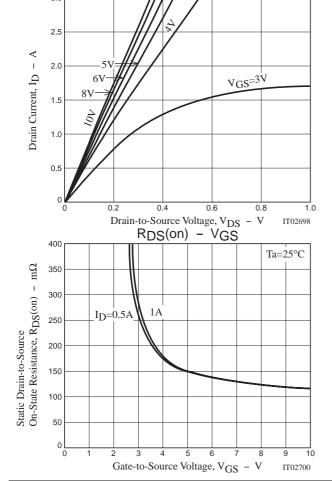
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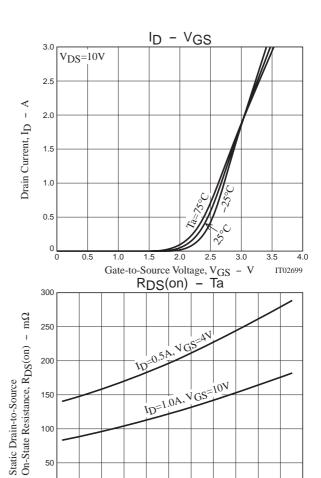
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Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Input Capacitance	Ciss	VDS=10V, f=1MHz		120		pF
Output Capacitance	Coss	V <sub>DS</sub> =10V, f=1MHz		30		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =10V, f=1MHz		15		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		6		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		4		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		17		ns
Fall Time	tf	See specified Test Circuit		5		ns
Total Gate Charge	Qg	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =2.2A		3.6		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =2.2A		0.6		nC
Gate-to-Drain "Miller" Charge	Qgd	VDS=10V, VGS=10V, ID=2.2A		0.5		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =2.2A, V <sub>G</sub> S=0		0.9	1.2	V

## **Switching Time Test Circuit**







100

50

-40

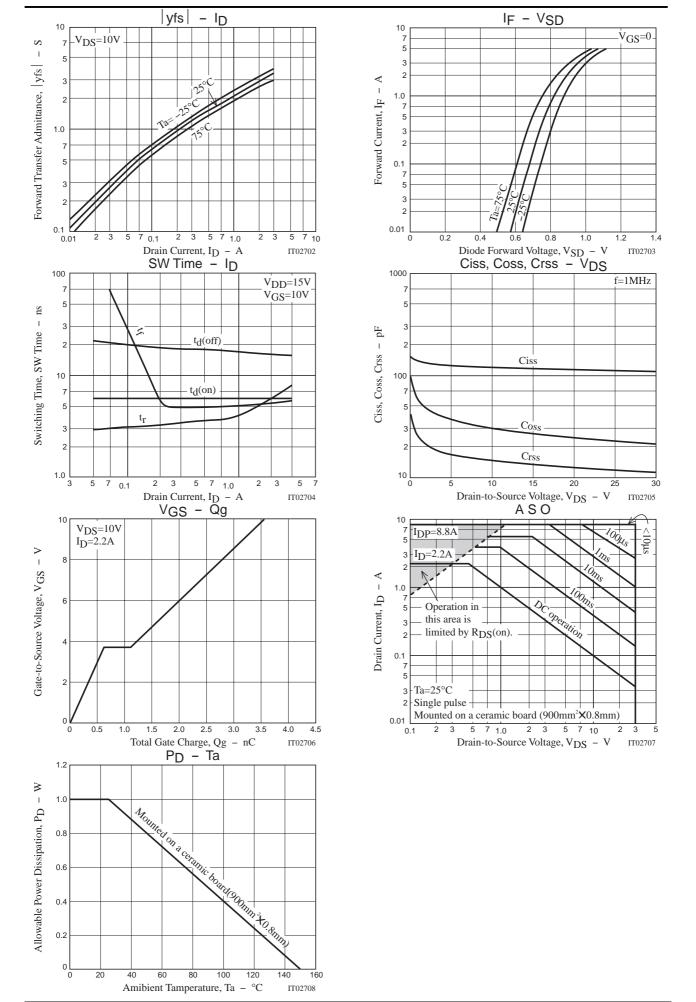
20 40 60

Ambient Temperature, Ta - °C

100 80

140 160

IT02701



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