Ordering number:ENN6418

N-Channel Silicon MOSFET

CPH6406

Ultrahigh-Speed Switching Applications

Features

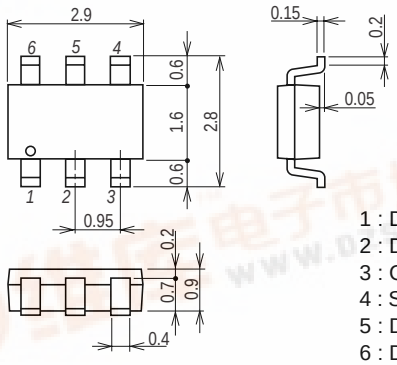
- Low ON resistance.
- Ultrahigh-speed switching.
- 4V drive.

Package Dimensions

unit:mm

2151A

[CPH6406]



1 : Drain
2 : Drain
3 : Gate
4 : Source
5 : Drain
6 : Drain
SANYO : CPH6

Specifications

Absolute Maximum Ratings at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		60	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		3	A
Drain Current (pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	12	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (900mm²×0.8mm)	1.6	W
Channel Temperature	Tch		150	˚C
Storage Temperature	Tstg		–55 to +150	˚C

Electrical Characteristics at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0	60			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=60V, V_{GS}=0			10	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±16V, V_{DS}=0			±10	μA
Cutoff Voltage	$V_{GS(off)}$	V_{DS}=10V, I_D=1mA	1.0		2.4	V
Forward Transfer Admittance	| yfs |	V_{DS}=10V, I_D=1.5A	2.6	3.6		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)}1$	I_D=1.5A, V_{GS}=10V		115	150	mΩ
	$R_{DS(on)}2$	I_D=1A, V_{GS}=4V		150	210	mΩ
Input Capacitance	Ciss	V_{DS}=20V, f=1MHz		220		pF
Output Capacitance	Coss	V_{DS}=20V, f=1MHz		75		pF
Reverse Transfer Capacitance	Crss	V_{DS}=20V, f=1MHz		25		pF

Marking : FB

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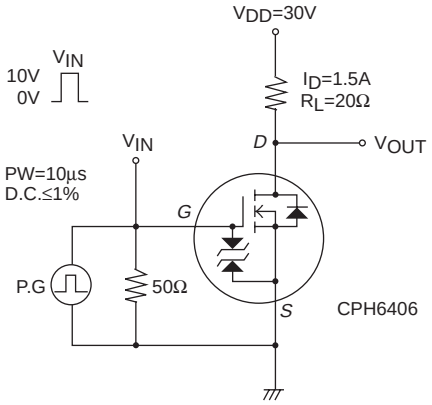
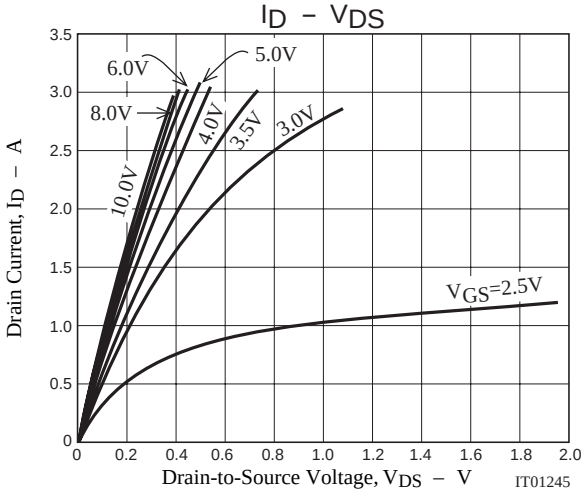
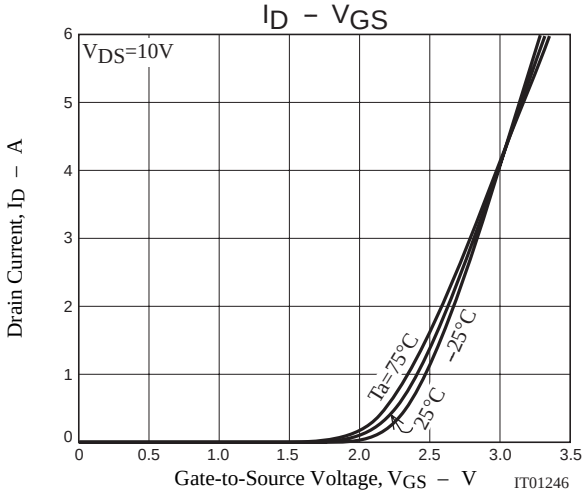
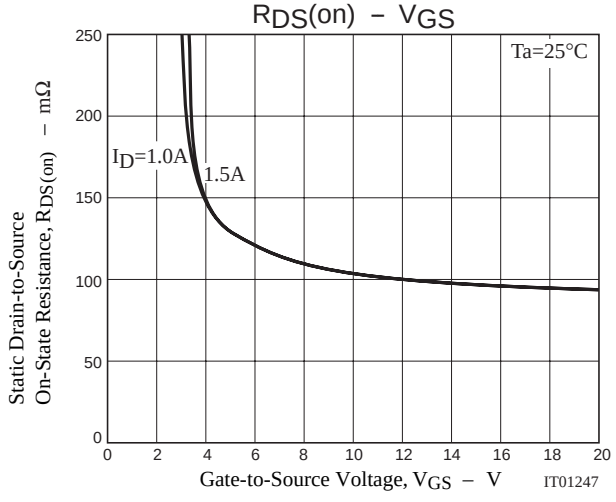
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		7		ns
Rise Time	t_r	See specified Test Circuit		8		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit		30		ns
Fall Time	t_f	See specified Test Circuit		29		ns
Total Gate Charge	Qg	V_{DS}=10V, V_{GS}=10V, I_D=3A		8.6		nC
Gate-to-Source Charge	Qgs	V_{DS}=10V, V_{GS}=10V, I_D=3A		1.3		nC
Gate-to-Drain "Miller" Charge	Qgd	V_{DS}=10V, V_{GS}=10V, I_D=3A		1.8		nC
Diode Forward Voltage	V_{SD}	I_S=3A, V_{GS}=0		0.83	1.2	V

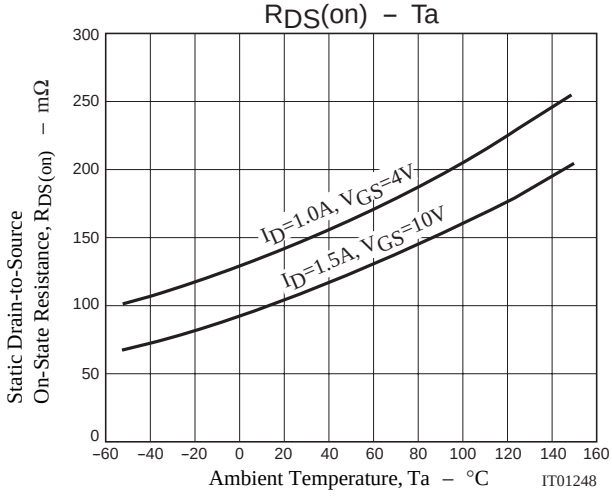
Switching Time Test Circuit

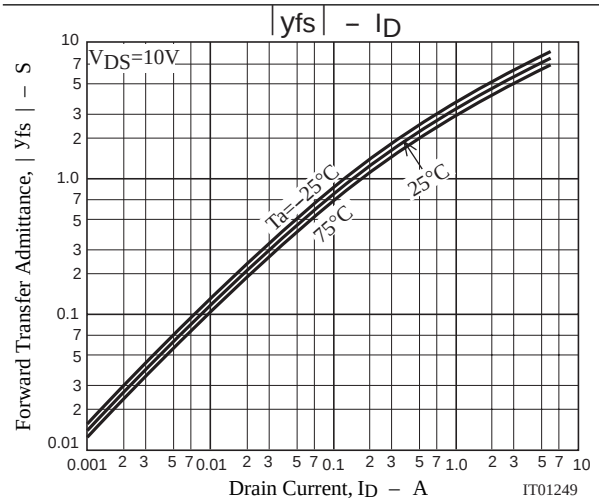


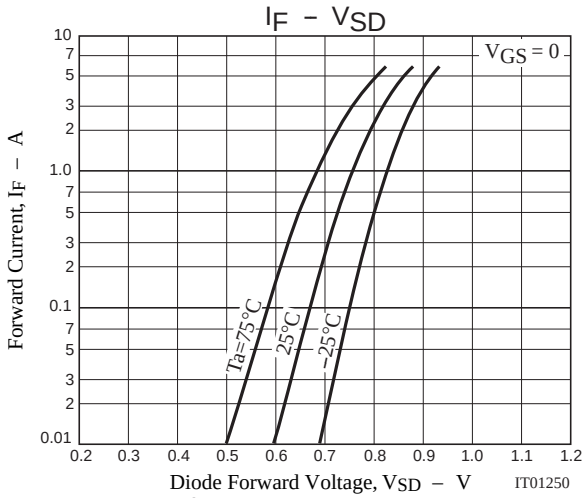


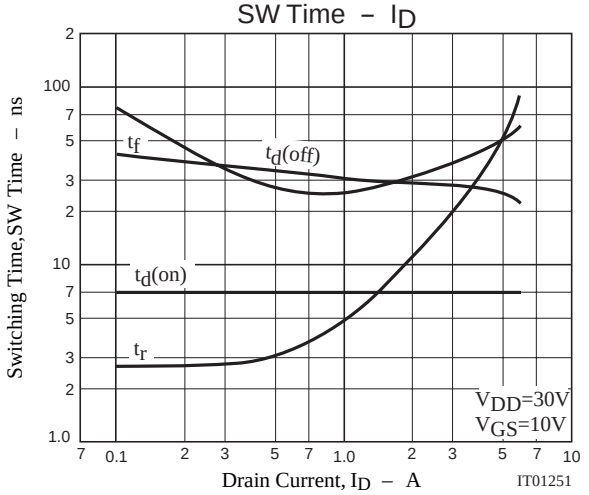


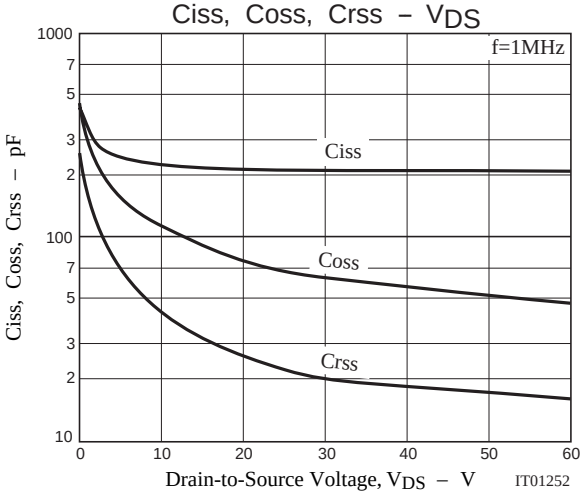


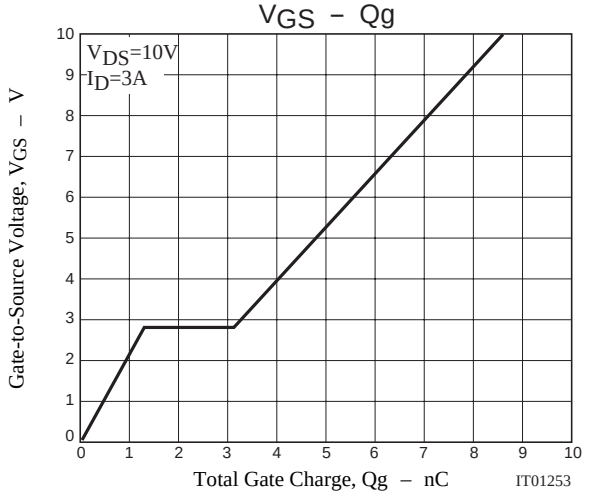


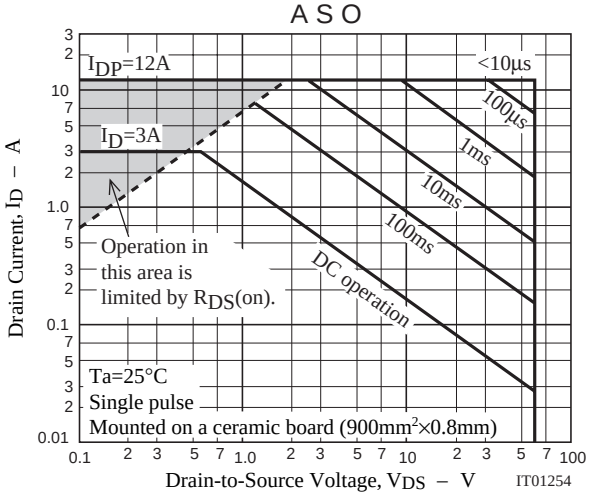

|yfs| – ID
VDS=10V
Ta=–25°C
25°C
75°C
Forward Transfer Admittance, |yfs| – S
Drain Current, ID – A
IT01249

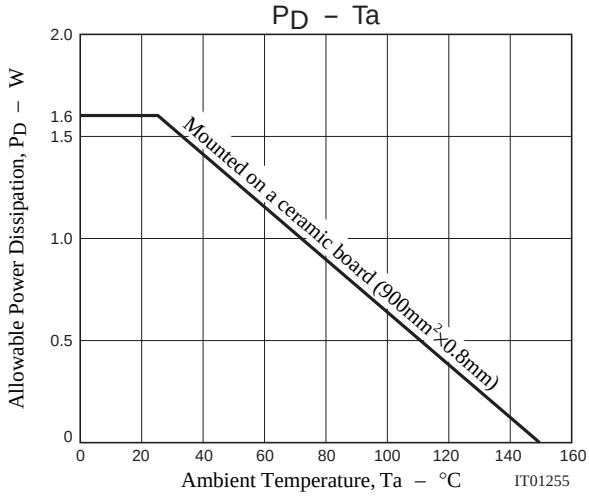

IF – VSD
VGS = 0
Ta=75°C
25°C
–25°C
Forward Current, IF – A
Diode Forward Voltage, VSD – V
IT01250


SW Time – ID
tf
td(off)
td(on)
tr
VDD=30V
VGS=10V
Switching Time,SW Time – ns
Drain Current, ID – A
IT01251


Ciss, Coss, Crss – VDS
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V
IT01252


VGS – Qg
VDS=10V
ID=3A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT01253


A S O
IDP=12A
ID=3A
<10µs
100µs
1ms
10ms
100ms
DC operation
Operation in
this area is
limited by RDS(on).
Ta=25°C
Single pulse
Mounted on a ceramic board (900mm²×0.8mm)
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT01254


PD – Ta
Mounted on a ceramic board (900mm²×0.8mm)
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C
IT01255