

Ordering number:ENN6007A

PNP Epitaxial Planar Silicon Transistor
Schottky Barrier Diode



CPH6701

DC/DC Converter Applications

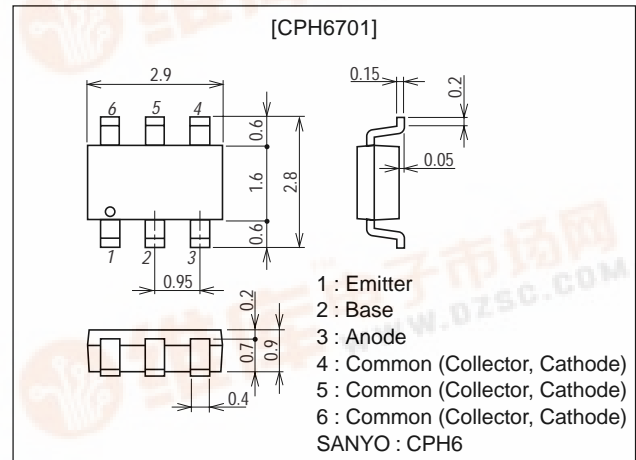
Features

- Composite type with a PNP transistor and a Schottky barrier diode contained in one package facilitating high-density mounting.
- The CPH6701 is formed with two chips, one being equivalent to the CPH3106 and the other the SBS001, encapsulated in one package.
- Ultrasmall package facilitates miniaturization in end products (mounting height : 0.9mm).

Package Dimensions

unit:mm

2153



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
[TR]				
Collector-to-Base Voltage	V _{CB0}		-15	V
Collector-to-Emitter Voltage	V _{CE0}		-12	V
Emitter-to-Base Voltage	V _{EBO}		-5	V
Collector Current	I _C		-3	A
Collector Current (Pulse)	I _{CP}		-5	A
Base Current	I _B		-600	mA
Collector Dissipation	P _C	Mounted on a ceramic board (600mm ² ×0.8mm)	1.3	W
Junction Temperature	T _J		150	°C
Storage Temperature	T _{stg}		-55 to +125	°C
[SBD]				
Repetitive Peak Reverse Voltage	V _{RRM}		11	V
Non-repetitive Peak Reverse Surge Voltage	V _{RSM}		15	V
Average Output Current	I _O		500	mA
Surge Current	I _{FSM}	50Hz sine wave, 1 cycle	5	A
Junction Temperature	T _J		-55 to +125	°C
Storage Temperature	T _{stg}		-55 to +125	°C

Marking : PA

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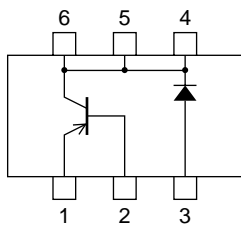


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Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[TR]						
Collector Cutoff Current	I_{CBO}	$V_{CB}=-12V, I_E=0$			-0.1	μA
Emitter Cutoff Current	I_{EBO}	$V_{EB}=-4V, I_C=0$			-0.1	μA
DC Current Gain	h_{FE1}	$V_{CE}=-2V, I_C=-0.5A$	200		560	
	h_{FE2}	$V_{CE}=-2V, I_C=-3A$	70			
Gain-Bandwidth Product	f_T	$V_{CE}=-2V, I_C=-0.5A$		280		MHz
Output Capacitance	C_{ob}	$V_{CB}=-10V, f=1MHz$		36		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=-1.5A, I_B=-30mA$		-110	-165	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=-1.5A, I_B=-30mA$		-0.85	-1.2	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=-10\mu A, I_E=0$	-15			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=-1mA, R_{BE}=\infty$	-12			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_C=-10\mu A, I_C=0$	-5			V
Turn-ON Time	t_{on}	See specified Test Circuit.		30		ns
Storage Time	t_{stg}	See specified Test Circuit.		90		ns
Turn-OFF Time	t_f	See specified Test Circuit.		10		ns
[Di]						
Reverse Voltage	V_R	$I_R=400\mu A$	11			V
Forward Voltage	V_F	$I_F=500mA$		0.4	0.45	V
Reverse Current	I_R	$V_R=6V$			200	μA
Interterminal Capacitance	C	$V_R=10V, f=1MHz$		50		pF
Reverse Recovery Time	t_{rr}	$I_F=I_R=100mA$, See specified Test Circuit.			10	ns
Thermal Resistance	R_{thj-a}	Mounted on a ceramic board (600mm ² ×0.8mm)		96		°C/W

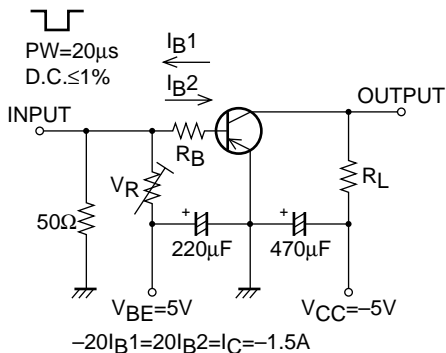
Electrical Connection



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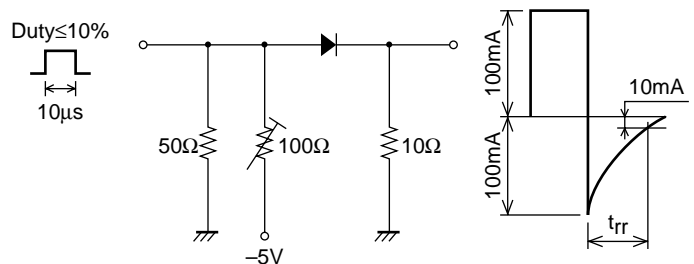
Switching Time Test Circuit

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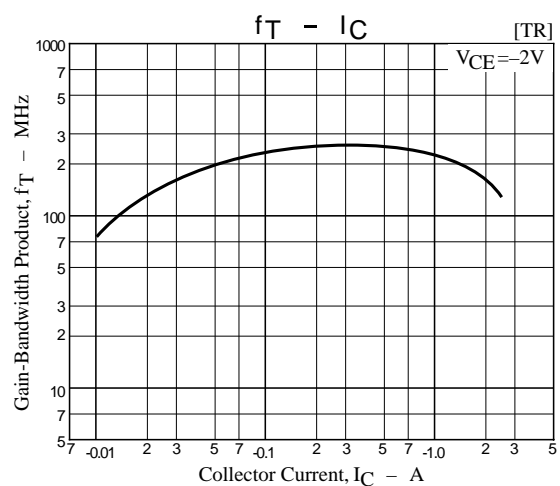
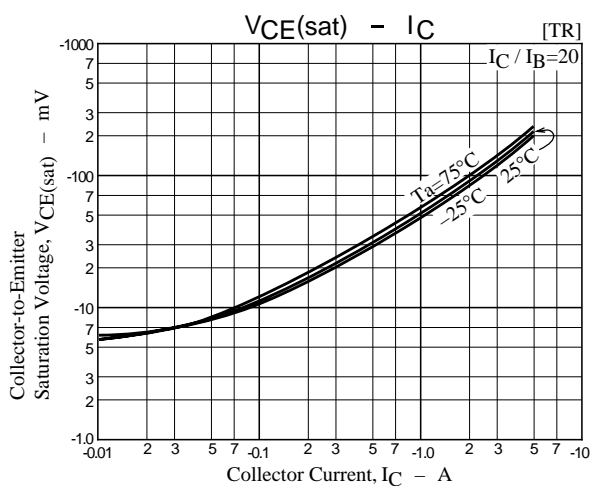
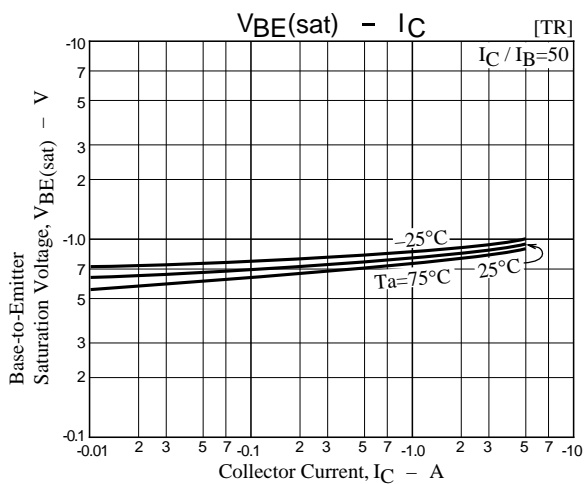
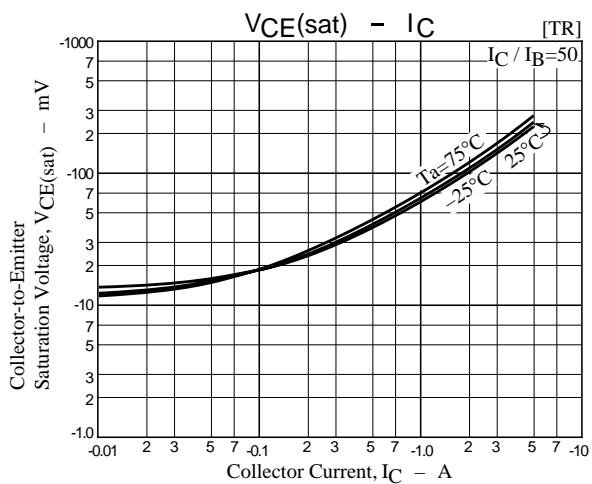
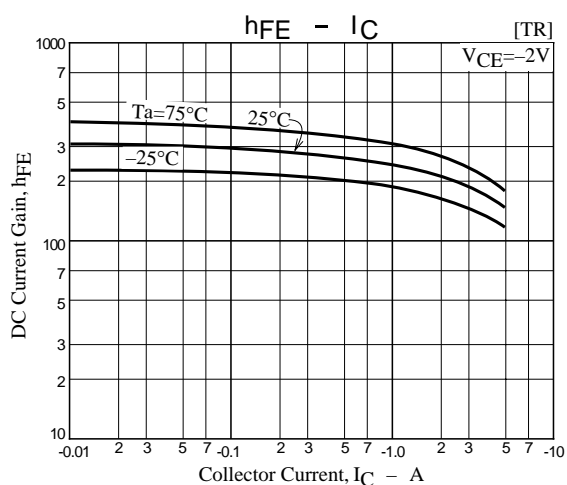
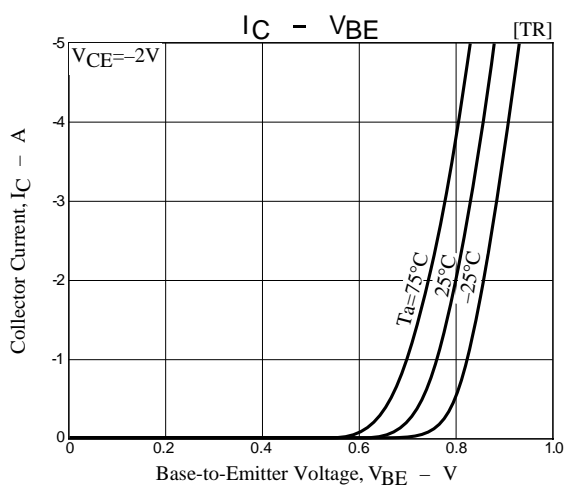
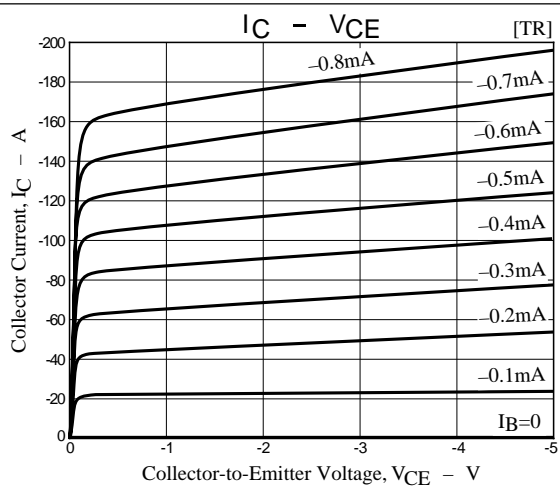
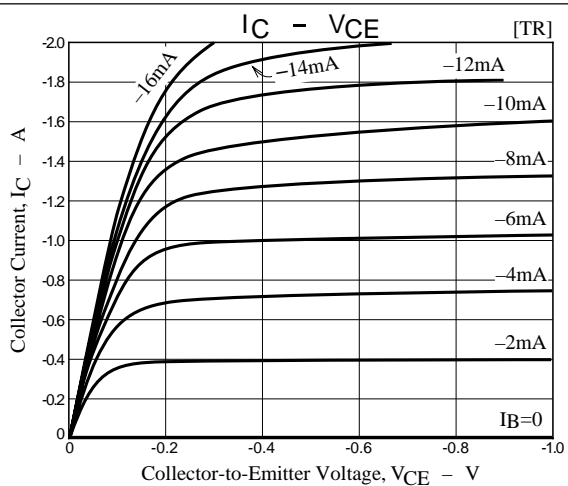
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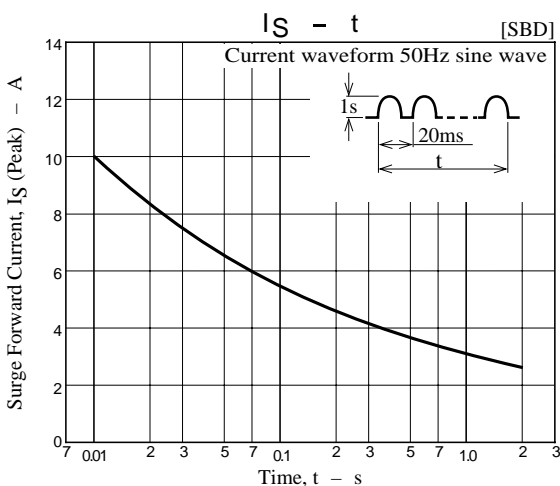
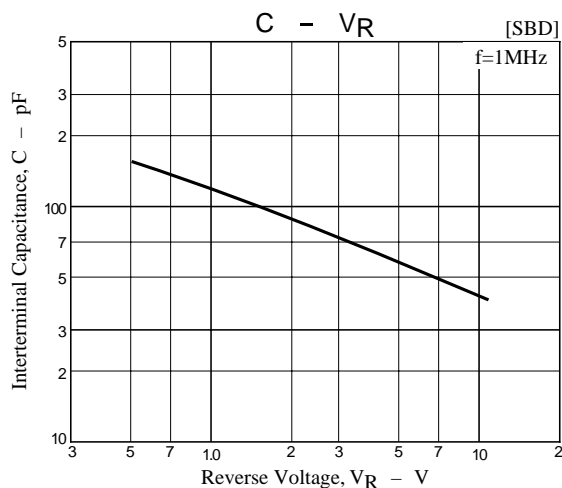
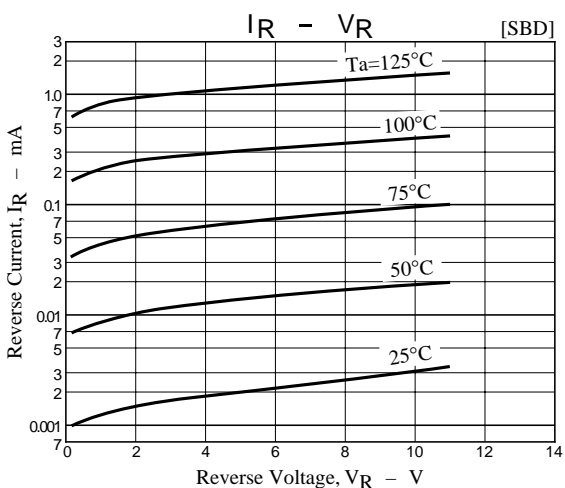
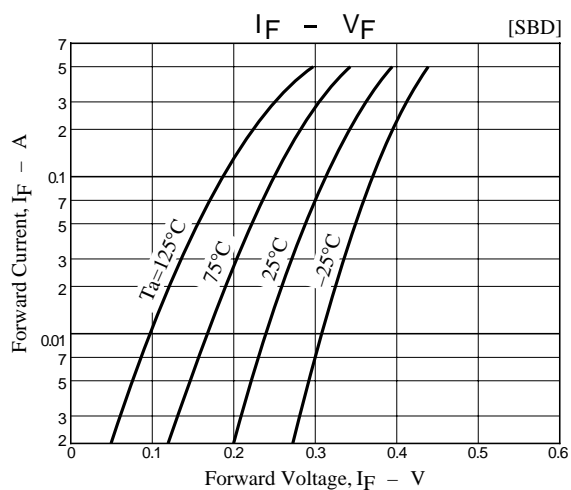
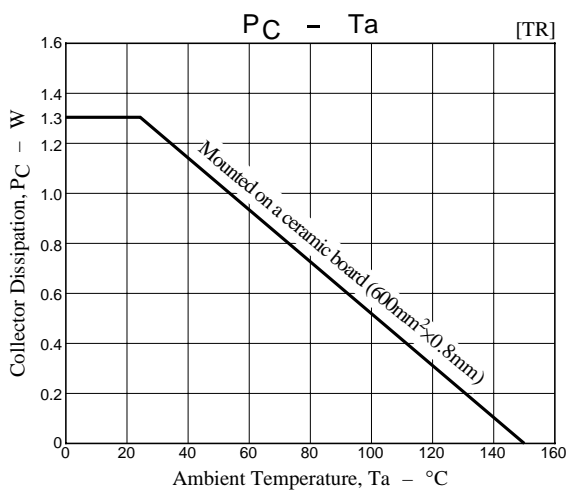
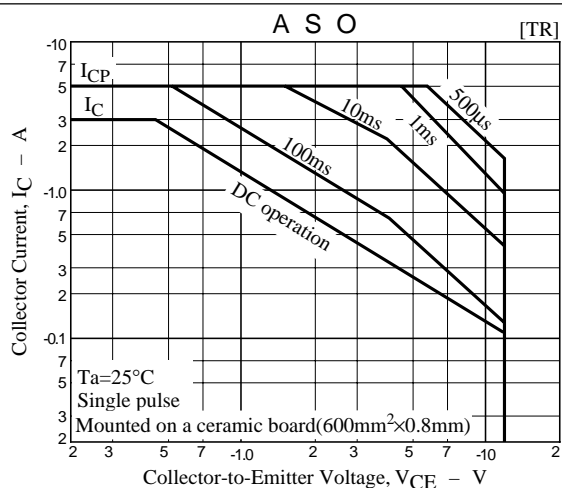
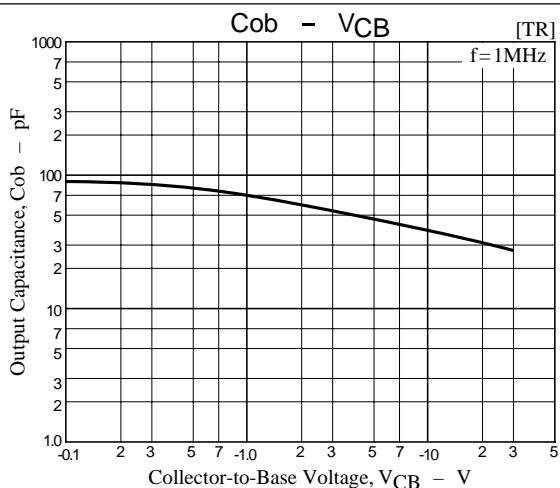


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